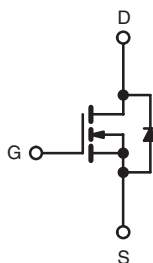
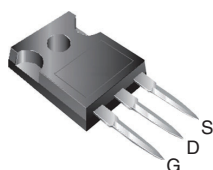


Power MOSFET

PRODUCT SUMMARY

V_{DS} (V)	600	
$R_{DS(on)}$ (Ω)	$V_{GS} = 10\text{ V}$	0.385
Q_g (Max.) (nC)	100	
Q_{gs} (nC)	30	
Q_{gd} (nC)	46	
Configuration	Single	

TO-247



N-Channel MOSFET

FEATURES

- Superfast Body Diode Eliminates the Need for External Diodes in ZVS Applications
- Lower Gate Charge Results in Simple Drive Requirements
- Enhanced dV/dt Capabilities Offer Improved Ruggedness
- Higher Gate Voltage Threshold Offers Improved Noise Immunity
- Lead (Pb)-free Available



Available
RoHS*
COMPLIANT

APPLICATIONS

- Zero Voltage Switching SMPS
- Telecom and Server Power Supplies
- Uninterruptible Power Supplies
- Motor Control Applications

ORDERING INFORMATION

Package	TO-247
Lead (Pb)-free	IRFP15N60LPbF
	SiHFP15N60L-E3
SnPb	IRFP15N60L
	SiHFP15N60L

ABSOLUTE MAXIMUM RATINGS $T_C = 25\text{ }^\circ\text{C}$, unless otherwise noted

PARAMETER	SYMBOL	LIMIT	UNIT
Drain-Source Voltage	V_{DS}	600	V
Gate-Source Voltage	V_{GS}	± 30	
Continuous Drain Current	I_D	$T_C = 25\text{ }^\circ\text{C}$	A
		$T_C = 100\text{ }^\circ\text{C}$	
Pulsed Drain Current ^a	I_{DM}	60	
Linear Derating Factor		2.3	
Single Pulse Avalanche Energy ^b	E_{AS}	320	mJ
Repetitive Avalanche Current ^a	I_{AR}	15	A
Repetitive Avalanche Energy ^a	E_{AR}	28	mJ
Maximum Power Dissipation	P_D	280	W
Peak Diode Recovery dV/dt^c	dV/dt	10	V/ns
Operating Junction and Storage Temperature Range	T_J, T_{stg}	- 55 to + 150	$^\circ\text{C}$
Soldering Recommendations (Peak Temperature)	for 10 s	300 ^d	
Mounting Torque	6-32 or M3 screw	10 1.1	

Notes

- Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11).
- Starting $T_J = 25\text{ }^\circ\text{C}$, $L = 2.9\text{ mH}$, $R_G = 25\text{ }\Omega$, $I_{AS} = 15\text{ A}$, $dV/dt = 10\text{ V/ns}$ (see fig. 12a).
- $I_{SD} \leq 15\text{ A}$, $dI/dt \leq 340\text{ A}/\mu\text{s}$, $V_{DD} \leq V_{DS}$, $T_J \leq 150\text{ }^\circ\text{C}$.
- 1.6 mm from case.

* Pb containing terminations are not RoHS compliant, exemptions may apply

THERMAL RESISTANCE RATINGS

PARAMETER	SYMBOL	TYP.	MAX.	UNIT
Maximum Junction-to-Ambient	R_{thJA}	-	40	°C/W
Case-to-Sink, Flat, Greased Surface	R_{thCS}	0.24	-	
Maximum Junction-to-Case (Drain)	R_{thJC}	-	0.44	

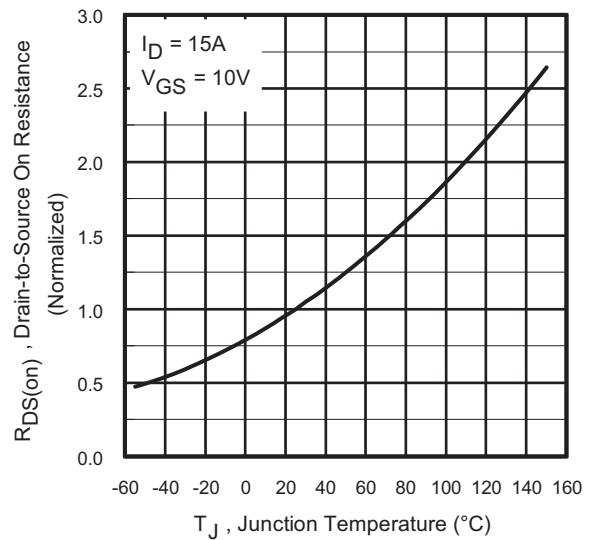
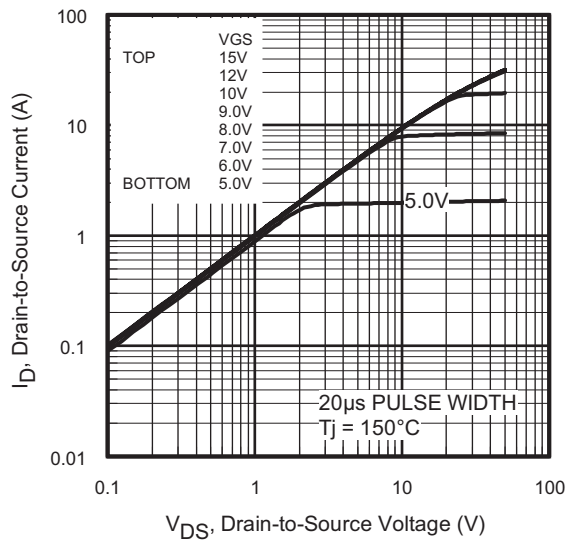
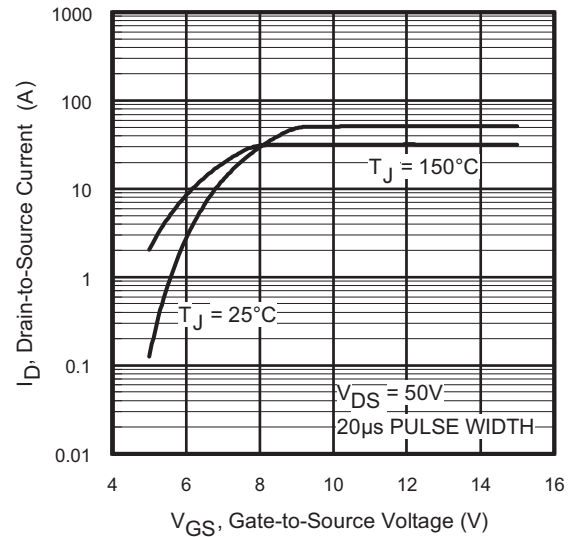
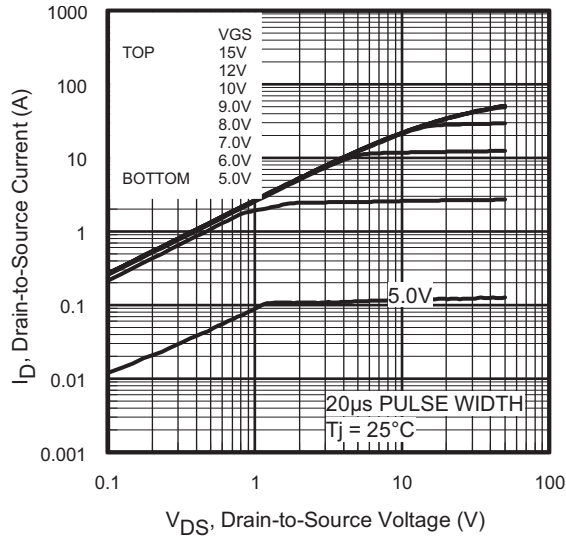
SPECIFICATIONS $T_J = 25\text{ }^{\circ}\text{C}$, unless otherwise noted

PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT
Static							
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0 V, I _D = 250 μA		600	-	-	V
V _{DS} Temperature Coefficient	ΔV _{DS} /T _J	Reference to 25 °C, I _D = 1 mA		-	0.39	-	V/°C
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA		3.0	-	5.0	V
Gate-Source Leakage	I _{GSS}	V _{GS} = ± 30 V		-	-	± 100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 600 V, V _{GS} = 0 V		-	-	50	μA
		V _{DS} = 480 V, V _{GS} = 0 V, T _J = 125 °C		-	-	2.0	mA
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} = 10 V	I _D = 9.0 A ^b	-	0.385	0.460	Ω
Forward Transconductance	g _{fs}	V _{DS} = 50 V, I _D = 9.0 A		8.3	-	-	S
Dynamic							
Input Capacitance	C _{iss}	V _{GS} = 0 V, V _{DS} = 25 V, f = 1.0 MHz, see fig. 5		-	2720	-	pF
Output Capacitance	C _{oss}			-	260	-	
Reverse Transfer Capacitance	C _{rss}			-	20	-	
Effective Output Capacitance	C _{oss} eff.	V _{GS} = 0 V, V _{DS} = 0 V to 480 V ^c		-	120	-	
Effective Output Capacitance (Energy Related)	C _{oss} eff. (ER)			-	100	-	
Total Gate Charge	Q _g	V _{GS} = 10 V	I _D = 15 A, V _{DS} = 480 V, see fig. 7 and 15 ^b	-	-	100	nC
Gate-Source Charge	Q _{gs}			-	-	30	
Gate-Drain Charge	Q _{gd}			-	-	46	
Turn-On Delay Time	t _{d(on)}	V _{DD} = 300 V, I _D = 15 A, R _G = 1.8 Ω, V _{GS} = 10 V, see fig. 11a and 11b ^b		-	20	-	ns
Rise Time	t _r			-	44	-	
Turn-Off Delay Time	t _{d(off)}			-	28	-	
Fall Time	t _f			-	5.5	-	
Drain-Source Body Diode Characteristics							
Continuous Source-Drain Diode Current	I _S	MOSFET symbol showing the integral reverse p - n junction diode		-	-	15	A
Pulsed Diode Forward Current ^a	I _{SM}			-	-	60	
Body Diode Voltage	V _{SD}	T _J = 25 °C, I _S = 15 A, V _{GS} = 0 V ^b		-	-	1.5	V
Body Diode Reverse Recovery Time	t _{rr}	T _J = 25 °C, I _F = 15 A		-	130	200	ns
		T _J = 125 °C, dI/dt = 100 A/μs ^b		-	240	360	
Body Diode Reverse Recovery Charge	Q _{rr}	T _J = 25 °C, I _F = 15 A, V _{GS} = 0 V ^b		-	450	670	nC
		T _J = 125 °C, dI/dt = 100 A/μs ^b		-	1080	1620	
Reverse Recovery Time	I _{RRM}	T _J = 25 °C		-	5.8	8.7	A
Forward Turn-On Time	t _{on}	Intrinsic turn-on time is negligible (turn-on is dominated by L _S and L _D)					

Notes

- a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11).
b. Pulse width $\leq 300\text{ }\mu\text{s}$; duty cycle $\leq 2\%$.
c. $C_{oss\text{ eff.}}$ is a fixed capacitance that gives the same charging time as C_{oss} while V_{DS} is rising from 0 to 80 % V_{DS} .
 $C_{oss\text{ eff. (ER)}}$ is a fixed capacitance that stores the same energy as C_{oss} while V_{DS} is rising from 0 to 80 % V_{DS} .

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



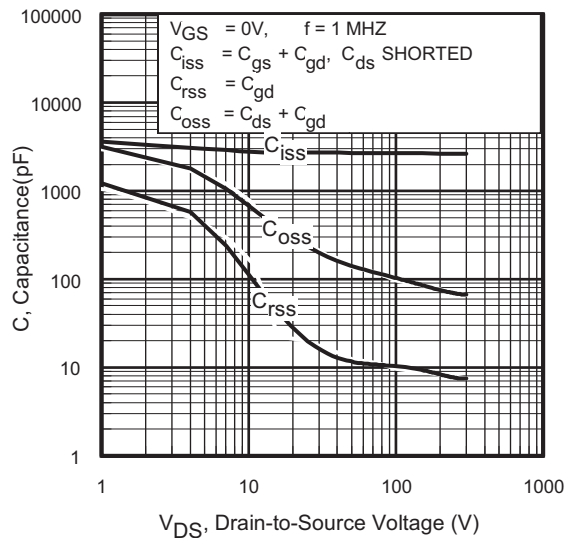


Fig. 5 - Typical Capacitance vs. Drain-to-Source Voltage

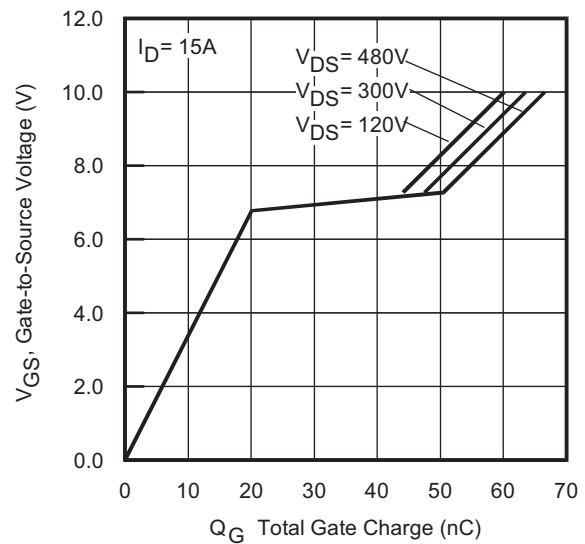


Fig. 7 - Typical Gate Charge vs. Gate-to-Source Voltage

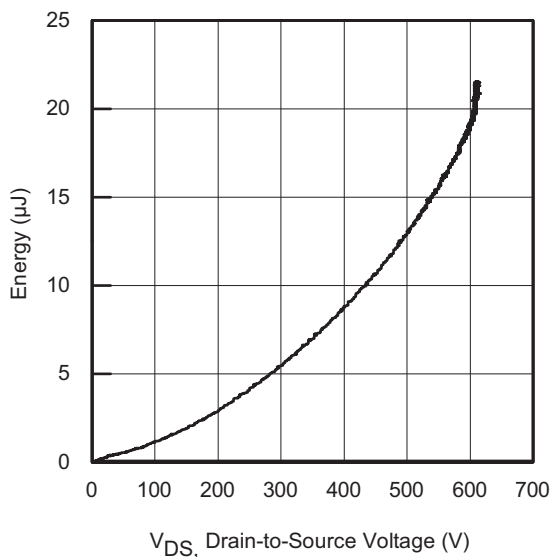


Fig. 6 - Typical Output Capacitance Stored Energy vs. V_{DS}

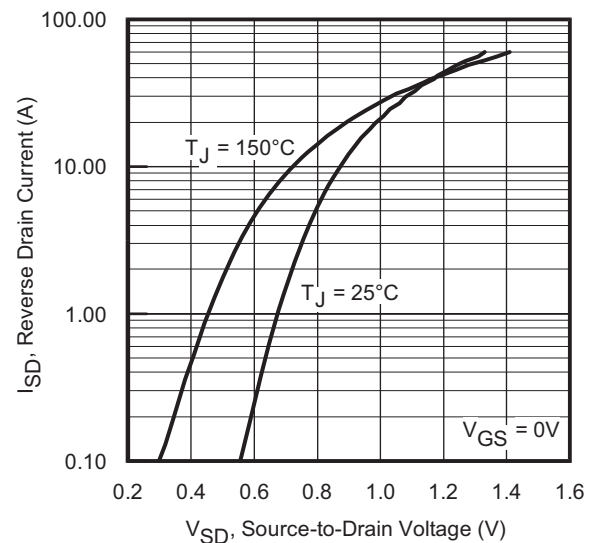


Fig. 8 - Typical Source-Drain Diode Forward Voltage

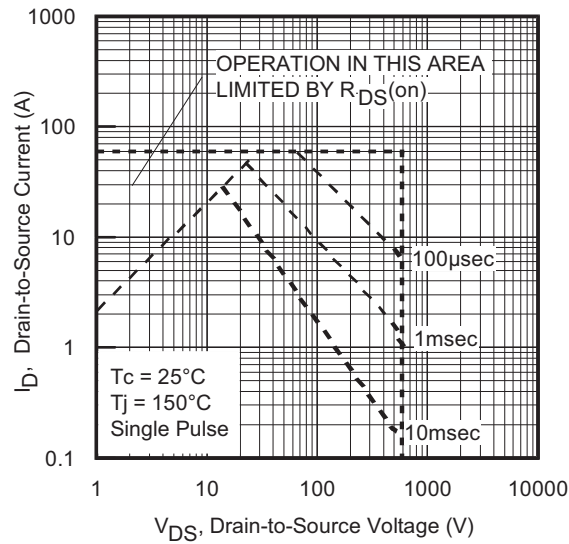


Fig. 9 - Maximum Safe Operating Area

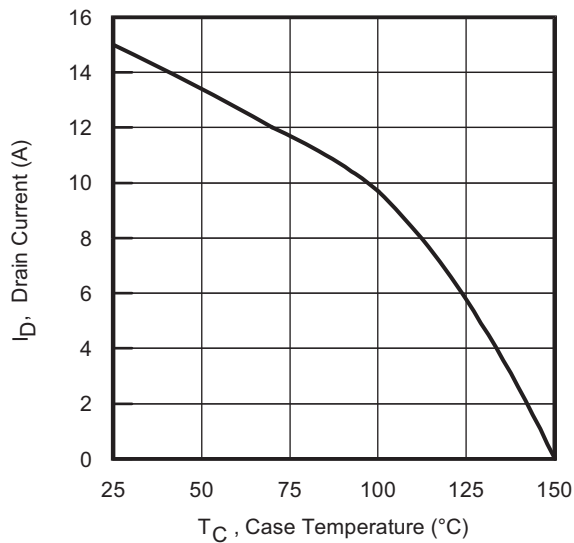


Fig. 10 - Maximum Drain Current vs. Case Temperature

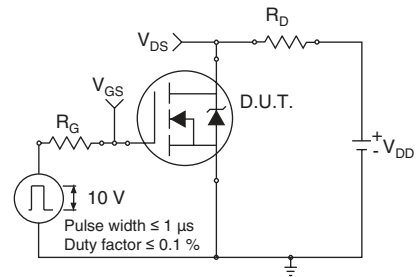


Fig. 11a - Switching Time Test Circuit

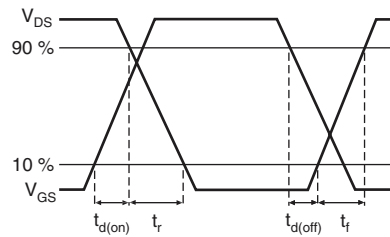


Fig. 11b - Switching Time Waveforms

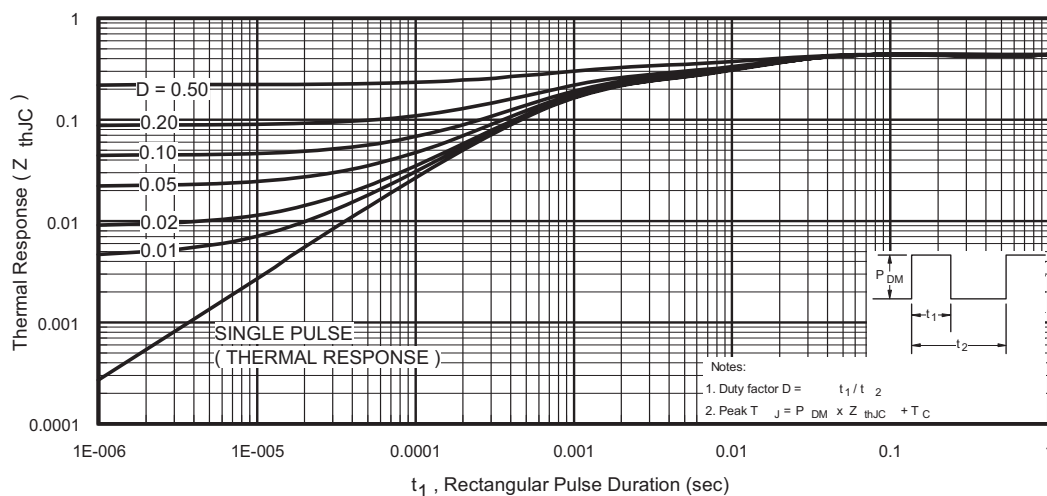


Fig. 12 - Maximum Effective Transient Thermal Impedance, Junction-to-Case

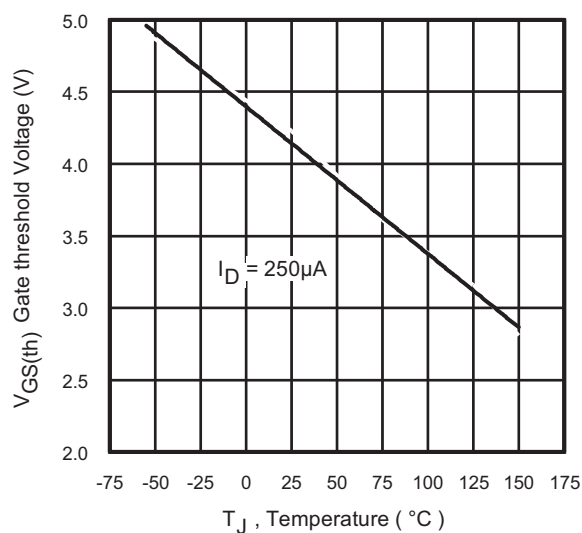


Fig. 13 - Threshold Voltage vs. Temperature

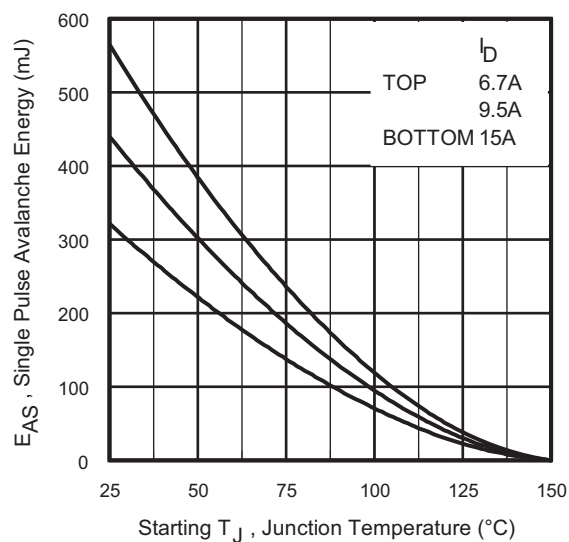


Fig. 14a - Maximum Avalanche Energy vs. Drain Current

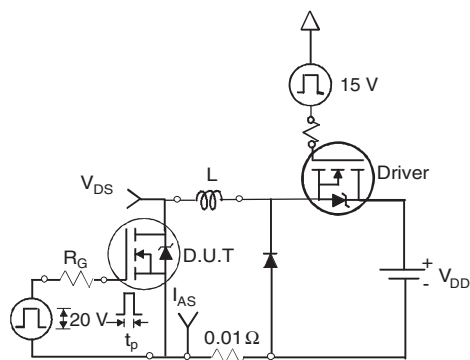


Fig. 14b - Unclamped Inductive Test Circuit

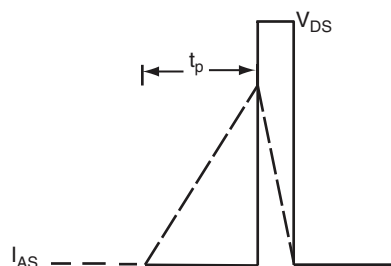


Fig. 14c - Unclamped Inductive Waveforms

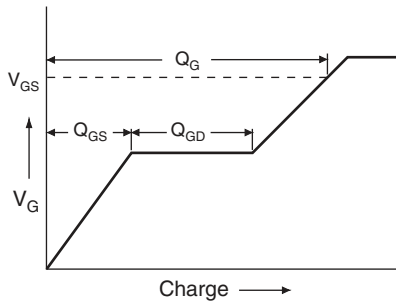


Fig. 15a - Basic Gate Charge Waveform

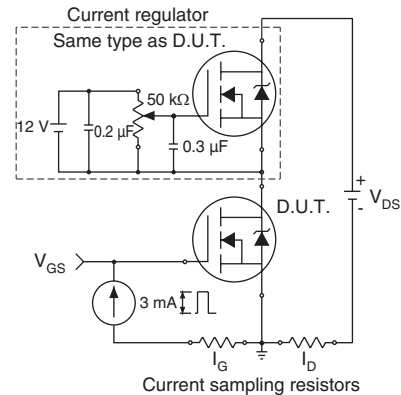


Fig. 15b - Gate Charge Test Circuit

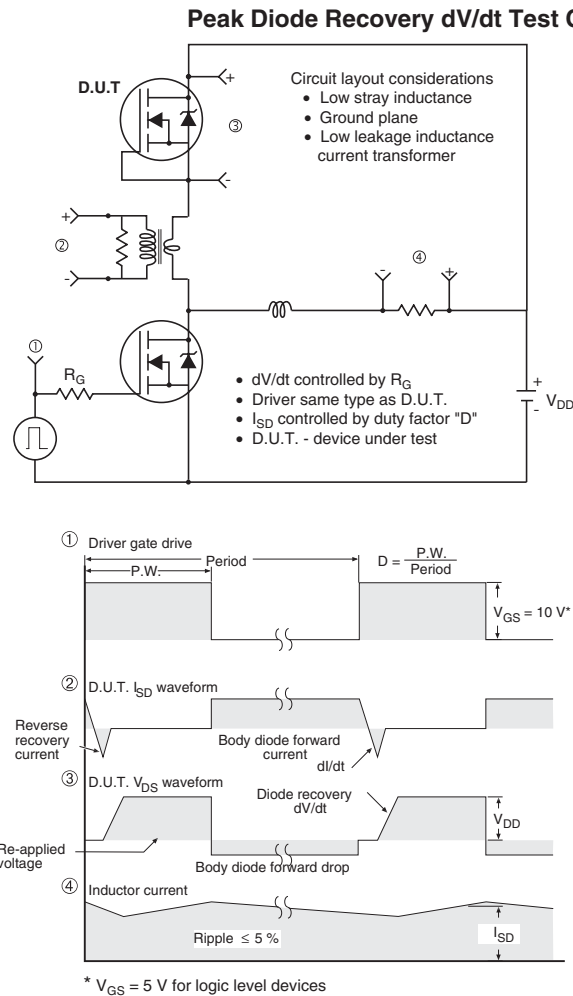


Fig. 16 - For N-Channel

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