

# 10/100Base-TX Interface Module for PC Card Applications EPF8015G



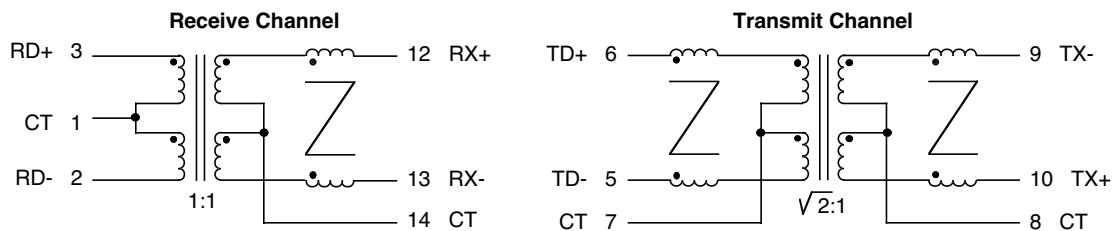
- Optimized for QSI6611/6612 PHY/MAC
- Guaranteed to operate with 8 mA DC Bias at 70°C on Cable Side
- Complies with or exceeds IEEE 802.3, 10/100Base-TX Standards

## Electrical Parameters @ 25° C

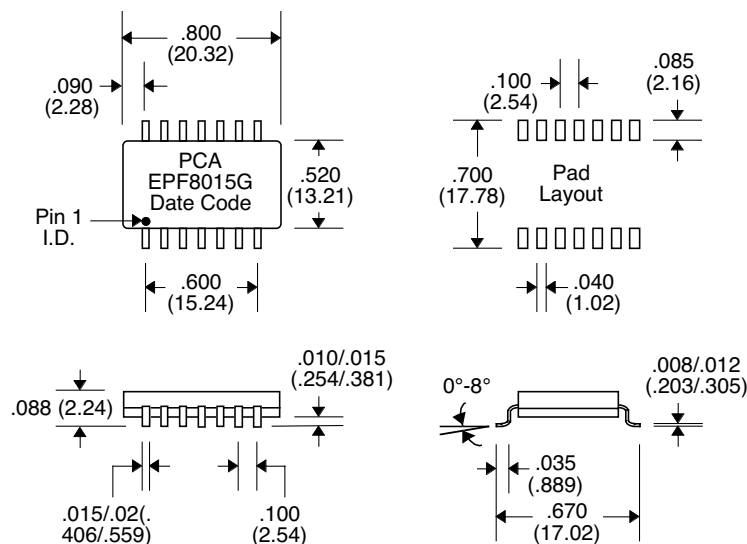
| OCL<br>(@ 70°C)                   | Insertion Loss<br>(dB Max.) |            |            | Return Loss<br>(dB Min.) |           |            | Common Mode Rejection<br>(dB Min.) |            |            | Crosstalk<br>(dB Min.)<br>Between Channels |
|-----------------------------------|-----------------------------|------------|------------|--------------------------|-----------|------------|------------------------------------|------------|------------|--------------------------------------------|
| 100 KHz, 0.1 Vrms<br>8 mA DC Bias | 1-80<br>MHz                 | 100<br>MHz | 150<br>MHz | 1-30<br>MHz              | 60<br>MHz | 100<br>MHz | 30-100<br>MHz                      | 200<br>MHz | 500<br>MHz | 1-100<br>MHz                               |
| <b>Cable Side</b>                 | Xmit/Rcv                    | Xmit/Rcv   | Xmit/Rcv   | Xmit/Rcv                 | Xmit/Rcv  | Xmit/Rcv   | Xmit/Rcv                           | Xmit/Rcv   | Xmit/Rcv   |                                            |
| 350 $\mu$ H                       | -1.5/-1                     | -2/-2      | -4/-3      | -18/-18                  | -12/-12   | -8/-10     | -38/-38                            | -30/-25    | -20/-15    | -40                                        |

• Isolation : 1500 Vrms • Impedance : 100  $\Omega$  • Rise Time : 3.0 nS Max. •

## Schematic



## Package



Unless Otherwise Specified Dimensions are in Inches /mm  $\pm .010 / .25$