

# INTELLIGENT INTERACTIVE SYNTHESIZER

## SURFACE MOUNT MODEL: LFSW80210-100

**WIDE BANDWIDTH**

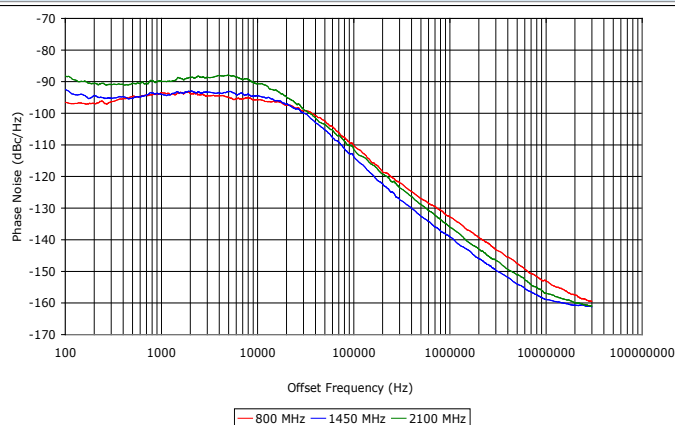
**800 - 2100 MHz**

### FEATURES:

- ▶ Exceptionally Low Phase Noise
- ▶ Interactive Communication
- ▶ Standard Programming Interface
- ▶ Ultra Wide Tuning Range
- ▶ Lead Free - RoHS Compliant
- ▶ Patent Pending REL-PRO® Technology
- ▶ Small Size, Surface Mount
- ▶ Buffered Output



### Phase Noise



### SPECIFICATIONS (Rev. A 11/20/07)

|                             |                              |             |             |
|-----------------------------|------------------------------|-------------|-------------|
| Frequency                   | 800 - 2100 MHz               |             |             |
| Step Size                   | 1 MHz                        |             |             |
| Reference Input Frequency   | 10 MHz *                     |             |             |
| Reference Input Voltage     | 1 V p-p to 3.3 V p-p         |             |             |
| Bias Voltage                | Vcc                          | V (Vdc)     | I (mA Max.) |
|                             | Digital                      | +5          | 35          |
|                             | VCO                          | +5          | 35          |
|                             | Converter                    | +5          | 65          |
| Output Power                | +3 dBm (Min.)                |             |             |
| Spurious Suppression        | 75 dB (Typ.)                 |             |             |
| Harmonic Suppression        | 20 dB (Typ.)                 |             |             |
| Settling Time               | 10 mSec (Typ.) **            |             |             |
| Output Impedance            | 50 Ohms (Nom.)               |             |             |
| Typical Phase Noise         | Offset                       | Phase Noise |             |
|                             | @ 1 kHz                      | -95 dbc/Hz  |             |
|                             | @ 10 kHz                     | -95 dbc/Hz  |             |
|                             | @ 100 kHz                    | -112 dbc/Hz |             |
| Operating Temperature Range | -40 to +85 °C                |             |             |
| Programming                 | See Application Note: AN7100 |             |             |

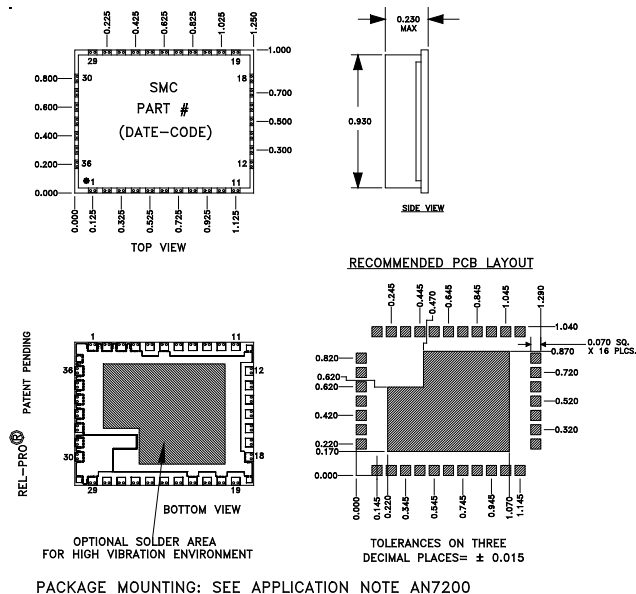
\* Reference input frequency is programmable in multiples of the step size, 10 MHz is the default setting. 150 MHz (Max.)

\*\* From the rising edge of LE to stable lock detect

### Absolute Maximum Ratings

|                              |                |
|------------------------------|----------------|
| Storage Temp. Range          | -55 to +125 °C |
| Bias Voltage (Digital)       | +5.25 V        |
| Bias Voltage (VCO)           | +5.5 V         |
| Bias Voltage (Converter)     | +8 V           |
| DC Voltage Applied to RF Out | ±25 V          |

### Package # 197LF



### Pin-Out

|                           |                               |                          |
|---------------------------|-------------------------------|--------------------------|
| Pin 5 - RF out            | Pin 19 - Latch Enable In      | Pin 28 - Vcc (Converter) |
| Pin 7 - Lock Detect Out * | Pin 21 - Ref. In              | Pin 8,12,14,15,17 - NC   |
| Pin 9 - Data In           | Pin 23 - Vcc (Digital)        | All Others - Ground      |
| Pin 10 - Error Flag       | Pin 24 - Vtune (Output)       |                          |
| Pin 11 - Clock In         | Pin 25 - Vcc (VCO)            | NC - No Connection       |
| Pin 13 - VCO (On/Off)***  | Pin 26 - Vout (Converter) *** |                          |
| Pin 16 - Reset **         | Pin 27 - Vcc (Tune) ***       |                          |

\* High when locked (source/sink 500 uA)

\*\* A low on this input will reset the internal controller. Leave unconnected for normal operation.

\*\*\* Connect externally. Bypassed with a 10uF 35V tantalum capacitor.

\*\*\*\* NC if unused.

This product is manufactured under one or more of the following patents: Approved: 5,180,810; 5,122,621; 5,380,346; 5,416,446; 5,630,754; 5,805,431; 6,325,623; 6,056,573; 7,088,189; 7,180,381; 7,185,591; 7,262,670; 7,265,642; 7,292,113  
 Pending: 6,648,307; 6,650,137; 6,658,719; 6,657,957; 6,653,675; 6,653,481; 6,654,173; 6,658,083; 6,660,822; 6,660,579; 6,673,803; 6,673,804; 6,673,805; 6,673,806; 6,673,807; 6,673,808; 6,673,809; 6,673,810; 6,673,811; 6,673,812; 6,673,813; 6,673,814; 6,673,815; 6,673,816; 6,673,817; 6,673,818; 6,673,819; 6,673,820; 6,673,821; 6,673,822; 6,673,823; 6,673,824; 6,673,825; 6,673,826; 6,673,827; 6,673,828; 6,673,829; 6,673,830; 6,673,831; 6,673,832; 6,673,833; 6,673,834; 6,673,835; 6,673,836; 6,673,837; 6,673,838; 6,673,839; 6,673,840; 6,673,841; 6,673,842; 6,673,843; 6,673,844; 6,673,845; 6,673,846; 6,673,847; 6,673,848; 6,673,849; 6,673,850; 6,673,851; 6,673,852; 6,673,853; 6,673,854; 6,673,855; 6,673,856; 6,673,857; 6,673,858; 6,673,859; 6,673,860; 6,673,861; 6,673,862; 6,673,863; 6,673,864; 6,673,865; 6,673,866; 6,673,867; 6,673,868; 6,673,869; 6,673,870; 6,673,871; 6,673,872; 6,673,873; 6,673,874; 6,673,875; 6,673,876; 6,673,877; 6,673,878; 6,673,879; 6,673,880; 6,673,881; 6,673,882; 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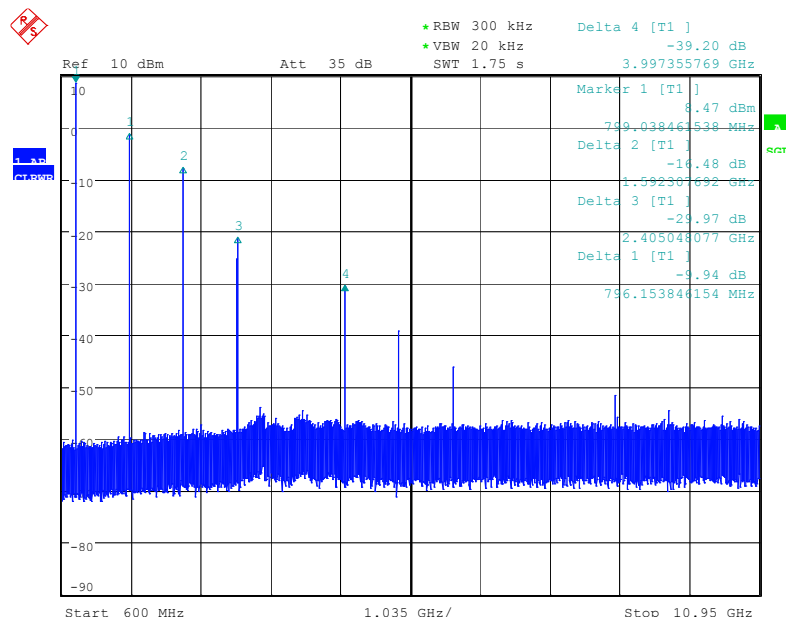
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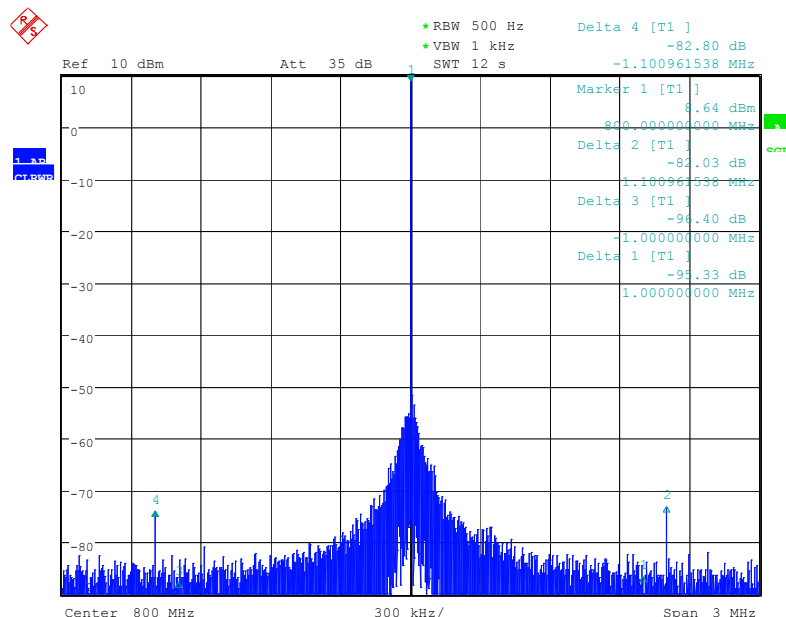
WIDE BANDWIDTH

800 - 2100 MHz

### HARMONIC SUPPRESSION



### REFERENCE SIDEBAND SUPPRESSION



PERFORMANCE PLOTS

This product is manufactured under one or more of the following patents: Approved: 5,180,810; 5,122,621; 5,330,348; 5,416,446; 5,630,754; 5,805,431; 6,325,623; 6,856,573; 7,088,189; 7,180,381; 7,185,591; 7,262,670; 7,263,642; 7,292,113  
 Pending: 66/48,307; 60/50,1371 & 65/58,1796; 60/50,7937 & 65/53,6675; 60/56,3481; 60/56,4173; 60/58,0083; 60/60,1822; 60/60,5791; 60/73,268034;  
 60/73,268034; 60/60,3485 & 65/71,0212; 60/60,732560; 60/73,26901; 60/73,20787

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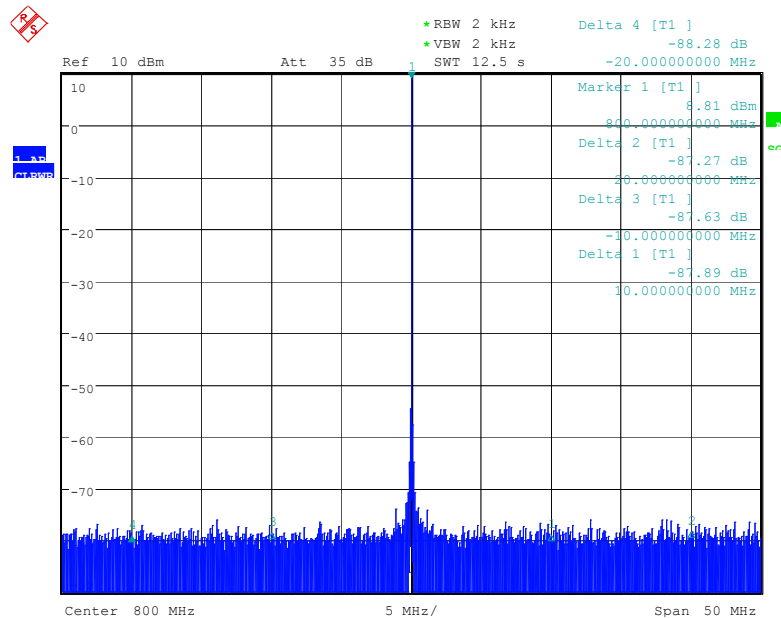
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 Pending: 66/48,307; 60/501,371 & 66/581,796; 60/507,957 & 66/538,675; 60/563,481; 60/564,173; 60/580,083; 60/601,822; 60/605,791; EV31268034;  
 RU258768; 60/623,485 & 66/710212; RU60732560/736901; 60/732787

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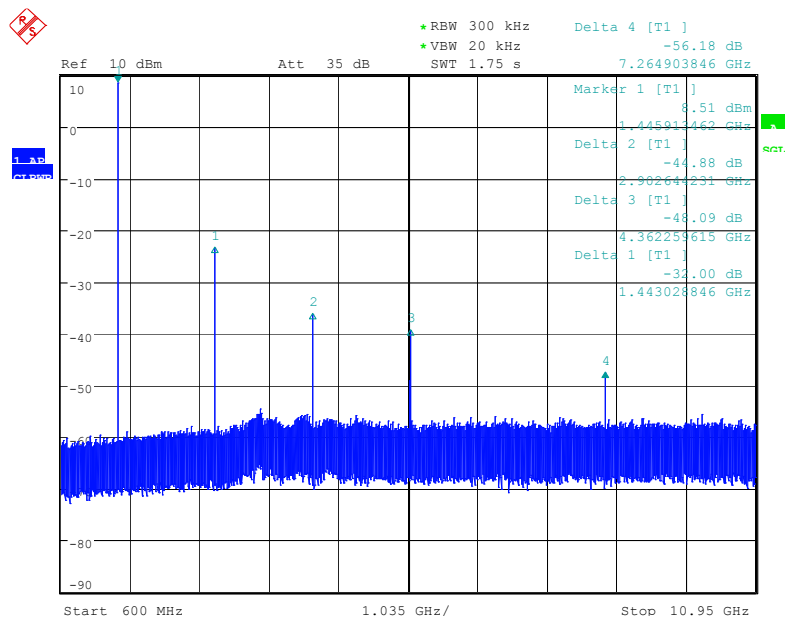
# INTELLIGENT INTERACTIVE SYNTHESIZER

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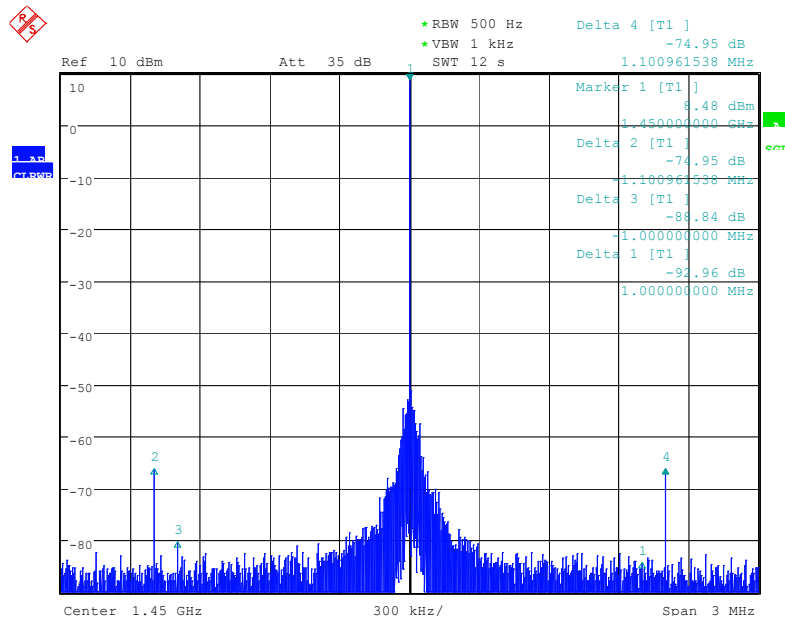
WIDE BANDWIDTH

800 - 2100 MHz

### HARMONIC SUPPRESSION



### REFERENCE SIDEBAND SUPPRESSION



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 60/73,2768; 60/62,3485 & 66/71,0212; 60/73,2560/736901; 60/73,2787

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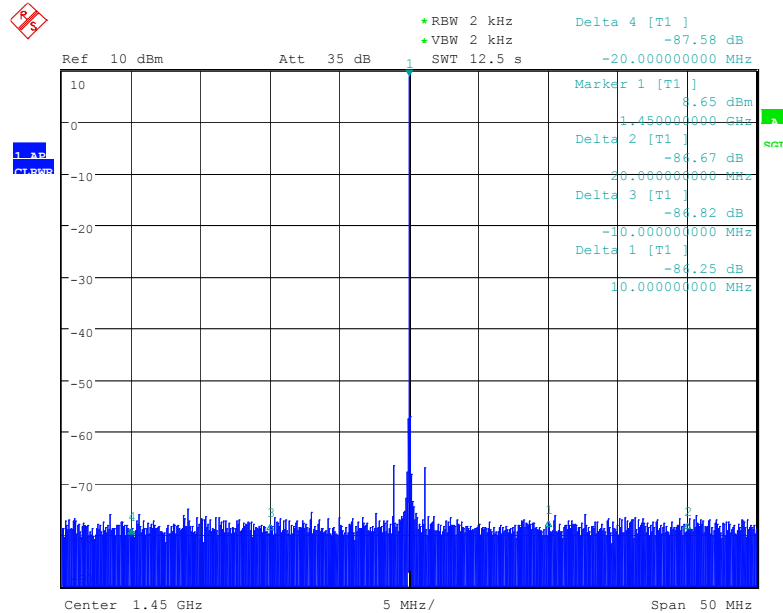
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PERFORMANCE PLOTS

This product is manufactured under one or more of the following patents: Approved: 5,183,810; 5,122,621; 5,330,348; 5,416,446; 5,630,754; 5,805,431; 6,325,623; 6,056,573; 7,088,189; 7,180,381; 7,185,591; 7,282,670; 7,285,642; 7,292,113  
Pending: 6,648,307; 6,650,137; 6,658,179; 6,652,793; 6,653,675; 6,663,481; 6,656,173; 6,658,083; 6,665,822; 6,665,791; EV31268034;  
RU258768; 6,662,485 & 6,671,031; RU60732560736901; 6,673,078

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# INTELLIGENT INTERACTIVE SYNTHESIZER

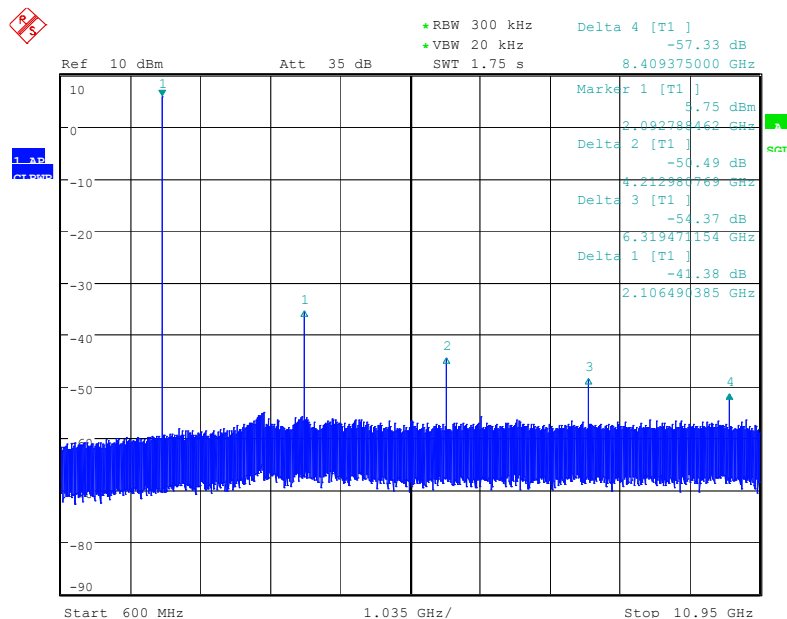
## SURFACE MOUNT MODEL: LFSW80210-100

WIDE BANDWIDTH

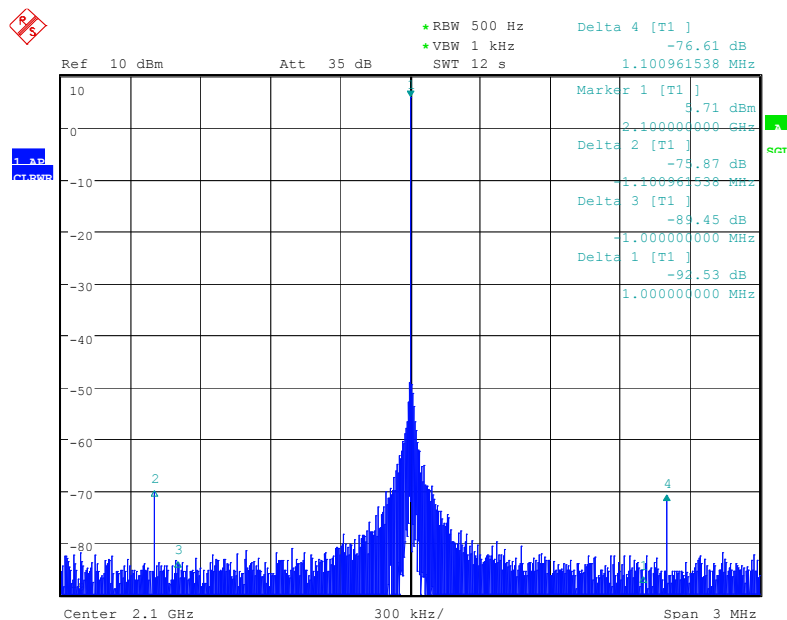
800 - 2100 MHz

PERFORMANCE PLOTS

HARMONIC SUPPRESSION



REFERENCE SIDEBAND SUPPRESSION



This product is manufactured under one or more of the following patents: Approved: 5,183,810; 5,122,621; 5,330,346; 5,416,446; 5,650,754; 5,805,431; 6,325,623; 6,856,573; 7,088,182; 7,180,381; 7,185,591; 7,262,670; 7,265,642; 7,292,113  
 Pending: 6,648,307; 6,650,137; 6,658,179; 6,652,937; 6,653,673; 6,653,481; 6,654,173; 6,658,083; 6,661,822; 6,660,579; 6,673,634; 6,673,768; 6,682,485; 6,671,031; 6,673,256; 6,673,691; 6,673,787

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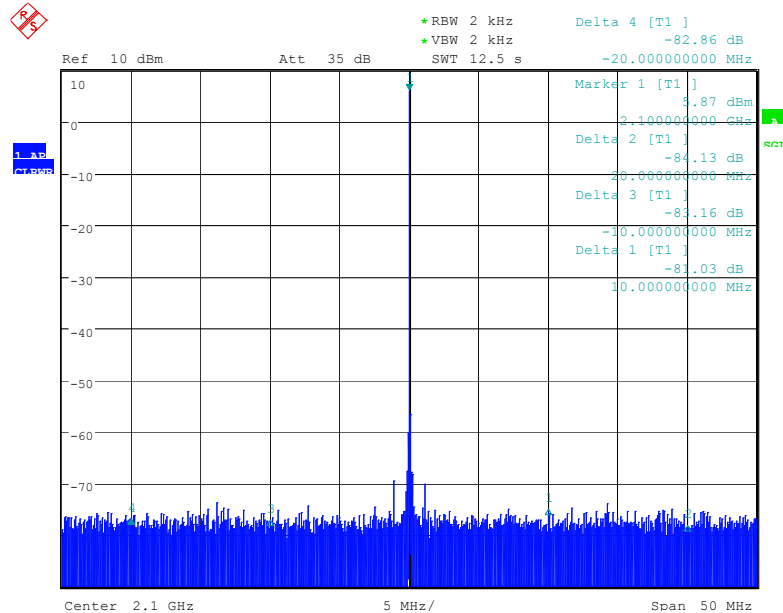
# INTELLIGENT INTERACTIVE SYNTHESIZER

## SURFACE MOUNT MODEL: LFSW80210-100

WIDE BANDWIDTH

800 - 2100 MHz

REFERENCE SIDEBAND SUPPRESSION



PERFORMANCE PLOTS

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Pending: 66/48,307; 60/501,371 & 66/581,796; 60/507,937 & 66/538,675; 60/563,481; 60/564,173; 60/580,083; 60/601,822; 60/605,791; EV31268034;  
14,259,768; 60/623,485 & 66/710219; 10/607,325/60/736901; 60/732787

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