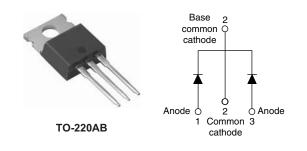
Vishay High Power Products

Ultrafast Rectifier, 2 x 5 A FRED Pt[™]



PRODUCT SUMMARY				
t _{rr}	25 ns			
I _{F(AV)}	2 x 5 A			
V _R	200 V			

FEATURES

- · Ultrafast recovery time
- Low forward voltage drop
- · Low leakage current
- 175 °C operating junction temperature
- Lead (Pb)-free ("PbF" suffix)
- Designed and qualified for industrial level

DESCRIPTION/APPLICATIONS

MUR.. series are the state of the art ultrafast recovery rectifiers specifically designed with optimized performance of forward voltage drop and ultrafast recovery time.

The planar structure and the platinum doped life time control, guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in the output rectification stage of SMPS, UPS, dc-to-dc converters as well as freewheeling diode in low voltage inverters and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS						
PARAMETER		SYMBOL	TEST CONDITIONS	MAX.	UNITS	
Peak repetitive reverse voltage		V _{RRM}		200	V	
Average rectified forward ourrest	per leg			5		
Average rectified forward current tota	total device	I _{F(AV)}	Rated V_R , $T_C = 149 \ ^\circ C$	10		
Non-repetitive peak surge current per leg		I _{FSM}		50	A	
Peak repetitive forward current per leg		I _{FM}	Rated V _R , square wave, 20 kHz T _C = 149 °C	10		
Operating junction and storage temperatures		T _J , T _{Stg}		- 65 to 175	°C	

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V _{BR} , V _R	I _R = 100 μA	200	-	-	
		I _F = 5 A, T _J = 125 °C	-	0.87	0.99	v
Forward voltage V _F	V _F	I _F = 10 A, T _J = 125 °C	-	1.02	1.20	
	I _F = 10 A	-	1.12	1.25		
Reverse leakage current I _R	$V_{R} = V_{R}$ rated	-	-	10		
	IR	$T_J = 150 \ ^{\circ}C, \ V_R = V_R \ rated$	-	-	250	μA
Junction capacitance	CT	V _R = 200 V	-	8	-	pF
Series inductance	L _S	Measured lead to lead 5 mm from package body	-	8.0	-	nH

* Pb containing terminations are not RoHS compliant, exemptions may apply

COMPLIANT

MUR1020CTPbF

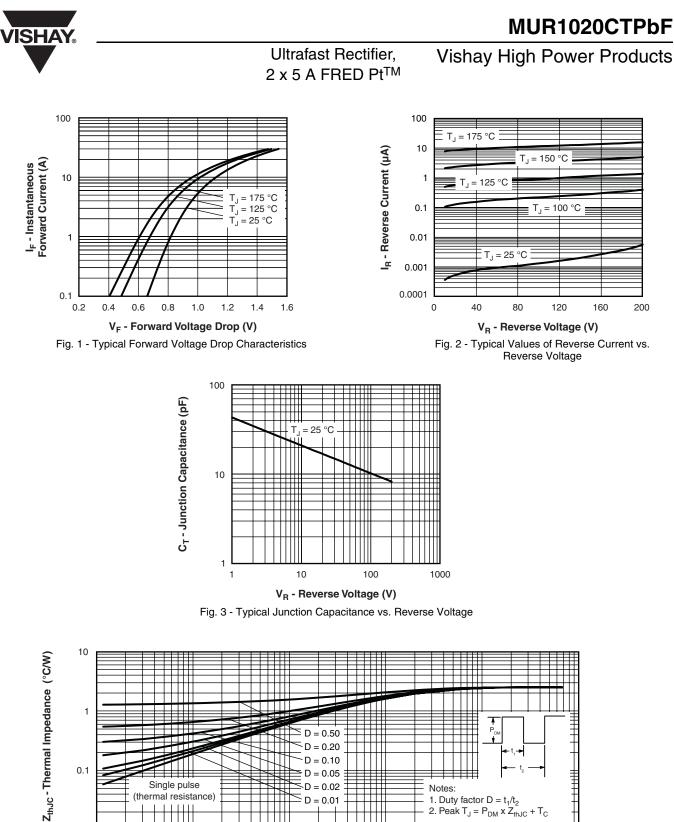
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DYNAMIC RECOVERY CHARACTERISTICS ($T_J = 25 \text{ °C}$ unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
		$I_F = 1.0 \text{ A}, \text{ d}I_F/\text{d}t = 50 \text{ A}/\mu\text{s}, \text{ V}_R = 30 \text{ V}$		-	-	35	
	$I_F = 0.5 \text{ A}, I_R = 1.0 \text{ A}, I_{REC} = 0.25 \text{ A}$		-	-	25		
neverse recovery lime	Reverse recovery time t _{rr}	T _J = 25 °C		-	24	-	ns
		T _J = 125 °C	$I_F = 5 A$	-	35	-	
Pools recovery ourrent		T _J = 25 °C		-	3.3	-	А
Peak recovery current I _{RRM}	T _J = 125 °C	dl _F /dt = 200 A/µs V _B = 160 V	-	5.0	-	~	
Reverse recovery charge	0	T _J = 25 °C		-	33	-	nC
	Q _{rr}	T _J = 125 °C		-	76	-	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		- 65	-	175	°C
Thermal resistance, junction to case per leg	R _{thJC}		-	-	5	
Thermal resistance, junction to ambient per leg	R _{thJA}		-	-	50	°C/W
Thermal resistance, case to heatsink	R _{thCS}	Mounting surface, flat, smooth and greased	-	0.5	-	
Maight			-	2.0	-	g
Weight			-	0.07	-	oz.
Mounting torque			6.0 (5.0)	-	12 (10)	kgf ⋅ cm (lbf ⋅ in)
Marking device		Case style TO-220AB		MUR	020CT	•



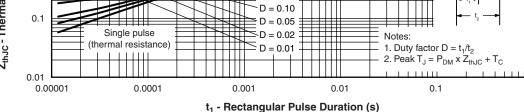


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics

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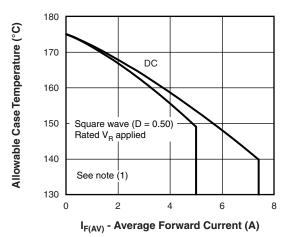
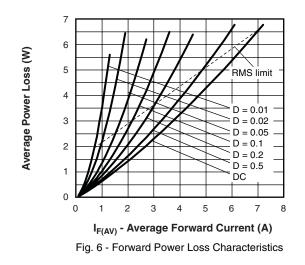


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current



Note

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC};$ $Pd = Forward power loss = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig. 6)};$ $Pd_{REV} = Inverse power loss = V_{R1} \times I_R (1 - D); I_R \text{ at } V_{R1} = Rated V_R$

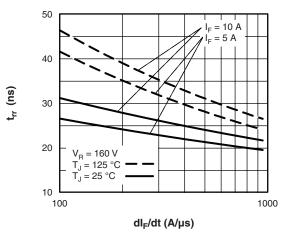


Fig. 7 - Typical Reverse Recovery Time vs. dl_F/dt

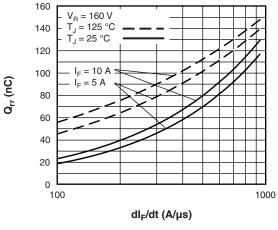


Fig. 8 - Typical Stored Charge vs. dl_F/dt



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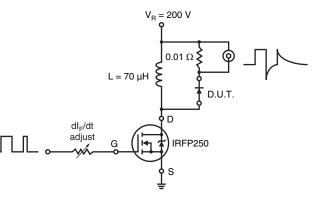
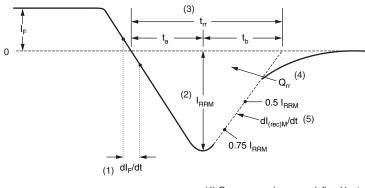


Fig. 9 - Reverse Recovery Parameter Test Circuit



- (1) dI_F/dt rate of change of current through zero crossing
- (4) ${\rm Q}_{\rm rr}$ area under curve defined by ${\rm t}_{\rm rr}$ and ${\rm I}_{\rm RRM}$

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(3) t_{rr} - reverse recovery time measured from zero crossing point of negative going I_F to point where a line passing through 0.75 I_{RRM} and 0.50 I_{RRM} extrapolated to zero current.

(2) I_{RRM} - peak reverse recovery current

(5) $dI_{(rec)M}/dt$ - peak rate of change of current during t_b portion of t_{rr}

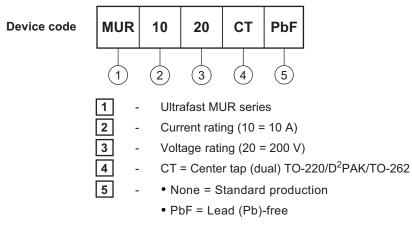
Fig. 10 - Reverse Recovery Waveform and Definitions

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ORDERING INFORMATION TABLE



Tube standard pack quantity: 50 pieces

LINKS TO RELATED DOCUMENTS			
Dimensions http://www.vishay.com/doc?95222			
Part marking information	http://www.vishay.com/doc?95225		



Vishay

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