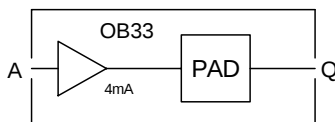


OB33 is a 4.0 mA output pad cell.

Truth Table

A	Q
L	L
H	H



Capacitance

	Ci (pF)
A	0.224

Area

71.61 mils²

Power

126.30 $\mu\text{W}/\text{MHz}$

Delay [ns] = $\text{tpd}_{..}$ = f(SL, L) with SL = Input Slope [ns] ; L = Output Load [pF]
 Output Slope [ns] = $\text{op_sl}_{..}$ = f(L) with L = Output Load [pF]

AC Characteristics : T_j = 25°C VDD = 3.3V Typical Process

AC Characteristics

Characteristics	Symbol	SL = 0.1			SL = 2.0		
		L = 10.0	L = 70.0	L = 100.0	L = 10.0	L = 70.0	L = 100.0
Delay A to Q	tpdar	1.70	9.11	12.52	1.89	9.09	13.06
	tpdaf	1.51	7.04	8.80	1.87	7.40	9.41
Output Slope A to Q	op_slar	4.02	27.75	39.35	4.13	27.52	37.90
	op_slaf	2.83	18.47	25.48	2.83	19.87	26.90