Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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MOS FIELD EFFECT TRANSISTOR NP82N04MDG, NP82N04NDG

SWITCHING N-CHANNEL POWER MOS FET

DESCRIPTION

The NP82N04MDG and NP82N04NDG are N-channel MOS Field Effect Transistors designed for high current switching applications.

ORDERING INFORMATION

PART NUMBER	LEAD PLATING	PACKING	PACKAGE
NP82N04MDG-S18-AY Note	D 0 (T)	Tube	TO-220 (MP-25K) typ. 1.9 g
NP82N04NDG-S18-AY Note	Pure Sn (Tin)	50 p/tube	TO-262 (MP-25SK) typ. 1.8 g

Note Pb-free (This product does not contain Pb in the external electrode.)

FEATURES

Logic level

• Super low on-state resistance

 $R_{DS(on)1} = 4.2 \text{ m}\Omega \text{ MAX.} \text{ (Vgs} = 10 \text{ V, Ip} = 41 \text{ A)}$

 $R_{DS(on)2} = 8.5 \text{ m}\Omega \text{ MAX.} \text{ (VGS = 4.5 V, ID = 41 A)}$

· High current rating

 $I_{D(DC)} = \pm 82 \text{ A}$

• Low input capacitance

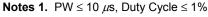
Ciss = 6000 pF TYP.

• Designed for automotive application and AEC-Q101 qualified

(TO-220)



VDSS	40	V
Vgss	±20	V
ID(DC)	±82	Α
D(pulse)	±328	Α
P _{T1}	143	W
P _{T2}	1.8	W
Tch	175	°C
Tstg	-55 to +175	°C
I AR	43	Α
Ear	185	mJ
	VGSS ID(DC) ID(pulse) PT1 PT2 Tch Tstg IAR	VGSS ±20 ID(DC) ±82 ID(pulse) ±328 PT1 143 PT2 1.8 Tch 175 Tstg -55 to +175 IAR 43



2. Tch $\leq 150^{\circ}$ C, RG = 25 Ω

THERMAL RESISTANCE

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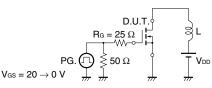


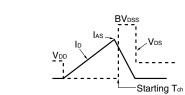
ELECTRICAL CHARACTERISTICS (TA = 25°C)

CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	IDSS	V _{DS} = 40 V, V _{GS} = 0 V			1	μΑ
Gate Leakage Current	Igss	V _{GS} = ±20 V, V _{DS} = 0 V			±100	nA
Gate to Source Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA	1.4		2.5	V
Forward Transfer Admittance Note	yfs	V _{DS} = 5 V, I _D = 41 A	20	65		S
Drain to Source On-state Resistance Note	RDS(on)1	V _{GS} = 10 V, I _D = 41 A		3.4	4.2	mΩ
	RDS(on)2	V _{GS} = 4.5 V, I _D = 41 A		5.4	8.5	mΩ
Input Capacitance	Ciss	V _{DS} = 25 V,		6000	9000	pF
Output Capacitance	Coss	V _{GS} = 0 V,		580	870	pF
Reverse Transfer Capacitance	Crss	f = 1 MHz		370	670	pF
Turn-on Delay Time	t _{d(on)}	V _{DD} = 20 V, I _D = 41 A,		26	60	ns
Rise Time	tr	V _{GS} = 10 V,		68	170	ns
Turn-off Delay Time	t _{d(off)}	$R_G = 0 \Omega$		73	150	ns
Fall Time	tf			11	30	ns
Total Gate Charge	Q _G	V _{DD} = 32 V,		100	150	nC
Gate to Source Charge	Qgs	V _{GS} = 10 V,		19		nC
Gate to Drain Charge	Q _{GD}	I _D = 82 A		32		nC
Body Diode Forward Voltage Note	V _{F(S-D)}	I _F = 82 A, V _{GS} = 0 V		0.9	1.5	V
Reverse Recovery Time	trr	IF = 82 A, VGS = 0 V,		43		ns
Reverse Recovery Charge	Qrr	di/dt = 100 A/ <i>μ</i> s		47		nC

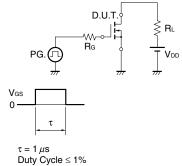
Note Pulsed test

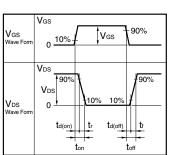
TEST CIRCUIT 1 AVALANCHE CAPABILITY





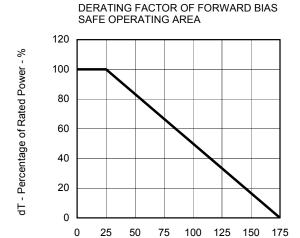
TEST CIRCUIT 2 SWITCHING TIME

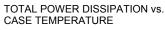


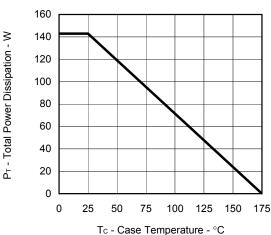


TEST CIRCUIT 3 GATE CHARGE

TYPICAL CHARACTERISTICS (TA = 25°C)

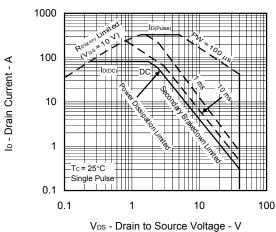




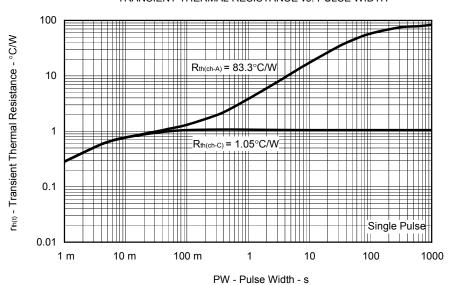


FORWARD BIAS SAFE OPERATING AREA

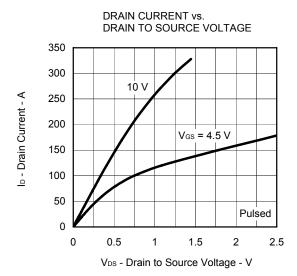
Tc - Case Temperature - °C

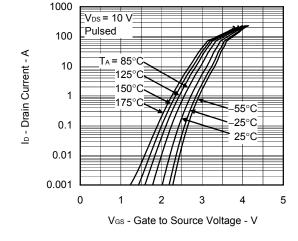


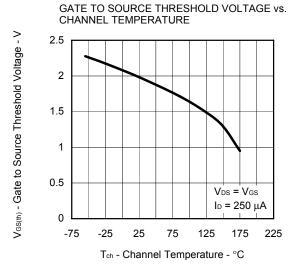


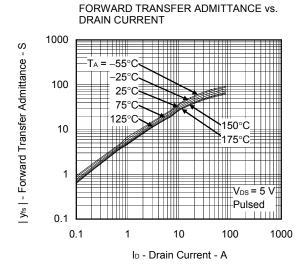


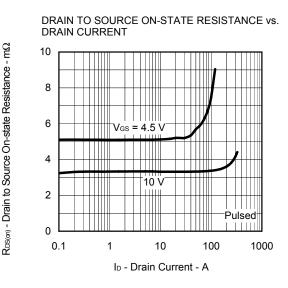
FORWARD TRANSFER CHARACTERISTICS

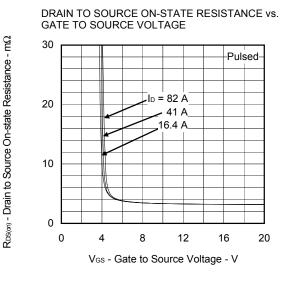




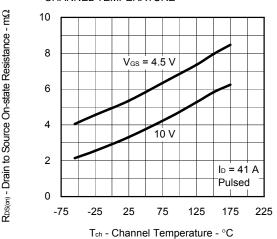




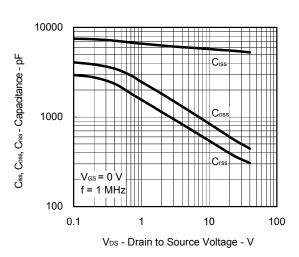




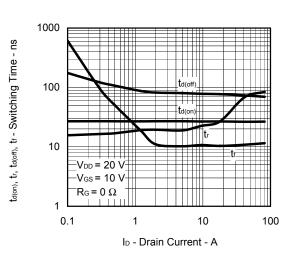
DRAIN TO SOURCE ON-STATE RESISTANCE vs. CHANNEL TEMPERATURE



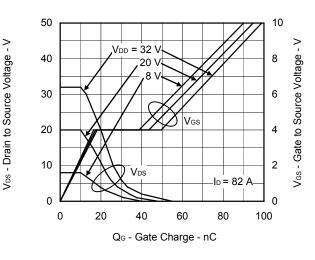
CAPACITANCE vs. DRAIN TO SOURCE VOLTAGE



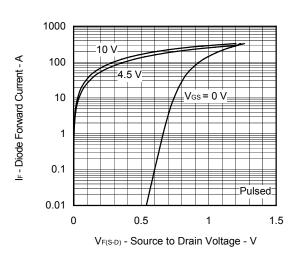
SWITCHING CHARACTERISTICS



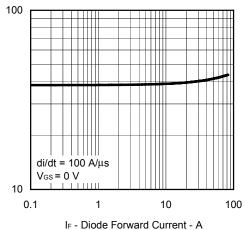
DYNAMIC INPUT/OUTPUT CHARACTERISTICS



SOURCE TO DRAIN DIODE FORWARD VOLTAGE

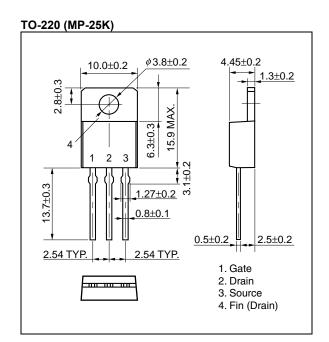


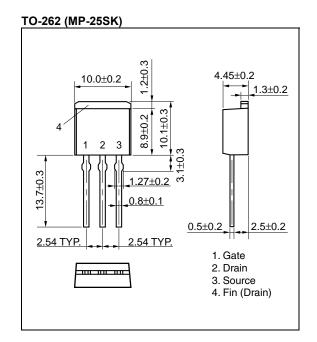
REVERSE RECOVERY TIME vs. DIODE FORWARD CURRENT



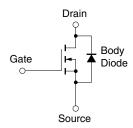
tr - Reverse Recovery Time - ns

PACKAGE DRAWINGS (Unit: mm)





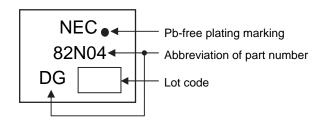
EQUIVALENT CIRCUIT



Remark Strong electric field, when exposed to this device, can cause destruction of the gate oxide and ultimately degrade the device operation. Steps must be taken to stop generation of static electricity as much as possible, and quickly dissipate it once, when it has occurred.



MARKING INFORMATION



RECOMMENDED SOLDERING CONDITIONS

These products should be soldered and mounted under the following recommended conditions.

For soldering methods and conditions other than those recommended below, please contact an NEC Electronics sales representative.

For technical information, see the following website.

Semiconductor Device Mount Manual (http://www.necel.com/pkg/en/mount/index.html)

Soldering Method	Soldering Conditions	Recommended Condition Symbol
Wave soldering NP82N04MDG, NP82N04NDG	Maximum temperature (Solder temperature): 260°C or below Time: 10 seconds or less Maximum chlorine content of rosin flux: 0.2% (wt.) or less	THDWS
Partial heating NP82N04MDG, NP82N04NDG	Maximum temperature (Pin temperature): 350°C or below Time (per side of the device): 3 seconds or less Maximum chlorine content of rosin flux: 0.2% (wt.) or less	P350

Caution Do not use different soldering methods together (except for partial heating).

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