

## Low Noise, Rail-to-Rail Output Dual CMOS Operational Amplifier

### ■ GENERAL DESCRIPTION

The NJU7029 is a CMOS operational amplifier that features low noise as  $V_{NI}=13\text{V}/\sqrt{\text{Hz}}$  (typ.) @ $f=1\text{kHz}$ , low operating voltage. FET input devices provide very low input bias current and are suitable for applications using current signal such as accelerometers, shock sensors and photodiode amplifiers.

### ■ FEATURES

#### Low Noise

- Voltage Noise       $13\text{nV}/\sqrt{\text{Hz}}$  (typ.) @ $f=1\text{kHz}$
- $3\mu\text{Vrms}$  (max.) @ $f=100\text{Hz} \sim 20\text{kHz}$

#### Easy to Use

- Gain Bandwidth      3MHz
- Slew Rate             $1\text{V}/\mu\text{s}$  (typ.) @ $R_L=50\text{k}\Omega$
- I<sub>source</sub> / I<sub>sink</sub>       $200\mu\text{A}$
- Specified for +5V, +3V and +2.2V operation

#### CMOS Process

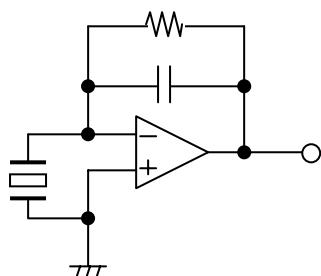
- Input Bias Current      1pA (typ.)
- Rail-to-Rail Output

- Offset Voltage      5mV (max.)
- Offset Voltage Drift       $2\mu\text{V}/^\circ\text{C}$  (typ.)
- Supply Range      2.2V ~ 5.5V
- Supply Current      850 $\mu\text{A}$ /all ch (typ.) @ $V_{DD}=+5\text{V}$
- Package      SSOP8, TVSP8

### ■ Application

- Shock sensors, Accelerometers
- Charge amplifiers
- Photodiode amplifiers
- Low noise signal processing applications
- Microphone amplifiers

### ■ Typical Application Circuit



Charge Amplifier

### ■ PACKAGE OUTLINE

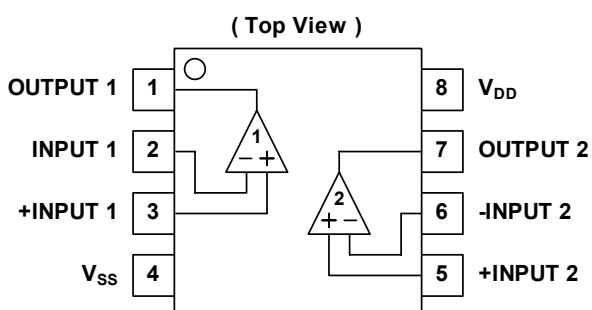


NJU7009V



NJU7009RB1

### ■ PIN CONFIGURATION



# NJU7029

## ■ ABSOLUTE MAXIMUM RATINGS (Ta=25°C)

PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	V <sub>DD</sub>	+7	V
Common Mode Input Voltage Range	V <sub>ICM</sub>	-0.3~+7 (Note1)	V
Differential Input Voltage Range	V <sub>ID</sub>	±7 (Note1)	V
Power Dissipation	P <sub>D</sub>	SSOP8:330 (Note2) TVSP8:410 (Note2)	mW
Operating Temperature Range	T <sub>opr</sub>	-40~+85	°C
Storage Temperature Range	T <sub>stg</sub>	-55~+125	°C

(Note 1) For supply voltage less than 7V, the absolute maximum input voltage is equal to the supply voltage.

(Note 2) On the PCB "EIA/JEDEC (6.2x114.3x1.6mm, two layers FR-4)"

Refer to following Figure 1 for a permissible loss when ambient temperature (Ta) is Ta≥25°C.

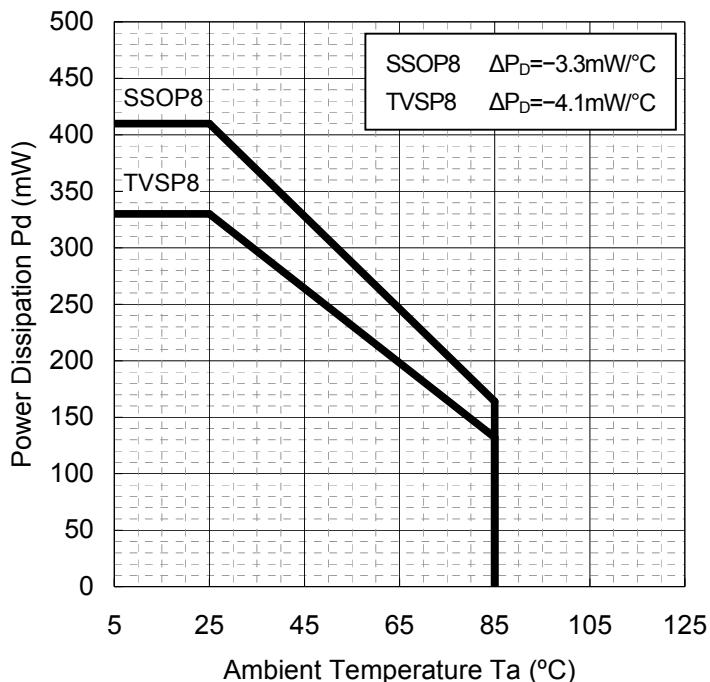


Figure 1 Power Dissipation vs. Ambient Temperature

## ■ OPERATING VOLTAGE (Ta=25°C)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Supply Voltage	V <sub>DD</sub>		2.2	-	5.5	V

## ■ +5V ELECTRICAL CHARACTERISTICS

● DC CHARACTERISTICS ( $V_{DD}=5V$ ,  $T_a=25^{\circ}C$ )

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Operating Current	$I_{DD}$	No Signal	-	850	1150	$\mu A$
Input offset Voltage	$V_{IO}$		-	2	5	$mV$
Input Offset Voltage Drift	$\Delta V_{IO}/\Delta T$	$V_{IN}=V_{DD}/2$ , $T_a=40^{\circ}C \sim +85^{\circ}C$	-	2	-	$\mu V/^{\circ}C$
Input Bias Current	$I_B$		-	1	-	$pA$
Input Offset Current	$I_{IO}$		-	1	-	$pA$
Large Signal Voltage Gain	$A_V$	$R_L=50k\Omega$ to 2.5V, $V_o=2.5V \pm 2V$	65	80	-	$dB$
Common Mode Rejection Ratio	CMR	$V_{ICM}=0V \sim 4.1V$	65	80	-	$dB$
Supply Voltage Rejection Ratio	SVR	$2.2V \leq V_{DD} \leq 5.5V$	65	80	-	$dB$
Output Voltage1	$V_{OH1}$	$R_L=50k\Omega$ to 2.5V	4.9	-	-	V
	$V_{OL1}$	$R_L=50k\Omega$ to 2.5V	-	-	0.1	V
Output Voltage2	$V_{OH2}$	$I_{source}=200\mu A$	4.8	-	-	V
	$V_{OL2}$	$I_{sink}=200\mu A$	-	-	0.2	V
Input Common Mode Voltage Range	$V_{ICM}$	CMR $\geq 65dB$	0	-	4.1	V

● AC CHARACTERISTICS ( $V_{DD}=5V$ ,  $T_a=25^{\circ}C$ )

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Unity Gain Frequency	$f_T$	$Gv=40dB$ , $R_L=50k\Omega$ to 2.5V, $C_L=10pF$	-	3	-	MHz
Equivalent Input Noise Voltage	$V_{NI}$	$Gv=40dB$ , $R_L=50k\Omega$ to 2.5V, $f=1kHz$ ,	-	13	-	$nV/\sqrt{Hz}$
	$V_{Nl rms}$	$Gv=40dB$ , $R_L=50k\Omega$ to 2.5V BPW=100Hz~20kHz	-	1.7	3	$\mu V_{rms}$
Total Harmonic Distortion	THD	$Gv=20dB$ , $R_L=50k\Omega$ to 2.5V, $f_{in}=1kHz$ , $V_{out}=3V_{pp}$ , BPW=400Hz~80kHz	-	0.01	-	%
Channel separation	CS	$f=1kHz$	-	130	-	dB

● TRANSIENT CHARACTERISTICS ( $V_{DD}=5V$ ,  $T_a=25^{\circ}C$ )

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Slew Rate	SR	$Gv=0dB$ , $R_T=50\Omega$ to 2.5V, $R_L=50k\Omega$ to 2.5V, $C_L=15pF$	-	1	-	$V/\mu s$

# NJU7029

## ■ +3V ELECTRICAL CHARACTERISTICS

### ● DC CHARACTERISTICS ( $V_{DD}=3V$ , $T_a=25^{\circ}C$ )

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Operating Current	$I_{DD}$	No Signal	-	610	950	$\mu A$
Input offset Voltage	$V_{IO}$		-	2	5	mV
Input Offset Voltage Drift	$\Delta V_{IO}/\Delta T$	$V_{IN}=V_{DD}/2$ , $T_a=40^{\circ}C \sim +85^{\circ}C$	-	2	-	$\mu V/\deg$
Input Bias Current	$I_B$		-	1	-	pA
Input Offset Current	$I_{IO}$		-	1	-	pA
Large Signal Voltage Gain	$A_V$	$R_L=50k\Omega$ to 1.5V, $V_o=1.5V \pm 1V$	65	80	-	dB
Common Mode Rejection Ratio	CMR	$V_{ICM}=0V \sim 2.1V$	65	80	-	dB
Supply Voltage Rejection Ratio	SVR	$2.2V \leq V_{DD} \leq 5.5V$	65	80	-	dB
Output Voltage1	$V_{OH1}$	$R_L=50k\Omega$ to 1.5V	2.9	-	-	V
	$V_{OL1}$	$R_L=50k\Omega$ to 1.5V	-	-	0.1	V
Output Voltage2	$V_{OH2}$	$I_{source}=200\mu A$	2.8	-	-	V
	$V_{OL2}$	$I_{sink}=200\mu A$	-	-	0.2	V
Input Common Mode Voltage Range	$V_{ICM}$	CMR $\geq 65dB$	0	-	2.1	V

### ● AC CHARACTERISTICS ( $V_{DD}=3V$ , $T_a=25^{\circ}C$ )

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Unity Gain Frequency	$f_T$	$Gv=40dB$ , $R_L=50k\Omega$ to 1.5V, $C_L=10pF$	-	3	-	MHz
Equivalent Input Noise Voltage	$V_{NI}$	$Gv=40dB$ , $R_L=50k\Omega$ to 1.5V, $f=1kHz$	-	13	-	nV/ $\sqrt{Hz}$
	$V_{Nl rms}$	$Gv=40dB$ , $R_L=50k\Omega$ to 1.5V, BPW=100Hz~20kHz	-	1.7	3	$\mu V_{rms}$
Total Harmonic Distortion	THD	$Gv=20dB$ , $R_L=50k\Omega$ to 1.5V, $f_{in}=1kHz$ , $V_{out}=1V_{pp}$ , BPW=400Hz~80kHz	-	0.02	-	%
Channel separation	CS	$f=1kHz$	-	120	-	dB

### ● TRANSIENT CHARACTERISTICS ( $V_{DD}=3V$ , $T_a=25^{\circ}C$ )

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Slew Rate	SR	$Gv=0dB$ , $R_T=50\Omega$ to 1.5V, $R_L=50k\Omega$ to 1.5V, $C_L=15pF$	-	1	-	V/ $\mu s$

## ■ +2.2V ELECTRICAL CHARACTERISTICS

● DC CHARACTERISTICS ( $V_{DD}=2.2V$ ,  $T_a=25^{\circ}C$ )

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Operating Current	$I_{DD}$	No Signal	-	550	890	$\mu A$
Input offset Voltage	$V_{IO}$		-	2	5	mV
Input Offset Voltage Drift	$\Delta V_{IO}/\Delta T$	$V_{IN}=V_{DD}/2$ , $T_a=40^{\circ}C \sim +85^{\circ}C$	-	2	-	$\mu V/\deg$
Input Bias Current	$I_B$		-	1	-	pA
Input Offset Current	$I_{IO}$		-	1	-	pA
Large Signal Voltage Gain	$A_V$	$R_L=50k\Omega$ to 1.5V, $V_o=1.1V \pm 0.5V$	60	80	-	dB
Common Mode Rejection Ratio	CMR	$V_{ICM}=0V \sim 1.3V$	60	80	-	dB
Supply Voltage Rejection Ratio	SVR	$2.2V \leq V_{DD} \leq 5.5V$	65	80	-	dB
Output Voltage1	$V_{OH1}$	$R_L=50k\Omega$ to 1.1V	2.1	-	-	V
	$V_{OL1}$	$R_L=50k\Omega$ to 1.1V	-	-	0.1	V
Output Voltage2	$V_{OH2}$	$I_{source}=200\mu A$	2.0	-	-	V
	$V_{OL2}$	$I_{sink}=200\mu A$	-	-	0.2	V
Input Common Mode Voltage Range	$V_{ICM}$	$CMR \geq 60dB$	0	-	1.3	V

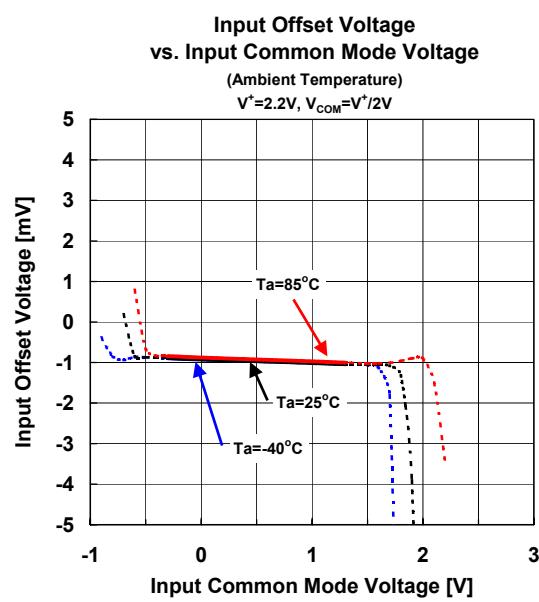
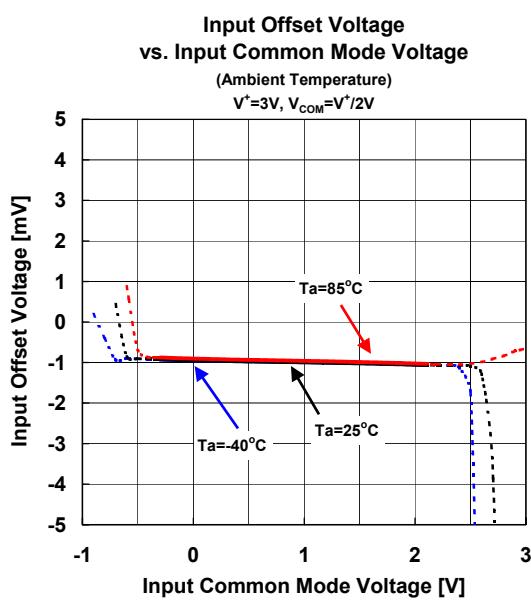
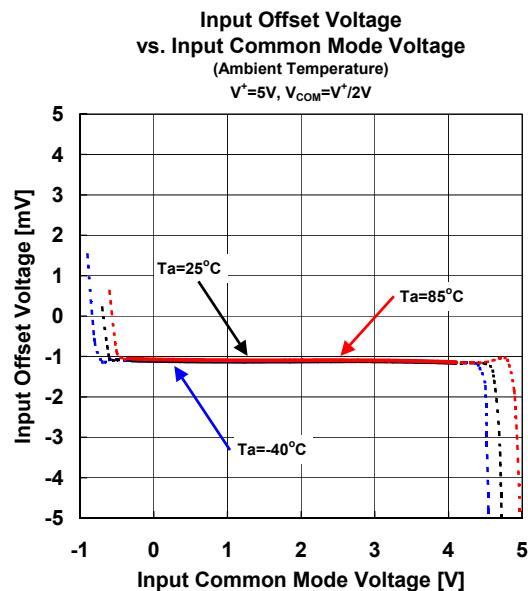
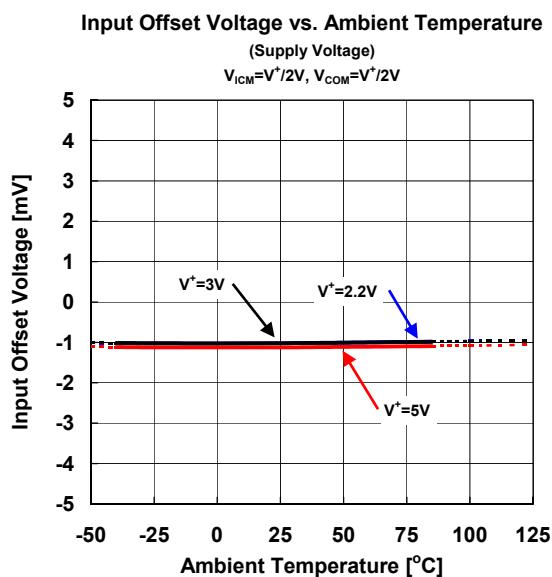
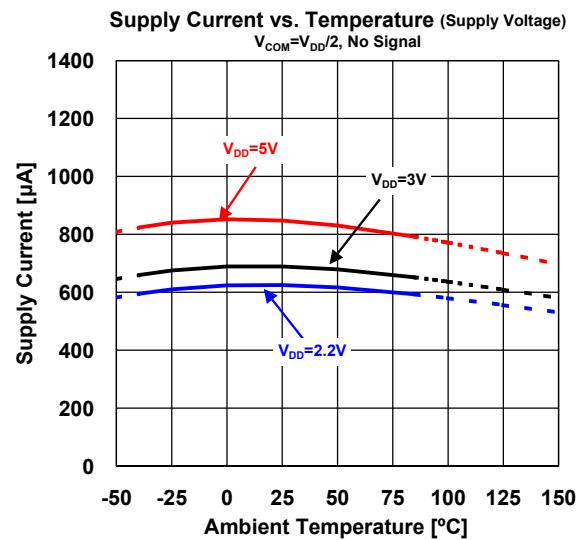
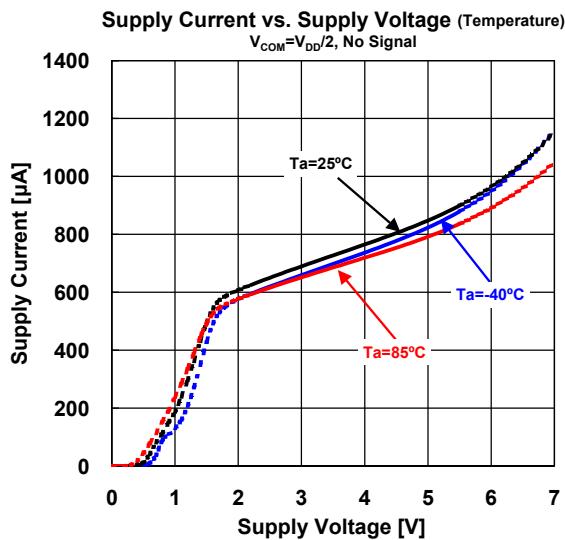
● AC CHARACTERISTICS ( $V_{DD}=2.2V$ ,  $T_a=25^{\circ}C$ )

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Unity Gain Frequency	$f_T$	$Gv=40dB$ , $R_L=50k\Omega$ to 1.1V, $C_L=10pF$	-	3	-	MHz
Equivalent Input Noise Voltage	$V_{NI}$	$Gv=40dB$ , $R_i=50k\Omega$ to 1.1V, $f=1kHz$	-	13	-	nV/ $\sqrt{Hz}$
	$V_{Nl rms}$	$Gv=40dB$ , $R_L=50k\Omega$ to 1.1V, $BPW=100Hz \sim 20kHz$	-	1.7	3	$\mu V_{rms}$
Total Harmonic Distortion	THD	$Gv=20dB$ , $R_L=50k\Omega$ to 1.1V, $f_{in}=1kHz$ , $V_{out}=0.5V_{pp}$ , $BPW=400Hz \sim 80kHz$	-	0.02	-	%
Channel separation	CS	$f=1kHz$	-	115	-	dB

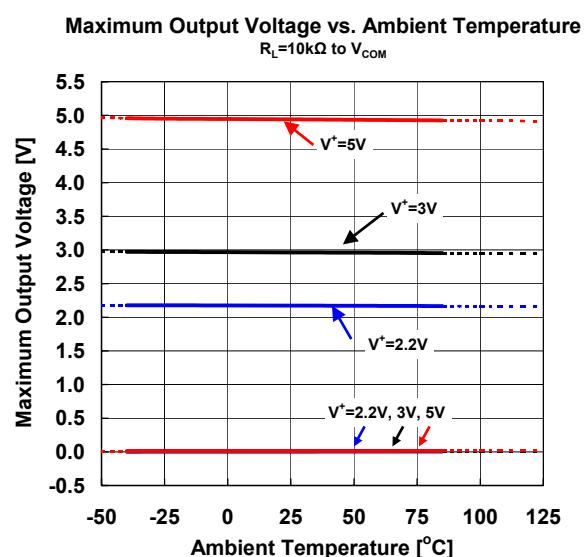
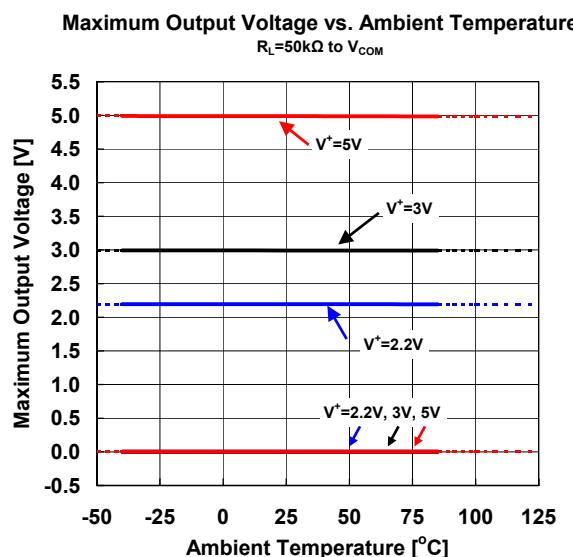
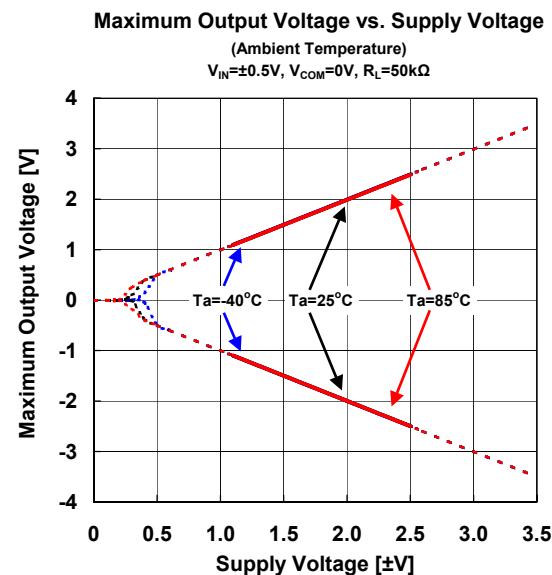
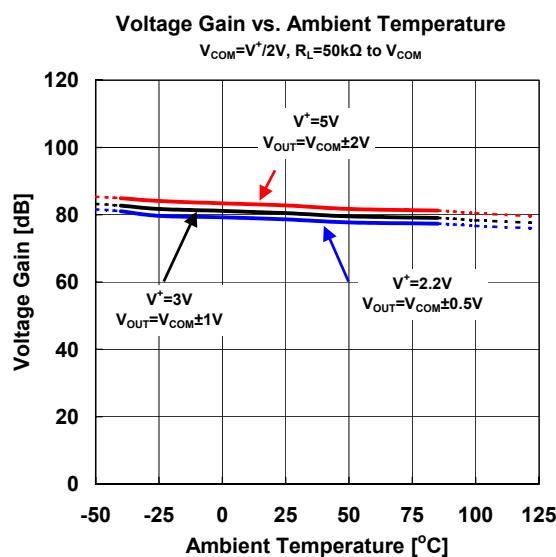
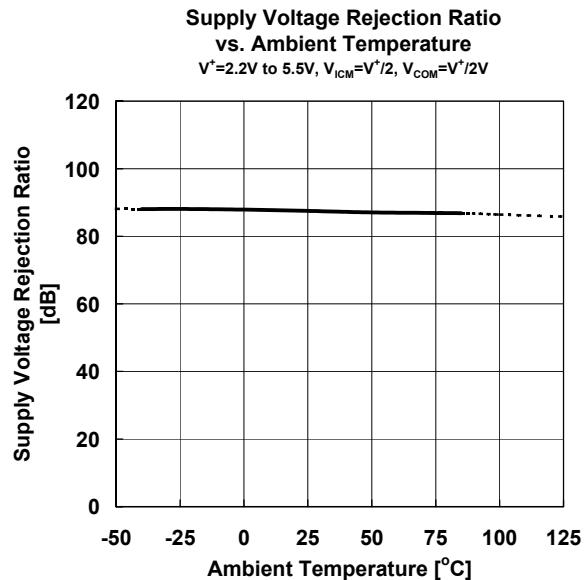
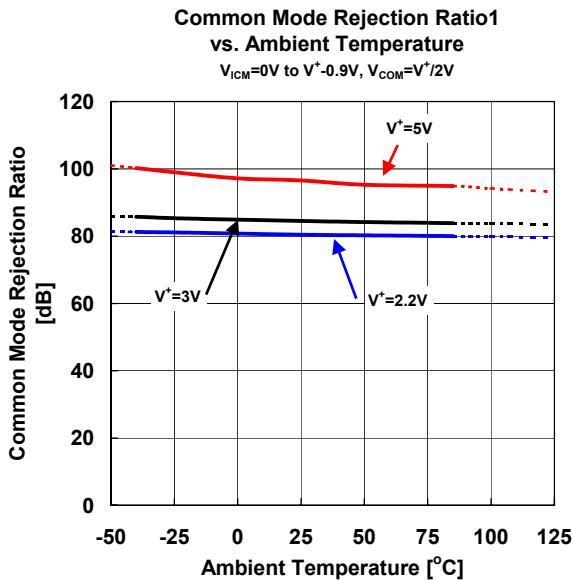
● TRANSIENT CHARACTERISTICS ( $V_{DD}=2.2V$ ,  $T_a=25^{\circ}C$ )

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Slew Rate	SR	$Gv=0dB$ , $R_T=50\Omega$ to 1.1V, $R_L=50k\Omega$ to 1.5V, $C_L=15pF$	-	1	-	V/ $\mu s$

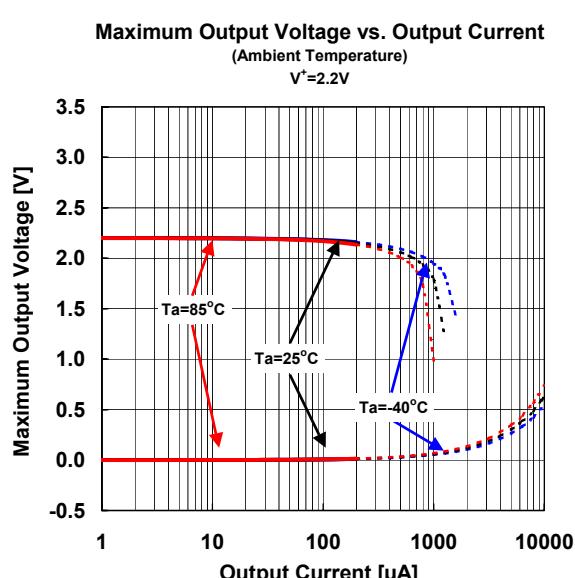
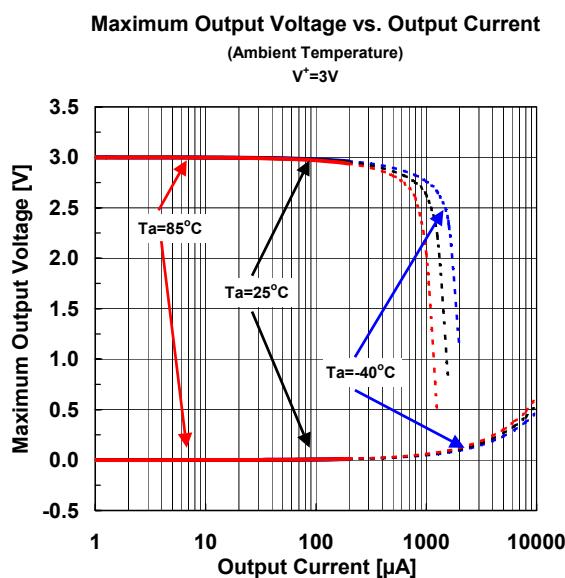
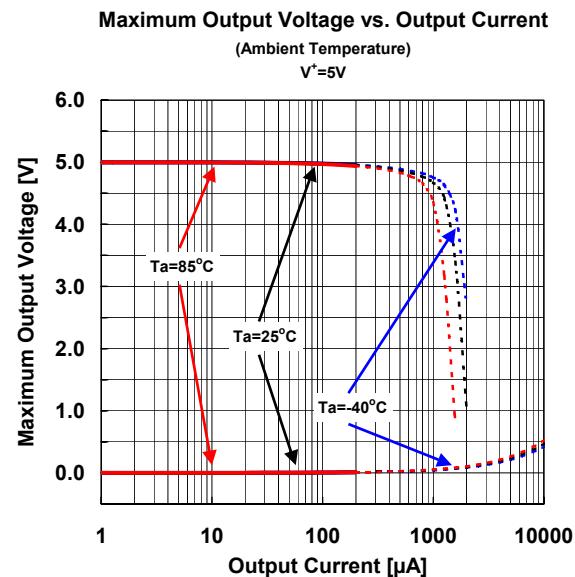
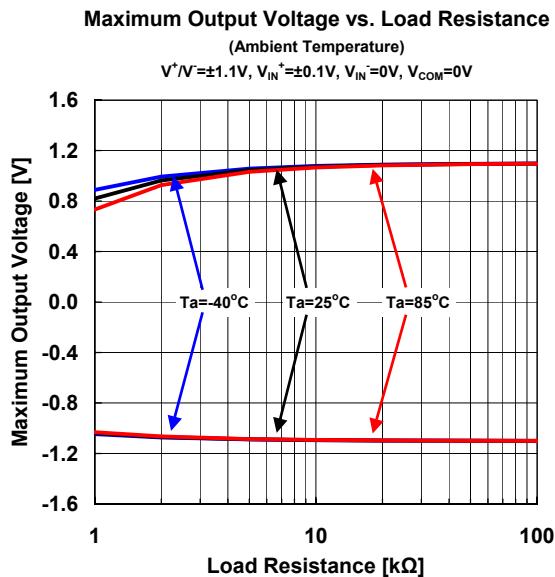
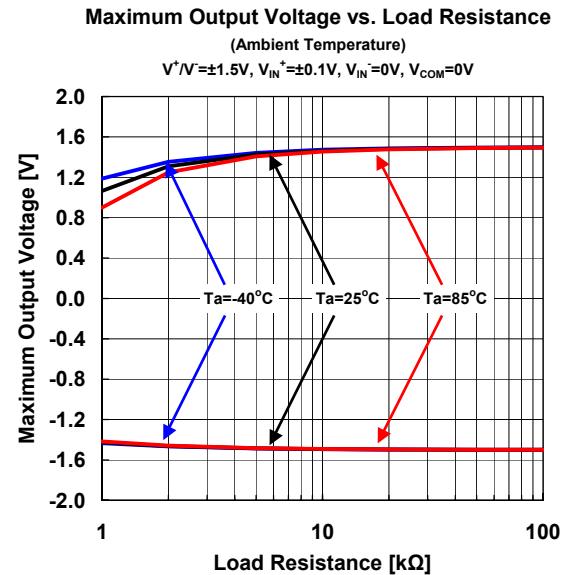
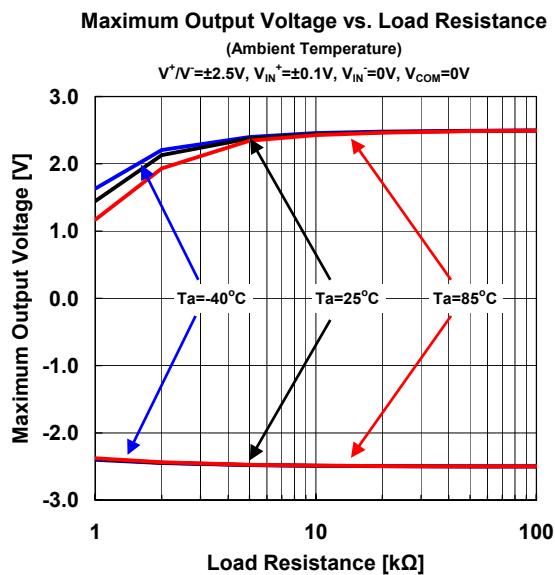
## ■ TYPICAL CHARACTERISTICS



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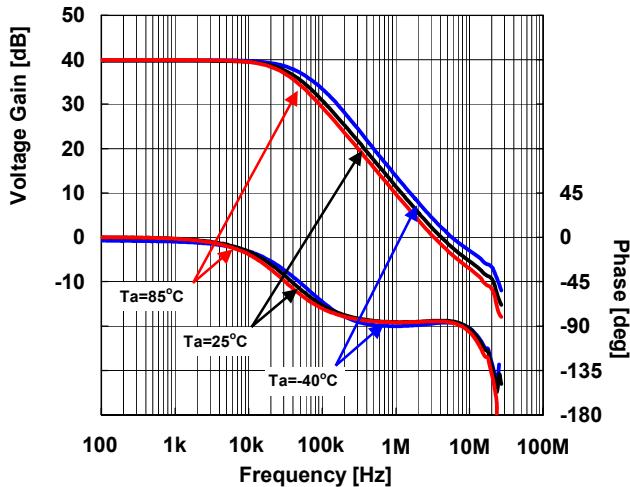


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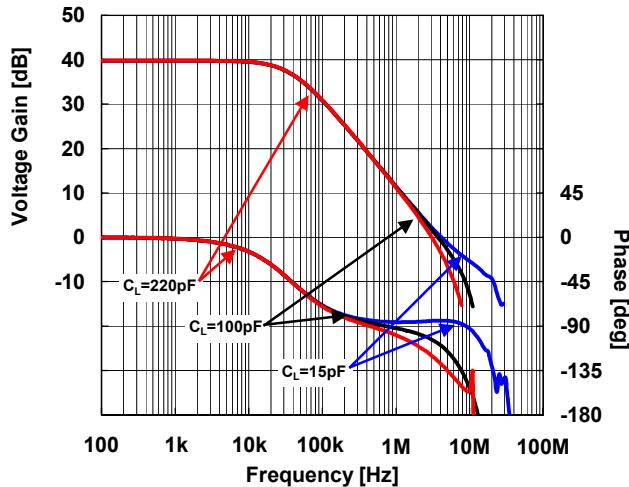


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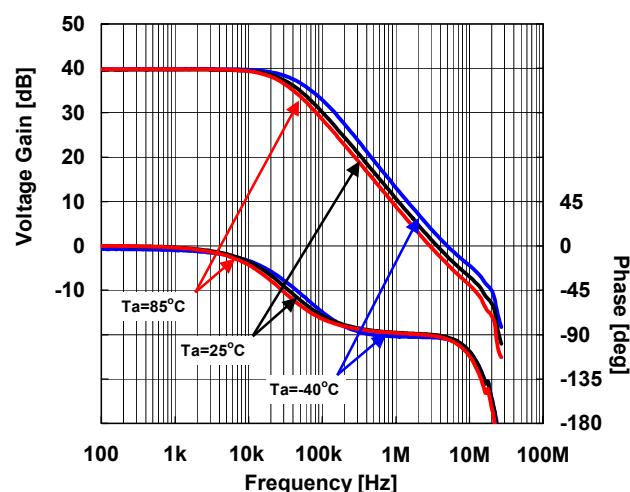
**40dB Gain/Phase vs. Frequency (Temperature)**  
 $V^+=5V$ ,  $V_{COM}=V^+/2$ ,  $G_V=40dB$ ,  $R_T=50\Omega$ ,  $R_L=50k\Omega$ ,  $C_L=15pF$



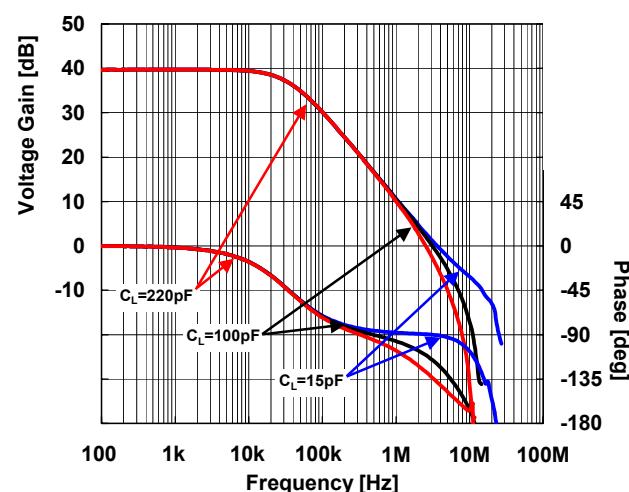
**40dB Gain/Phase vs. Frequency (Load Capacitance)**  
 $V^+=5V$ ,  $V_{COM}=V^+/2$ ,  $G_V=40dB$ ,  $R_S=50\Omega$ ,  $R_L=50k\Omega$ ,  $Ta=25^\circ C$



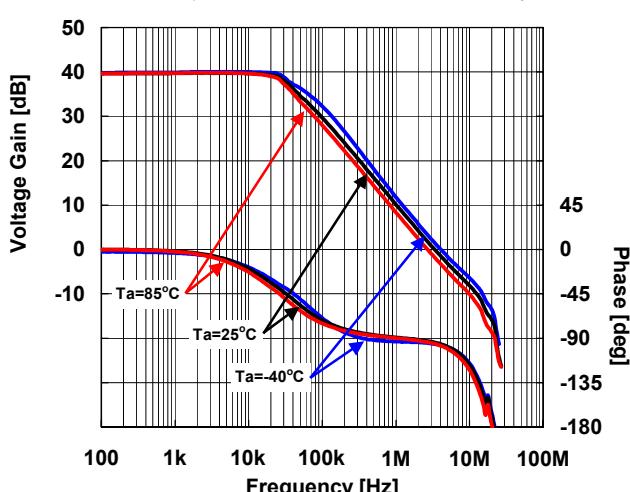
**40dB Gain/Phase vs. Frequency (Temperature)**  
 $V^+=3V$ ,  $V_{COM}=V^+/2$ ,  $G_V=40dB$ ,  $R_T=50\Omega$ ,  $R_L=50k\Omega$ ,  $C_L=15pF$



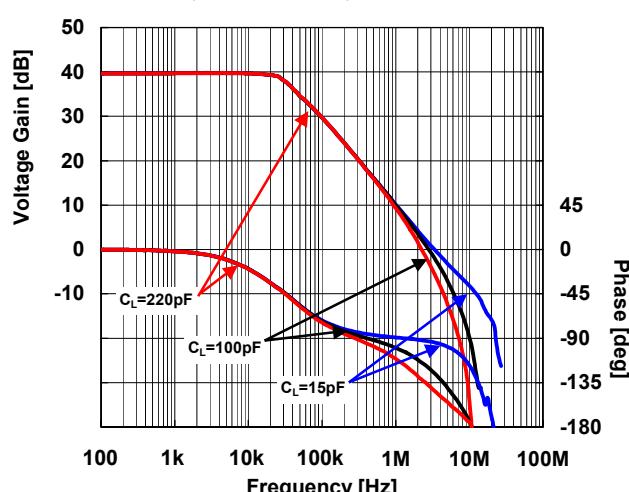
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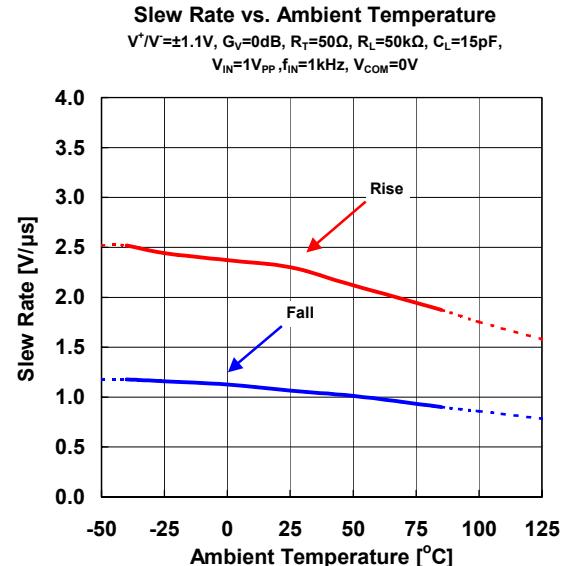
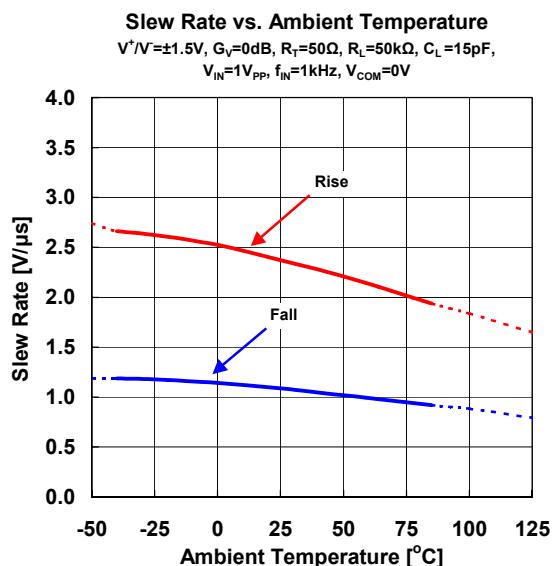
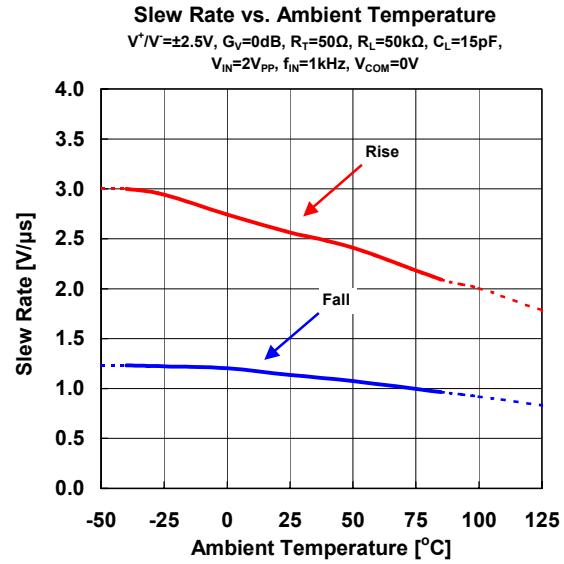
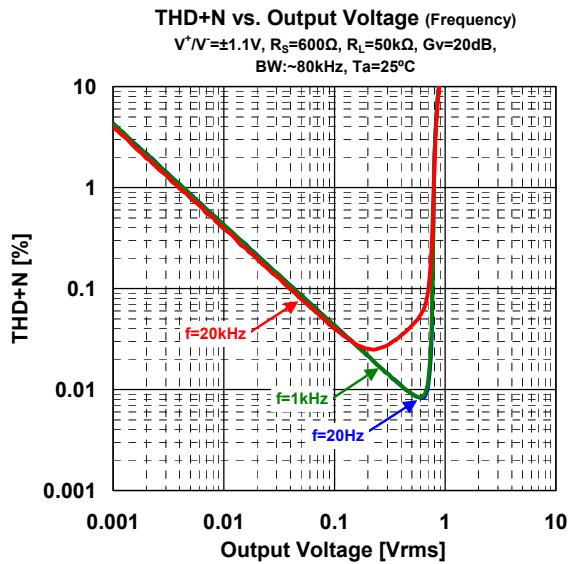
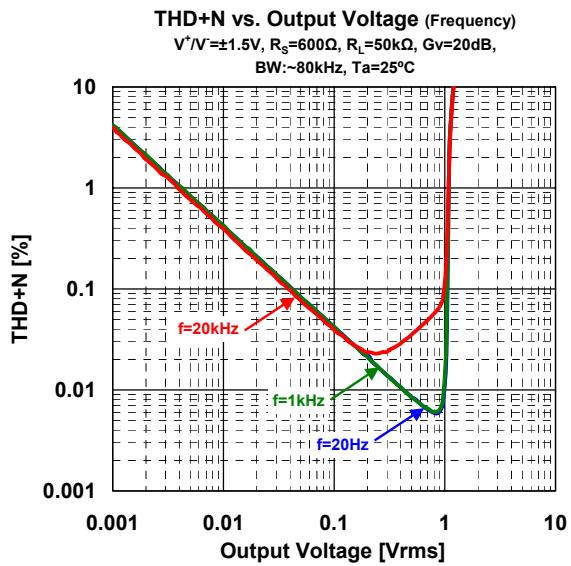
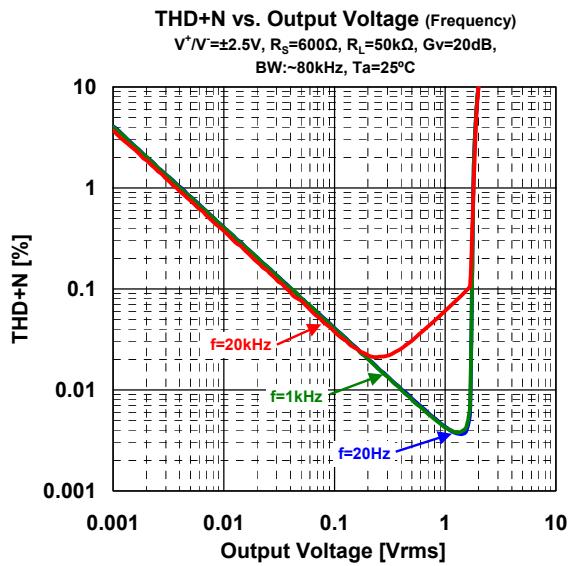
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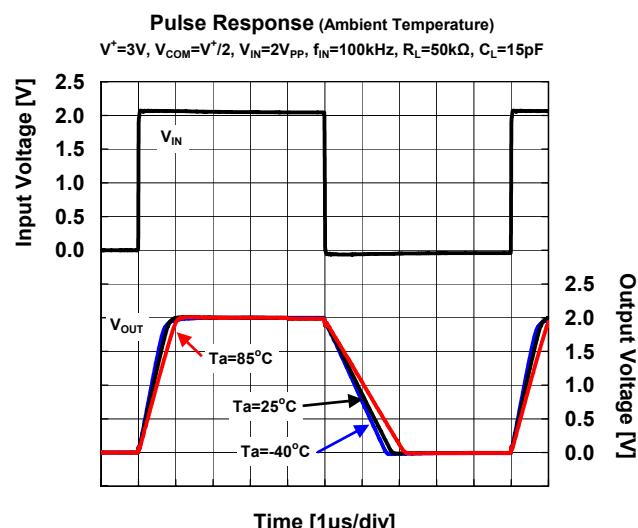
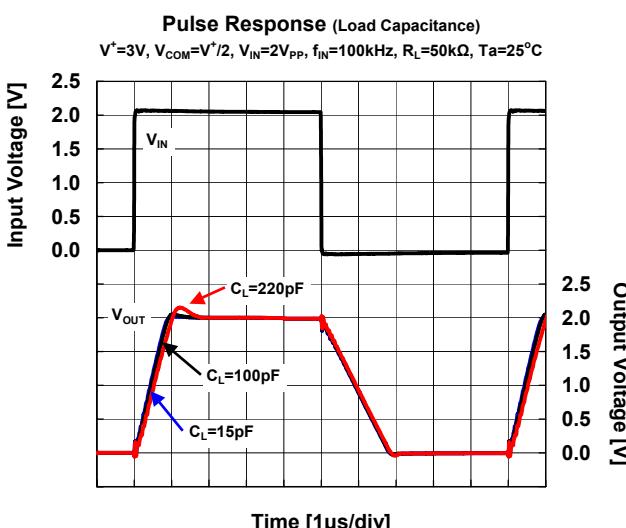
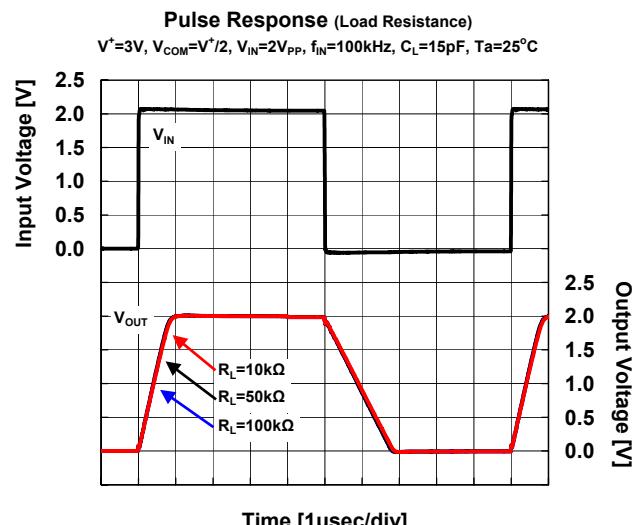
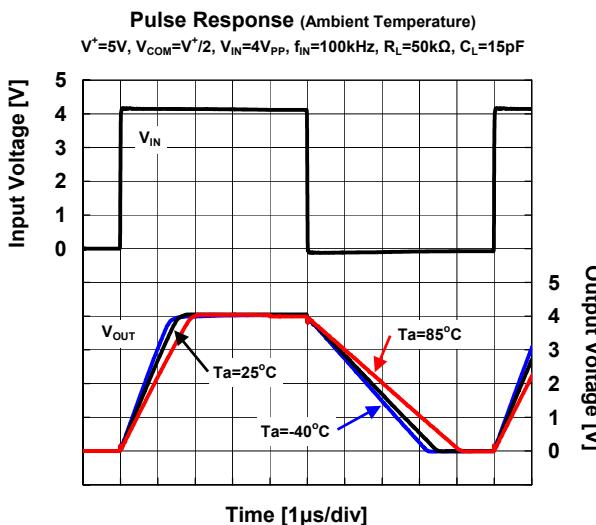
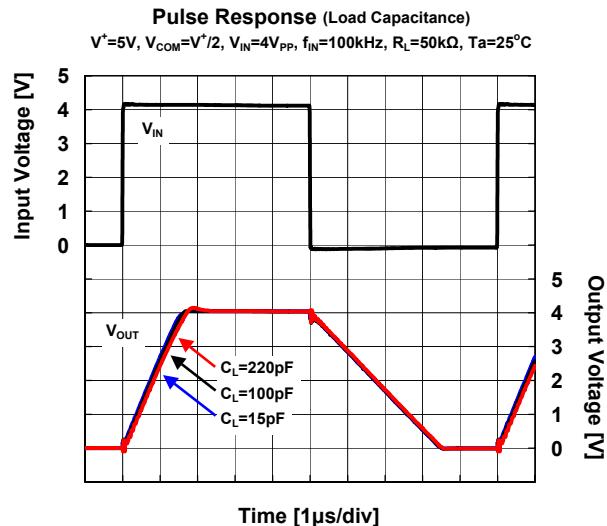
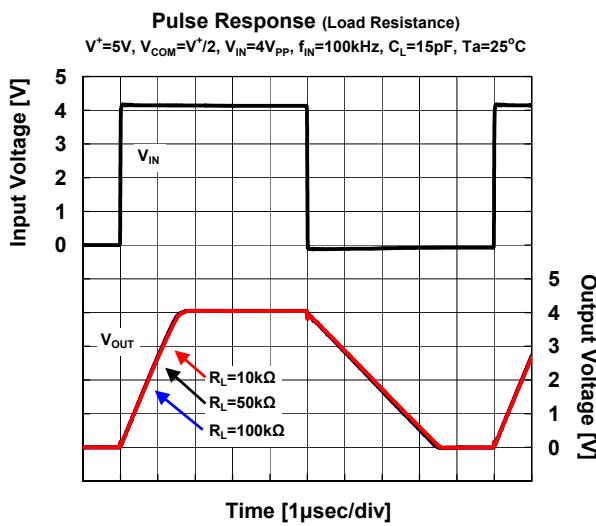
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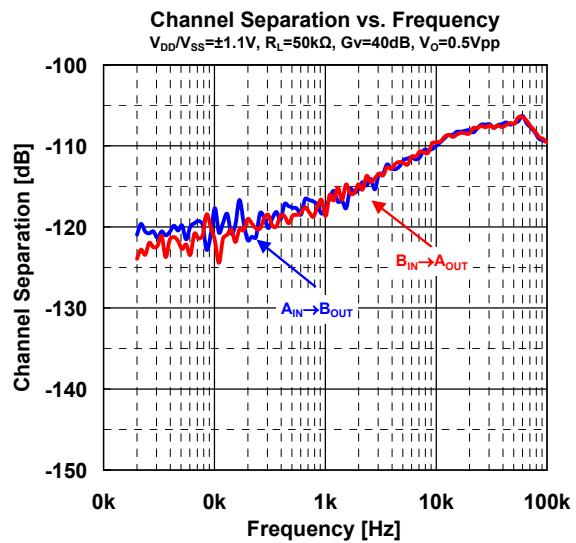
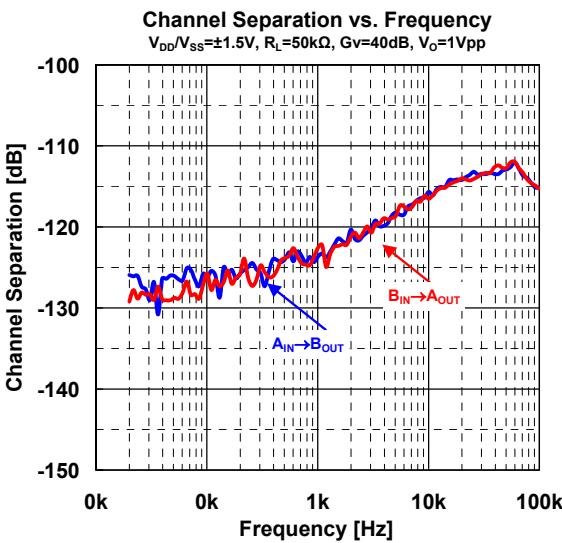
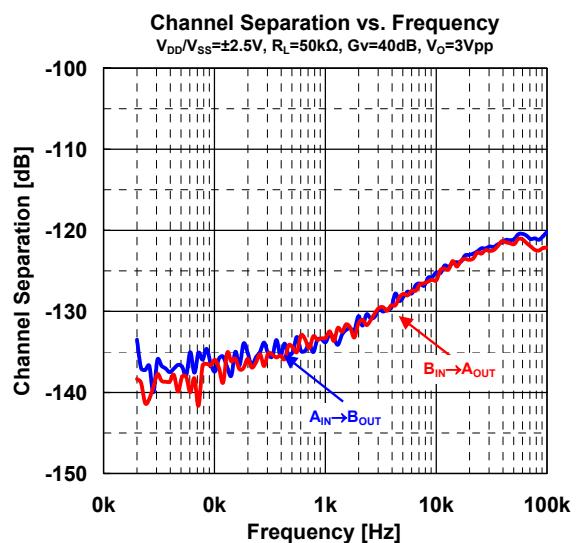
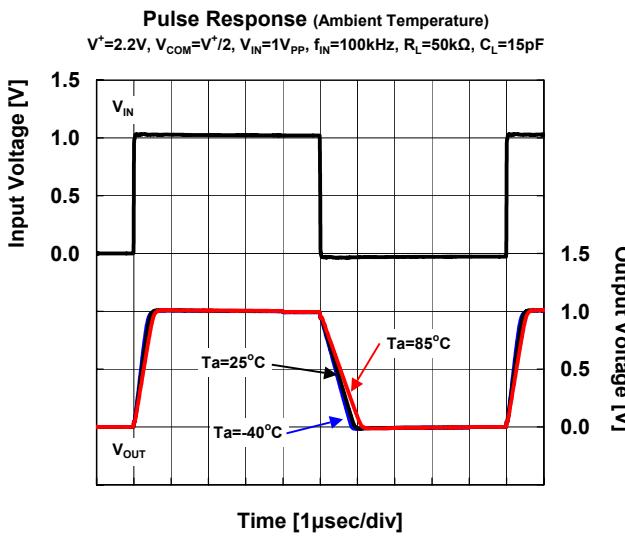
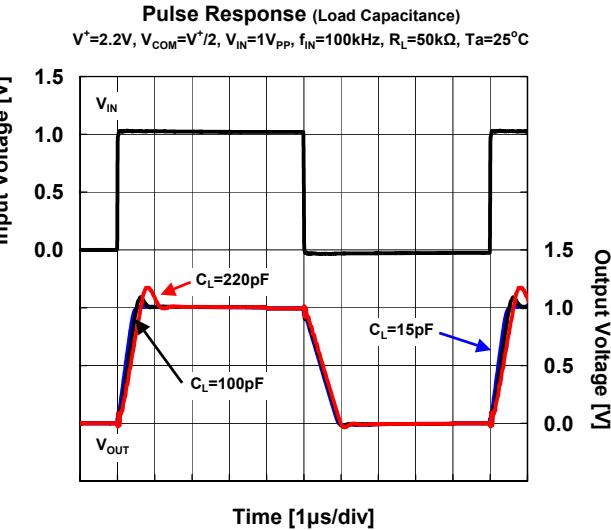
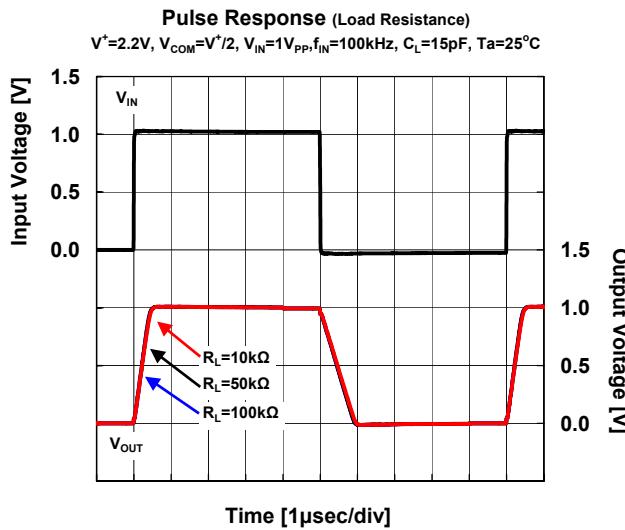
## ■ TYPICAL CHARACTERISTICS



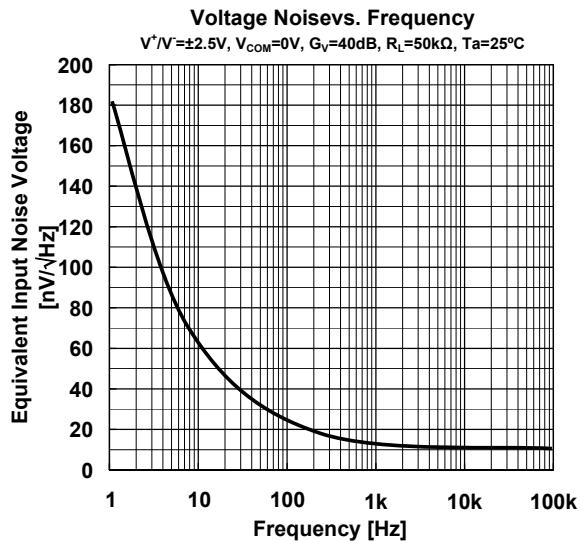
## ■ TYPICAL CHARACTERISTICS



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## ■ TYPICAL CHARACTERISTICS



[CAUTION]  
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