

Silicon PNP Power Transistors

2SB609

DESCRIPTION

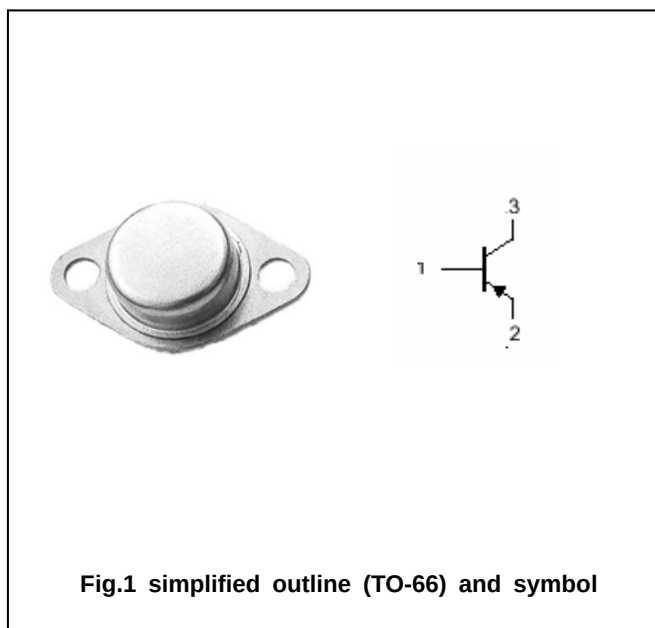
- With TO-66 package
- Wide area of safe operation

APPLICATIONS

- For use in audio frequency power amplifier application

PINNING(see Fig.2)

PIN	DESCRIPTION
1	Base
2	Emitter
3	Collector

Absolute maximum ratings($T_a = ^\circ\text{C}$)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V_{CBO}	Collector-base voltage	Open emitter	-100	V
V_{CEO}	Collector-emitter voltage	Open base	-80	V
V_{EBO}	Emitter-base voltage	Open collector	-5	V
I_C	Collector current		-4	A
P_C	Collector power dissipation	$T_C = 25^\circ\text{C}$	40	W
T_j	Junction temperature		150	$^\circ\text{C}$
T_{stg}	Storage temperature		-40~150	$^\circ\text{C}$

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CHARACTERISTICS

T_j=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-emitter breakdown voltage	I _C =-10mA ; I _B =0	-80			V
V _{(BR)CBO}	Collector-base breakdown voltage	I _C =-1mA ; I _E =0	-100			V
V _{(BR)EBO}	Emitter-base breakdown voltage	I _E =-1mA ; I _C =0	-5			V
V _{CEsat}	Collector-emitter saturation voltage	I _C =-2A; I _B =-0.2A			-1.0	V
V _{BE}	Base-emitter on voltage	I _C =-0.5A ; V _{CE} =-4V			-1.5	V
I _{CBO}	Collector cut-off current	V _{CB} =-80V; I _E =0			-0.1	mA
I _{EBO}	Emitter cut-off current	V _{EB} =-4V; I _C =0			-0.1	mA
h _{FE}	DC current gain	I _C =-0.5A ; V _{CE} =-4V	60		320	

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PACKAGE OUTLINE

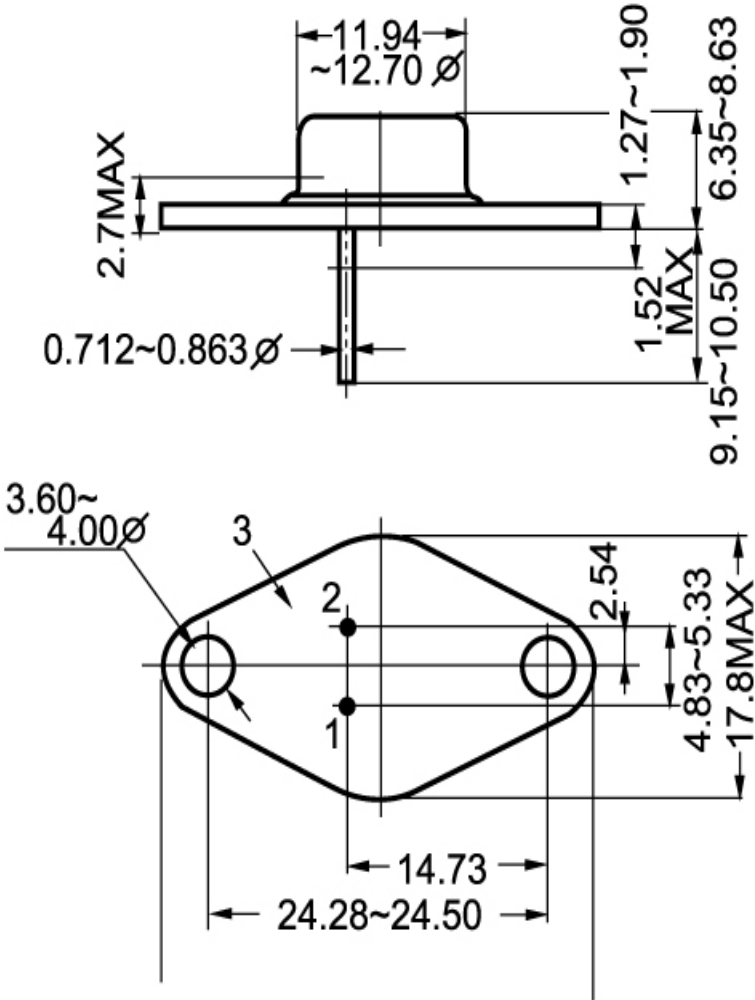


Fig.2 outline dimensions