

FEATURES

- For surface mounted applications
- Low profile package
- Built-in strain relief
- Easy pick and place
- Superfast recovery times for high efficiency
- Plastic package has Underwriters Laboratory Flammability Classification 94V-O
- Glass passivated junction
- High temperature soldering:
260 °C/10 seconds at terminals

MECHANICAL DATA

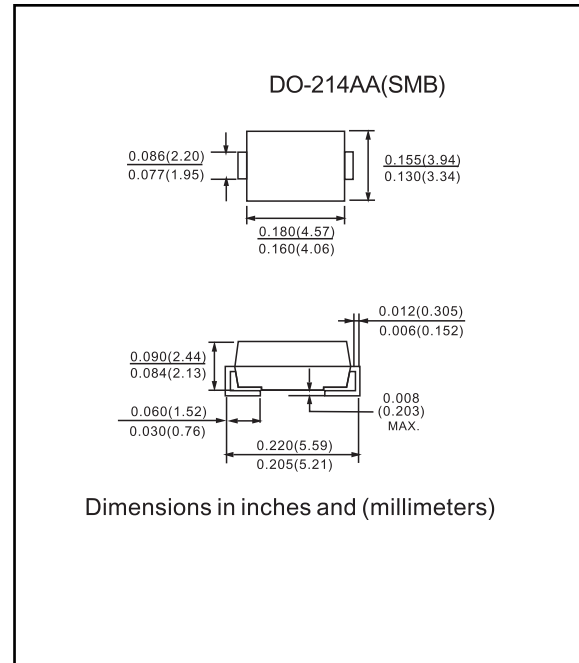
Case: JEDEC DO-214AA molded plastic

Terminals: Solder plated, solderable per
MIL-STD-750, Method 2026

Polarity: Indicated by cathode band

Standard packaging: 12mm tape (EIA-481)

Weight: 0.003 ounce, 0.093 gram



Maximum Ratings and Electrical Characteristics @T_A=25°C unless otherwise specified

Characteristic	Symbol	ER2A	ER2B	ER2C	ER2D	ER2E	ER2G	ER2J	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _{RWM} V _R	50	100	150	200	300	400	600	V
RMS Reverse Voltage	V _{R(RMS)}	35	70	105	140	210	280	420	V
Average Rectified Output Current @T _L = 110°C	I _O	2.0							A
Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load (JEDEC Method)	I _{FSM}	50							A
Forward Voltage @I _F = 2.0A	V _{FM}	0.95				1.25		1.7	V
Peak Reverse Current @T _A = 25°C At Rated DC Blocking Voltage @T _A = 100°C	I _{RM}	5.0 500							μA
Reverse Recovery Time (Note 1)	t _{rr}	35							nS
Typical Junction Capacitance (Note 2)	C _j	25							pF
Typical Thermal Resistance (Note 3)	R _{θ JL}	20							K/W
Operating and Storage Temperature Range	T _j , T _{STG}	-65 to +150							°C

Note: 1. Measured with I_F = 0.5A, I_R = 1.0A, I_{rr} = 0.25A,
2. Measured at 1.0 MHz and applied reverse voltage of 4.0 V DC.
3. Mounted on P.C. Board with 8.0mm² land area.

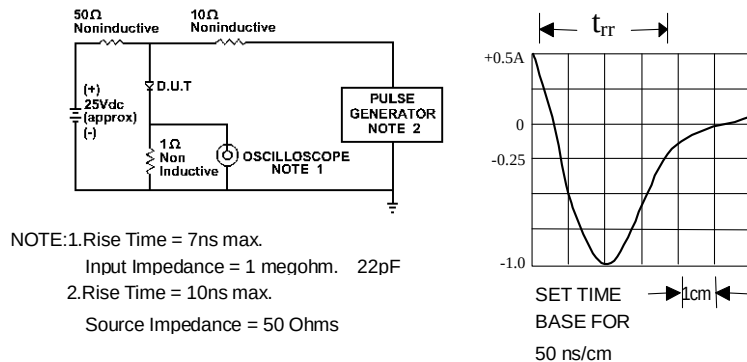


Fig. 1-REVERSE RECOVERY TIME CHARACTERISTIC AND TEST CIRCUIT DIAGRAM

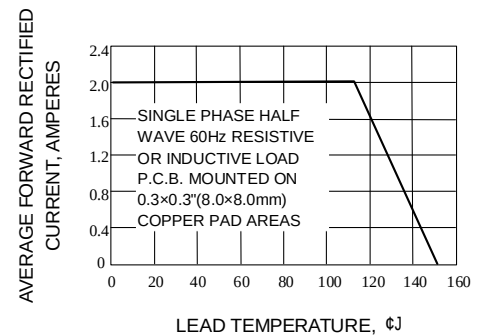


Fig. 2-MAXIMUM AVERAGE FORWARD CURRENT RATING

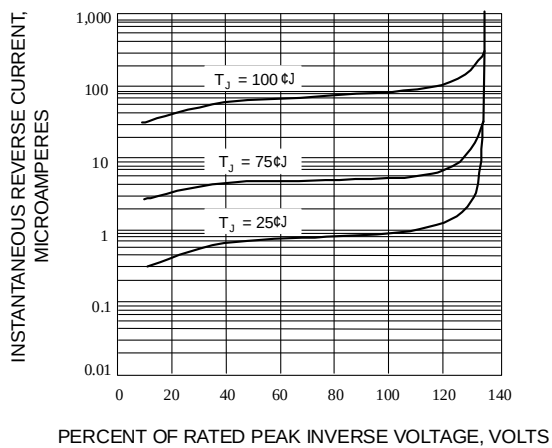


Fig. 3-TYPICAL REVERSE CHARACTERISTICS

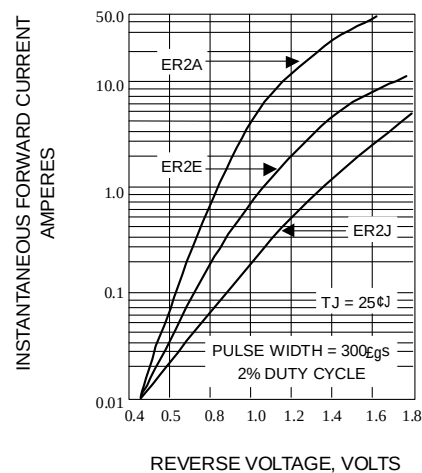


Fig. 4-TYPICAL FORWARD CHARACTERISTICS

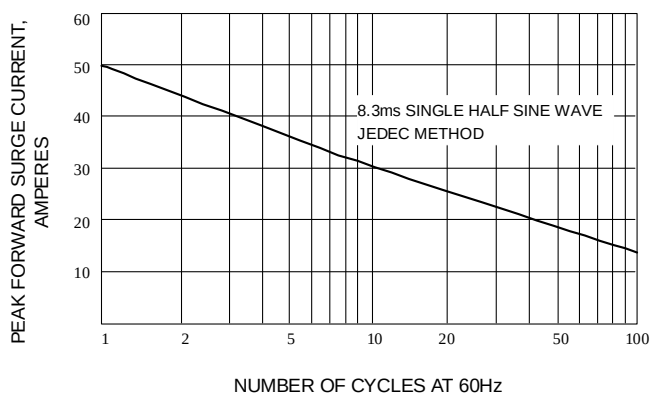


Fig. 5-MAXIMUM NON-REPETITIVE PEAK FORWARD SURGE CURRENT

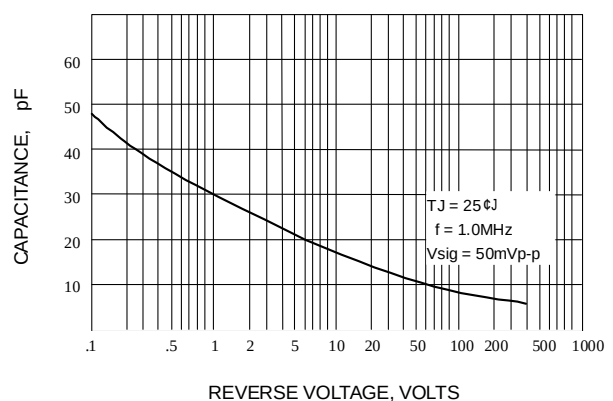


Fig. 6-TYPICAL JUNCTION CAPACITANCE