

Silicon NPN Power Transistors

2SC4981

DESCRIPTION

- With ITO-220 package
- Switching power transistor
- Low collector saturation voltage

PINNING

PIN	DESCRIPTION
1	Base
2	Collector
3	Emitter

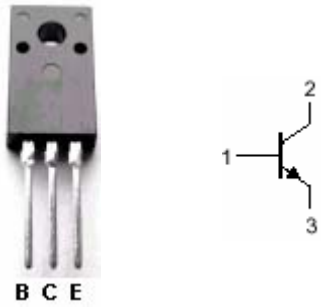


Fig.1 simplified outline (ITO-220) and symbol

Absolute maximum ratings(Ta=25)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V _{CBO}	Collector-base voltage	Open emitter	100	V
V _{CEO}	Collector-emitter voltage	Open base	80	V
V _{EBO}	Emitter-base voltage	Open collector	7	V
I _C	Collector current		7	A
I _{CM}	Collector current-Peak		14	A
I _B	Base current		1.5	A
I _{BM}	Base current-peak		2	A
P _T	Total power dissipation	T _C =25	25	W
T _j	Junction temperature		150	
T _{stg}	Storage temperature		-55~150	

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
R _{th j-C}	Thermal resistance junction case	5.0	/W

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CHARACTERISTICS

Tj=25 unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
$V_{CE(SUS)}$	Collector-emitter sustaining voltage	$I_C=0.1A ; I_B=0$	80			V
V_{CEsat}	Collector-emitter saturation voltage	$I_C=3.5A ; I_B=0.2A$			0.3	V
V_{BEsat}	Base-emitter saturation voltage	$I_C=3.5A ; I_B=0.2A$			1.2	V
I_{CBO}	Collector cut-off current	At rated voltage			0.1	mA
I_{CEO}	Collector cut-off current					
I_{EBO}	Emitter cut-off current	At rated voltage			0.1	mA
h_{FE}	DC current gain	$I_C=3.5A ; V_{CE}=2V$	70			
f_T	Transition frequency	$I_C=0.7A ; V_{CE}=10V$		50		MHz
Switching times						
t_{on}	Turn-on time	$I_C=3.5A ; I_{B1}=0.35A$ $I_{B2}=0.35A , R_L=8\Omega$ $V_{BB2}=4V$			0.3	μs
t_s	Storage time				1.5	μs
t_f	Fall time				0.2	μs

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PACKAGE OUTLINE

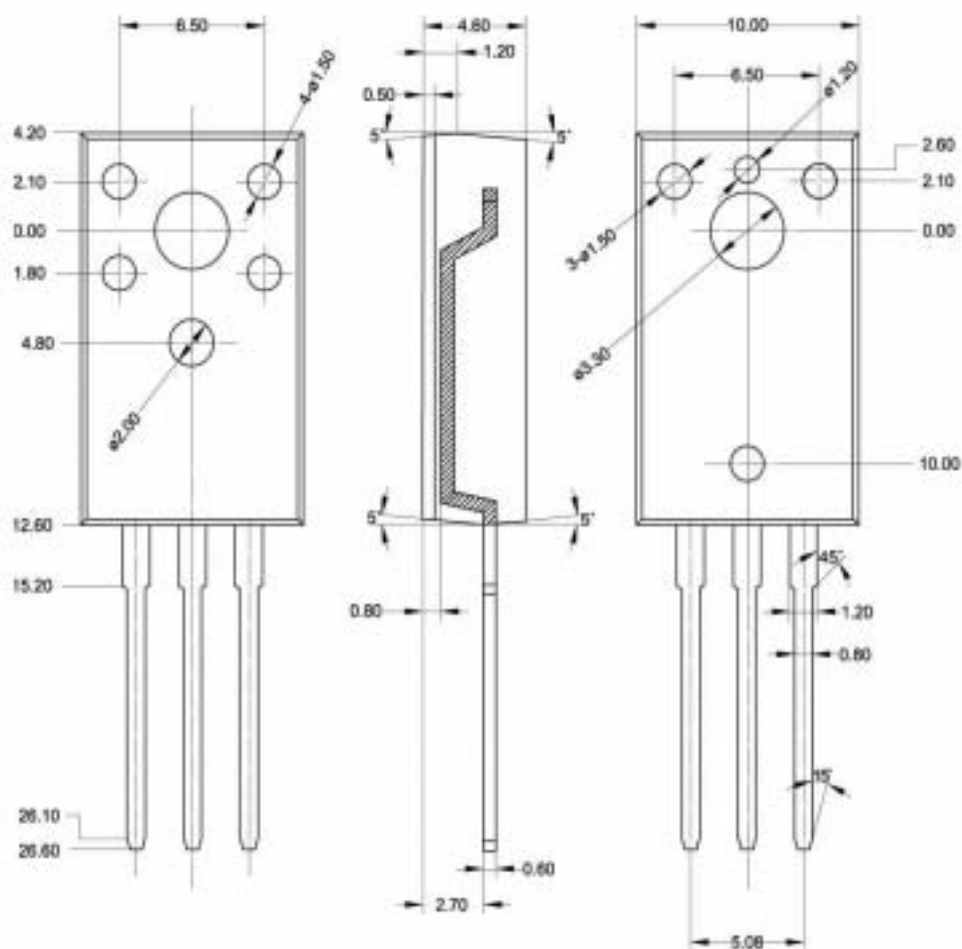


Fig.2 Outline dimensions (unindicated tolerance: ± 0.20 mm)