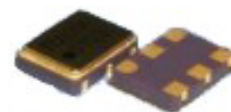


7 x 5 x 1.8mm SMD VCXO

- Frequency range 60MHz to 240MHz
- LVPECL Output
- Supply Voltage 3.3 VDC
- Phase jitter 0.2ps typical
- Pull range from $\pm 30\text{ppm}$ to $\pm 150\text{ppm}$



DESCRIPTION

GPA576 VCXOs are packaged in a 6 pad 7mm x 5mm SMD package. Typical phase jitter for GPA series VCXOs is 0.2 ps. Output is LVPECL. Applications include phase lock loop, SONET/ATM, set-top boxes, MPEG, audio/video modulation, video game consoles and HDTV.

SPECIFICATION

Frequency Range:	60.0MHz to 240.0MHz
Supply Voltage:	3.3 VDC $\pm 5\%$
Output Logic:	LVPECL
RMS Period Jitter	
60.0MHz ~ 120MHz:	2.5ps typical
120MHz ~ 240MHz:	4.7ps typical
Peak to Peak Jitter	
60.0MHz ~ 120MHz:	17.5ps typical
120MHz ~ 240MHz:	24.5ps typical
Phase Jitter:	0.2ps typical
Initial Frequency Accuracy:	Tune to the nominal frequency with $V_c = 1.65 \pm 0.2\text{VDC}$
Output Voltage HIGH (1):	$V_{dd} - 1.025\text{V}$ minimum $V_{dd} - 0.880\text{V}$ maximum
Output Voltage LOW (0):	$V_{dd} - 1.810\text{V}$ minimum $V_{dd} - 1.620\text{V}$ maximum ($R_L = 50\Omega$ to $V_{dd} - 2\text{V}$)
Pulling Range:	From $\pm 30\text{ppm}$ to $\pm 150\text{ppm}$
Control Voltage Range:	1.65 ± 0.35 Volts
Temperature Stability:	See table
Output Load:	50Ω into V_{dd} or Thevenin equiv.
Rise/Fall Times:	0.5ns typ., 0.7ns max. 20% V_{dd} to 80% V_{dd}
Duty Cycle:	50% $\pm 5\%$ (Measured at $V_{dd} - 1.3\text{V}$)
Start-up Time:	10ms maximum, 5ms typical
Current Consumption:	75mA maximum at 212.5MHz 80mA maximum at 622.08MHz
Static Discharge Protection:	2kV maximum
Storage Temperature:	-55° to $+150^\circ\text{C}$
Ageing:	$\pm 2\text{ppm}$ per year maximum
Enable/Disable:	See table
RoHS Status:	Fully compliant

FREQUENCY STABILITY

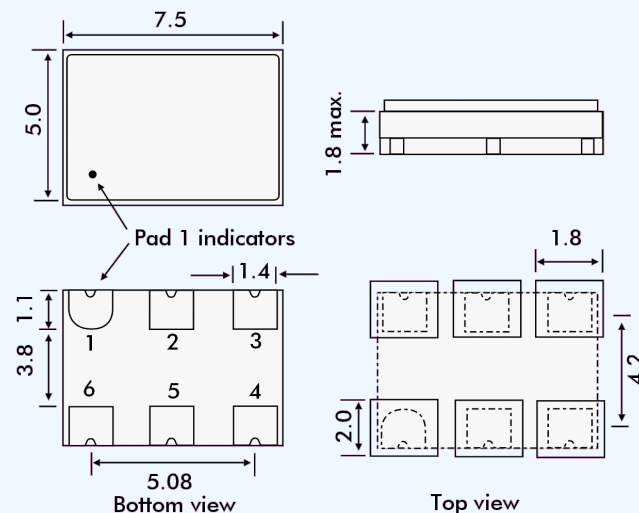
Stability Code	Stability $\pm\text{ppm}$	Temp. Range
A	25	$0^\circ \sim +70^\circ\text{C}$
B	50	$0^\circ \sim +70^\circ\text{C}$
C	100	$0^\circ \sim +70^\circ\text{C}$
D	25	$-40^\circ \sim +85^\circ\text{C}$
E	50	$-40^\circ \sim +85^\circ\text{C}$
F	100	$-40^\circ \sim +85^\circ\text{C}$

If non-standard frequency stability is required
Use 'I' followed by stability, i.e. I20 for $\pm 20\text{ppm}$

ENABLE/DISABLE FUNCTION

Tristate Pad Status	Output Status
Not connected Below 0.3V _{dd} (Ref. to ground)	LVPECL and Complimentary LVPECL enabled Both outputs are disabled (high impedance)
Above 0.7V _{dd} (Ref. to ground)	Both outputs are enabled

OUTLINE & DIMENSIONS



Pad Connections

- 1 Voltage control
- 2 Enable/Disable (Tristate)
- 3 Ground
- 4 Output
- 5 Complimentary Output
- 6 V_{cc}

PART NUMBERING

Example: 3GPA576B-80N-60.000

