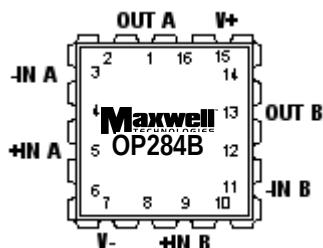
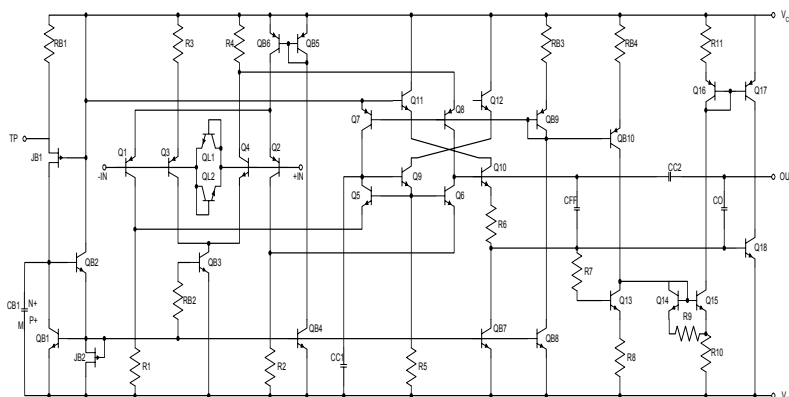


Flat Pack



Leadless Chip
Carrier (LCC)



Logic Diagram

FEATURES:

- RAD-PAK® radiation-hardened against natural space radiation
- Package:
 - 8-pin RAD-PAK® flat pack
 - 16-pin RAD-PAK® Leadless Chip Carrier (LCC) pack
- Single-Supply Operation
- Wide Bandwidth: 4 MHz
- Unity-Gain Stable
- High Slew Rate: 4.0 V/us
- Low Noise: 3.9 nV/(Hz)^{1/2}

DESCRIPTION:

Maxwell Technologies' OP284B is a dual single-supply, 4 MHz bandwidth amplifier featuring rail-to-rail inputs and outputs. Using Maxwell Technologies' radiation-hardened RAD-PAK® technology, it is guaranteed to operate from +3 to +36 (or ± 1.5 to ± 18) volts and will function with a single supply as low as +1.5 volts.

This amplifier is superb for single supply applications requiring both ac and precision dc performance. The combination of bandwidth, low noise and precision makes the OP284B useful in a wide variety of applications, including filters and instrumentation. Other applications for this amplifier include portable telecom equipment, power supply control and protection, and as an amplifier or buffer for transducers with wide output ranges.

The ability to swing rail-to-rail at both the input and output enables designers to build multi-stage filters in single-supply systems and to maintain high signal-to-noise ratios.

Maxwell Technologies' patented RAD-PAK® packaging technology incorporates radiation shielding in the microcircuit package. It eliminates the need for box shielding while providing the radiation shielding for a space mission. This product is available with screening up to Class S.

TABLE 1. FLAT PACK PINOUT DESCRIPTION

PIN	SYMBOL	DESCRIPTION
1, 7	OUT A, B	Outputs
2, 6	-IN A, B	Negative Inputs
3, 5	+IN A, B	Positive Inputs
4	V-	Ground
8	V+	Supply Voltage

TABLE 2. LEADLESS CHIP CARRIER (LCC) PINOUT DESCRIPTION

PIN	SYMBOL	DESCRIPTION
1, 13	OUT A, B	Outputs
3, 11	-IN A, B	Negative Inputs
5, 9	+IN A, B	Positive Inputs
7	V-	Ground
15	V+	Supply Voltage

TABLE 3. OP284A ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	MIN	MAX	UNIT
Supply Voltage	V_{CC}	--	± 18	V
Input Voltage	V_I	--	± 18	V
Differential Input Voltage ¹	V_{DIF}	--	± 0.6	V
Output Short-Circuit Duration to GND		Indefinite		
Storage Temperature Range	T_S	-65	+150	°C
Operating Temperature Range	T_A	-40	+125	°C
Junction Temperature Range	T_J	-65	+150	°C
Package Weight			0.75	Grams
Thermal Impedance	Θ_{JC}	--	6.48	°C/W

1. For input voltages greater than 0.6 volts, the input current should be limited to less 5 mA to prevent degradation or destruction of the input devices.

TABLE 4. DELTA LIMITS

PARAMETER	VARIATION
I_{CC}	$\pm 10\%$ of specified value in Table 5

TABLE 5. OP284B ELECTRICAL CHARACTERISTICS

(V₊ = 15V, V₋ = -15V, V_{OUT} = 0V, R_S = 50Ω, R_L = 100kΩ, V_{CM} = 0V, T_A = -40°C TO +125°C UNLESS OTHERWISE SPECIFIED.)

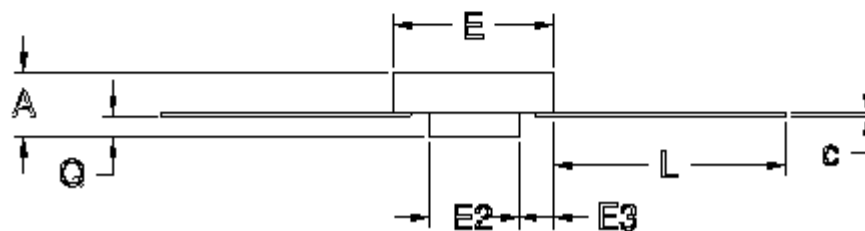
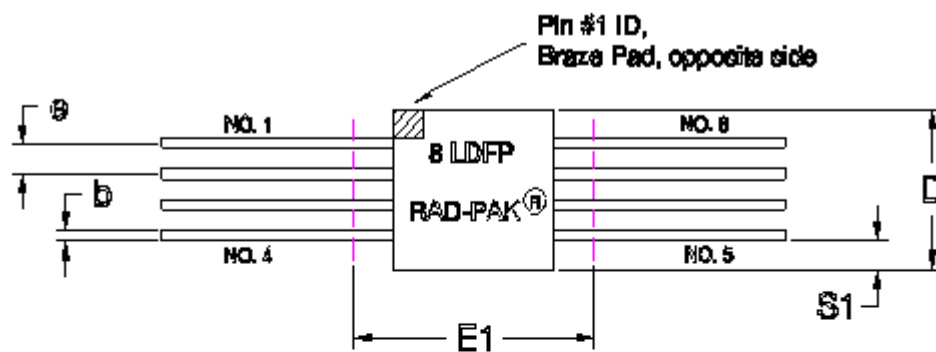
PARAMETER	SYMBOL	TEST CONDITIONS	SUBGROUPS	MIN	TYP	MAX	UNIT
Input offset voltage	V _{IO}	+25°C	1	-175	--	175	μV
		-40 to +125°C	2, 3	-375	--	375	
Average offset voltage drift ¹	V _{IO} /TC	R _L = 2kΩ	1, 2, 3	--	--	2	μV/°C
Offset voltage match ¹	ΔV _{IO}	V _{IO} (max) - V _{IO} (min) , +25°C	1	-	--	100	μV
		V _{IO} (max) - V _{IO} (min) , -40 to +125°C	2, 3	-	--	200	
Input bias current	I _B	R _S = 50Ω, +25°C	1	-350	--	350	nA
		R _S = 50Ω, -40 to +125°C	2, 3	-575	--	575	nA
Input offset current	I _{OS}	R _S = 50Ω, +25°C	1	-35	--	35	nA
		R _S = 50Ω, -40 to +125°C	2, 3	-50	--	50	nA
Input bias current drift ¹	I _{IBTC}	R _S = 50Ω	1, 2, 3	--	--	200	pA/°C
Input voltage range	I _{VR}		1, 2, 3	-15	--	15	V
Common mode rejection ratio	CMRR	V _{CM} = -14V to +14V	4,5,6	86	--	--	dB
		V _{CM} = -15V to +15V, T _A = 25°C	4	80	--	--	dB
Output current	+I _O	V _{OUT} = -10V, 1ms pulse, T _A = 25°C	1	10	--	--	mA
		V _{OUT} = -10V, 1ms pulse	2, 3	5	--	--	
	-I _O	V _{OUT} = +10V, 1ms pulse, T _A = 25°C	1	--	--	-10	mA
		V _{OUT} = -10V, 1ms pulse	2, 3	--	--	-5	
Quiescent power supply current/amplifier	+I _{CC}	I _{OUT} = 0mA V _S = ±18V, T _A = 25°C	1	--	--	1.80	mA
		I _{OUT} = 0mA V _S = ±18V	2, 3	--	--	2.25	
	-I _{CC}	I _{OUT} = 0mA V _S = ±18V, T _A = 25°C	1	-1.80	--	--	mA
		I _{OUT} = 0mA V _S = ±18V	2, 3	2.25	--	--	
Power supply rejection ratio	+PSRR	V _S = ±2V to ±18V	1, 2, 3	90	--	--	dB
	-PSRR	V _S = ±2V to ±18V	1, 2, 3	90	--	--	dB
Low frequency, peak-to-peak noise ¹	E _{np-p}	0.1Hz to 10Hz, R _L = 2kΩ, C _L = 50pF, T _A = +25°C	1	--	0.30	--	μV _{p-p}
Input noise voltage density ¹	E _n	R _S = 20Ω, f _O = 1kHz, R _L = 2kΩ, C _L = 50pF, T _A = +25°C	1	--	3.9	--	nV/(Hz) ^{1/2}
Input noise current density ¹	I _n	R _S = 20MΩ, f _O = 1kHz, R _L = 2kΩ, C _L = 50pF, T _A = +25°C	1	--	0.4	--	pA/(Hz) ^{1/2}
Power consumption ^{1,2}	P _C	+10V, I _{OUT} = 0mA, R _L = 2kΩ	1, 2, 3	--	--	81	mW
Large signal voltage gain	+A _{VOL}	+10V, R _L = 2.0kΩ, T _A = 25°C	4	--	--	150	V/mV
		+10V, R _L = 2.0kΩ	5,6	--	--	75	
	-A _{VOL}	-10V, R _L = 2.0kΩ, T _A = 25°C	4	--	--	150	V/mV
		-10V, R _L = 2.0kΩ	5,6	--	--	75	

TABLE 5. OP284B ELECTRICAL CHARACTERISTICS

(V+ = 15V, V- = -15V, V_{OUT} = 0V, R_S = 50Ω, R_L = 100kΩ, V_{CM} = 0V, T_A = -40°C TO +125°C UNLESS OTHERWISE SPECIFIED.)

PARAMETER	SYMBOL	TEST CONDITIONS	SUBGROUPS	MIN	TYP	MAX	UNIT
Output voltage swing	+V _{OUT1}	I _L =1.0mA	4,5,6	14.8	--	--	V
	-V _{OUT1}	I _L =1.0mA	4,5,6	--	--	-14.8	V
Gain bandwidth product ¹	GBWP	V _O =200mV, R _L =2.0kΩ, C _L =50pF, T _A =+25°C	4	--	4.25	--	MHz
Full power bandwidth ^{1,3}	FPBW	V _{PEAK} =29vp-p, R _L =2.0kΩ, C _L =50pF, T _A =+25°C, 1% distortion	4	--	35	--	kHz
Settling time	t _s	A _V =+1.0, C _L =50pF, R _L =2.0kΩ To 0.01%, 10V step	9	--	--	5	μs
Phase margin	F _O	A _V =1.0, C _L =50pF, R _L =2.0kΩ, T _A =+25°C	4	--	--	50	Degrees
Slew rate	+SR	C _L =50pF, R _L =2.0kΩ	4,5,6	2.4	4.0	--	V/μs
	-SR	C _L =50pF, R _L =2.0kΩ	4,5,6	2.4	4.0	--	V/μs

1. Guaranteed by design.
2. Power consumption is based upon quiescent supply current maximum test limit. No load on outputs.
3. Full power bandwidth is based on slew rate measurement using FBBW = slew rate/(2V_{PEAK}).

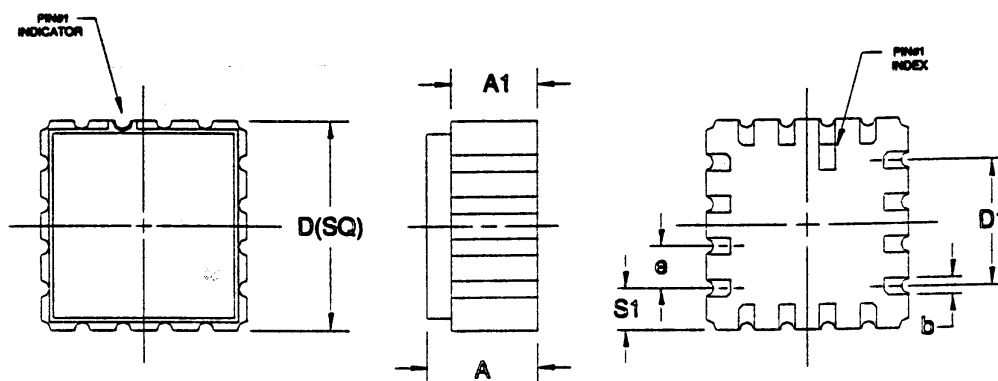


8-PIN RAD-PAK® FLAT PACKAGE

SYMBOL	DIMENSION		
	MIN	NOM	MAX
A	0.119	0.132	0.149
b	0.010	0.017	0.022
c	0.004	0.005	0.009
D	0.250	0.255	0.260
E	0.250	0.255	0.260
E1	--	--	0.290
E2	0.125	0.145	--
E3	0.030	0.040	--
e	0.050 BSC		
L	0.338	0.348	0.358
Q	0.021	0.025	0.045
S1	0.005	0.019	--
N	8		

F8-01

Note: All dimensions in inches



16-PIN RAD-PAK® LCC PACKAGE

SYMBOL	DIMENSION		
	MIN	NOM	MAX
A	0.123	0.135	0.150
b	0.018	0.020	0.025
D	0.245	0.250	0.260
D1	0.150 BSC		
S1	0.050 BSC		
e	0.050 BSC		
A1	0.095	0.105	0.116
N	16		

LC16-01

Note: All dimensions in inches.

Important Notice:

These data sheets are created using the chip manufacturer's published specifications. Maxwell Technologies verifies functionality by testing key parameters either by 100% testing, sample testing or characterization.

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Product Ordering Options

