

PARALLEL TO SDI CONVERTER CARD WITH OPTIONAL EDH INSERTION

PI-001

INPUT Parallel multiplexed component signals to CCIR Rec 656.

Note. To ease interfacing into equipment data is latched by the rising edge of the clock.

Data Set up time 3 ns min to positive edge of clock

Data hold time 2.5 ns min from positive edge of clock

OUTPUT Serial digital 143,177, 270 & 360Mb/s NRZI

No of Outputs: 3 off serial

Output Impedance: 75 Ohms.

Output return loss: >15dB 5 MHz to 270 MHz.

EDH OPTION

Indications: EDH - Error detected Here (Note 1.)

EDA - Error detected already (Note 1.)

UES - Unknown Error status (Note 1.)

Controls: Transmit/Receive Mode: (Note 2.)

Bypass Mode: (Note 2.)

Note 1. These are accessible only through the serial interface SCL and SDA.

Note 2. These are available only through the I/O interface see connector for details.

Mechanical Details

The following formats are available:

Format A STD configuration with low profile pins and BNC connectors for outputs.

Format B LOW profile pins and MCX connectors for outputs.

Format C STAND-OFF configuration with BNC outputs.

Connector Pinout

1 +5V	2 CLK	3 D0	4 D1	5 D2	6 D3	7 D4	8 D5	9 D6	10 D7	11 D8	12 D9
13 +5V	14 +5V	15 A0	16 A1	17 SDA	18 SCL	19 RESET	20 R \bar{T} EDH	21 BYPASS EDH	22 GND	23 GND	24 GND
25 NC	26 NC	27 NC	28 NC	29 NC	30 NC	31 NC	32 NC	33 NC	34 NC	35 NC	36 NC
37 NC	38 NC	39 NC	40 NC	41 NC	42 NC	43 NC	44 NC	45 NC	46 ID0	47 ID1	48 ID2

D0 to D9

Parallel Data Bus D0 LSB , D9 MSB

CLK

Parallel Data clock

A0.A1

Address lines to modify I²c bus address.

SCL,SDA

I²c data and clock lines

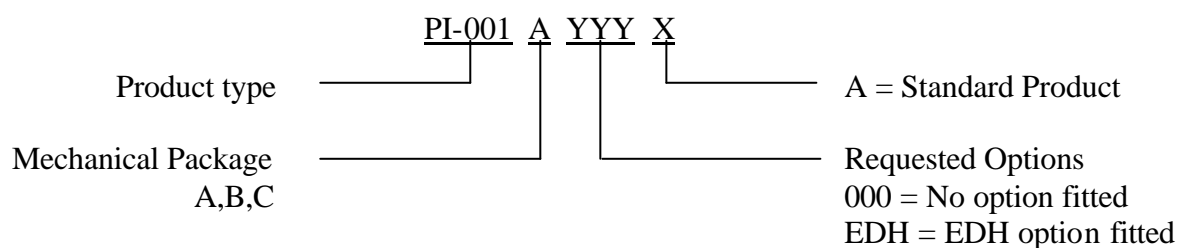
ID0,ID1,ID2

Card type identification code.

Card Type Identification code.

CARD TYPE	ID2	ID1	ID0
PI-001	0V	0V	+5V

Ordering codes



Note: This item is **NOT** aqueous washable.

PACKAGE DETAIL

