

AGING @ +25 °C

PACKAGE, SEAL & LEAD FINISH

X200 SERIES (CMOS)

STANDARD SPECIFICATIONS

(Similar to M55310/18)

FREQUENCY RANGE 1.5 Hz to 12 MHz FREQUENCY ACCURACY @ + 25 °C ± 0.0015% (± 15 PPM) 0.200" Max. FREQUECY STABILITY Vs. TEMPERATURE See Options Below 0.250" OPERATING TEMPERATURE RANGE See Options Below 0.020" INPUT VOLTAGE (See note below) + 5 VDC to + 15 VDC <u>+</u> 10% Pin 1 is identified with a Sq. Corner 0.439" Max. INPUT CURRENT 5 mA Max. @ + 5 VDC 0.254" 1 0 0 0 0 0 0 0 25 mA Max. @ + 15 VDC 0.507" Max Max. **OUTPUT CMOS** 1400000008 LOAD 200 K Ω in parallel with 50 pf .0.305<u>"</u> 0.295" 60/40% @ 50% Output Leve SYMMETRY 0.605 Max **RISE & FALL TIMES** 150 nS Max. @ + 5 VDC (10% to 90% Output Level) 50 nS Max. @ + 15 VDC Pin Connections START-UP TIME 14 B+ 20 mS Max. < 10 MHz 7 **GND** 8 OUTPUT 15 mS Max. ≥ 10 MHz All Others N/C FREQUENCY STABILITY Vs. VOLTAGE ± 0.0005% (± 5 PPM) Max. (for 10% change in Voltage)

Note: Input Voltage must be specified for 200 Series CMOS parts, minimum input voltage required depends upon

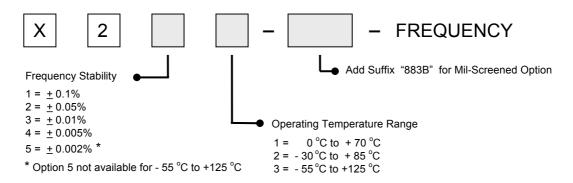
output frequency and operating temperature range. Consult factory for your specific application.

Contact Xsis Engineering for special requirements such as, Output Symmetry, Start-up Time, Frequency Accuracy, Complementary Outputs, Multiple Outputs, etc.

± 0.0005% (± 5 PPM)/year Max.

Conforms with the Requirements of MIL-PRF-55310

ORDERING INFORMATION (Select from options below):



EXAMPLE: X243 - 883B - 4.000 MHz = 14 Pin Package with "X" Pinout, CMOS, \pm 0.005% over -55 °C to +125 °C, Mil-Screened, and 20.000 MHz