Vishay Foil Resistors



Bulk Metal[®] Foil Technology Surface Mount Hermetic Resistor Networks in Gull Wing Configuration



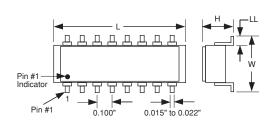
Vishay Model VSM networks incorporate all the performance features of Vishay Bulk Metal[®] Foil technology in a product ready for surface mounting. The 8, 14 and 16 pin side brazed DIPs are a ceramic package. Ceramic has the advantage of electrical isolation on the underside, and, in DIP form, a favorable pin arrangement when two networks are to be placed side by side and connected together.

Review data sheet "7 technical reasons to specify Bulk Metal Foil resistor networks."

ORDERING INFORMATION - VSM40, VSM42, VSM45 OR VSM46 NETWORKS

Networks are built to your requirements. Send your schematic and electrical requirements to the Applications Engineering Department. (See data sheet "Network Worksheet.") A unique part number will be assigned which defines all aspects of your network.

FIGURE 1 - PACKAGE SIZES AND CHARACTERISTICS



NOTE:

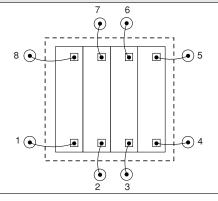
- These networks utilize Vishay Bulk Metal[®] Foil resistor chips V5X5 and V15X5 or VTF15X5 Thin Film chips.
- The V5X5 and V15X5 chips have maximum resistance values of 10K and 33K respectively in Bulk Metal[®] Foil and 500K in VTF15X5 Thin Film chips
- 3. The V5X5 and V15X5 chip(s) can be intermixed in a package.

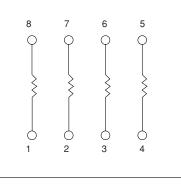
VISHAY MODEL	NO. OF PINS	MAXIMUM DIMENSIONS in Inches (mm)				CHIP CAPACITY		MAXIMUM POWER RATING
		L	W	Н	LL	V5X5	V15X5	(WATTS) @ +70°C
VSM40	8	0.405 (10.29)	0.470 (11.938)	0.180 (4.572)	0.090 (2.286)	12	4	0.4
VSM42	8	0.540 (13.716)	0.470 (11.938)	0.180 (4.572)	0.090 (2.286)	12	4	0.4
VSM45	14	0.785 (19.914)	0.470 (11.938)	0.180 (4.572)	0.090 (2.286)	30	10	1.2
VSM46	16	0.810 (20.574)	0.470 (11.938)	0.180 (4.572)	0.090 (2.286)	36	12	1.4

FIGURE 2 - SAMPLE CIRCUIT DESIGN AND CHIP LAYOUT

NOTE:

Usable area is represented by the dotted lines— a rectangle 0.150 Inches x 0.200 Inches. Illustrations not to scale. Chips shown undersize for clarity. Drawing view is from the top looking down into the package.





SALES

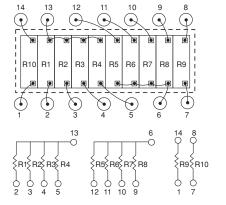
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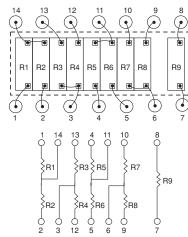
Bulk Metal[®] Foil Technology Vishay Foil Resistors Surface Mount Hermetic Resistor Networks in Gull Wing Configuration

FIGURE 3 - SAMPLE CIRCUIT DESIGNS AND CHIP LAYOUTS

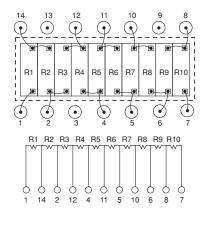
NOTE: Usable area is represented by the dotted lines— a rectangle 0.150" x 0.500". Illustrations not to scale. Chips shown undersize for clarity. Drawing view is from the top looking down into the package.



TWO DECADES OF BCD LADDER PLUS TWO SCALING RESISTORS



FOUR DIVIDERS PLUS APPLICATION RESISTOR



TEN RESISTOR DIVIDER

FIGURE 4 - SAMPLE CIRCUIT DESIGNS AND CHIP LAYOUTS

NOTE: Usable area is represented by the dotted lines— a rectangle 0.150" x 0.600". Illustrations not to scale. Chips shown undersize for clarity. Drawing view is from the top looking down into the package.

ELEVEN RESISTOR DIVIDER

EIGHT RESISTOR

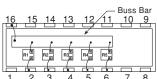
PACKAGE

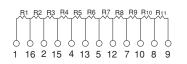


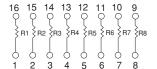
16 15 14 13 12 11 10

R2 R3 R4 R5 R6 R7 R8 R9 R10 R1

FIVE RESISTOR CURRENT SUMMING NETWORK







SALES

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Vishay

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