

3.3V, 8-Channel, 2:1 Mux/DeMux Gigabit Ethernet LAN Switch with Single Enable and Enhanced ESD Protection

Features

- R_{ON} is 4Ω typical
- Low bit-to-bit skew: 200ps
- Low Crosstalk: -22dB @ 250MHz
- Near-Zero propagation delay: 250ps
- Switching speed: 9ns
- Channel On capacitance: 10pF (typical)
- Status Indicator LEDs Switched with Ethernet pairs
- V_{CC} Operating Range: +3.0V to +3.6V
- Enhanced ESD Protection:
 - 8kV (contact)⁽¹⁾
 - Switch pins: 1.1kV (machine model),
12kV (human body model)
- >550 MHz bandwidth (or data frequency)
- Packaging:
 - Pb-free and Green, 56-contact TQFN (ZF)

Applications

- Dual Physical Layer Device sharing to one interface connector
- Routes signals for 10/100/1000 Mbit Ethernet

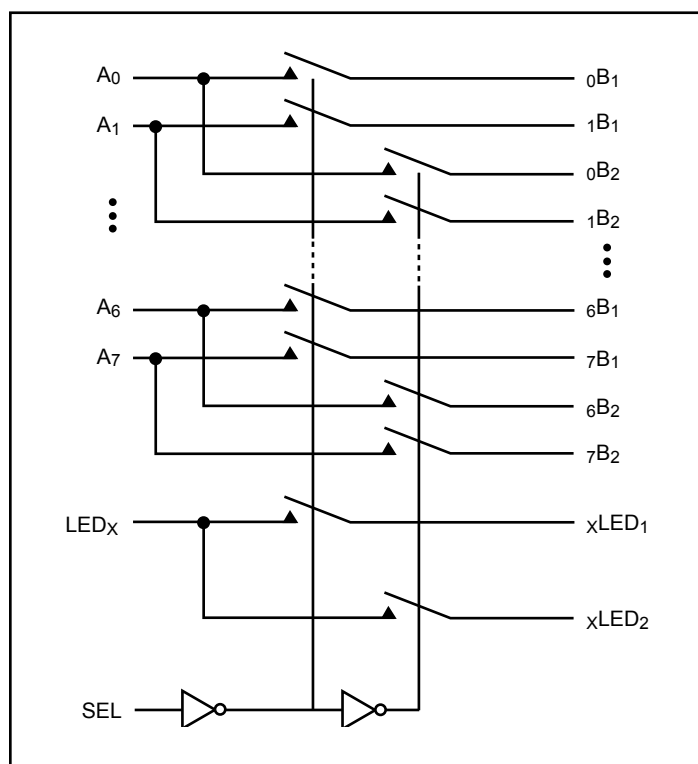
Description

The PI3L500-A is a 8-Channel 2:1 multiplexer/demultiplexer LAN Switch with Hi-Z outputs. Industry leading advantages include a propagation delay of less than 250ps, resulting from its low channel resistance and I/O capacitance. The device multiplexes differential outputs from a Gigabit Ethernet transceiver (PHY) device to one of two corresponding B1 or B2 outputs. The switch is bidirectional and offers little or no attenuation of the high-speed signals at the outputs. It is designed for low bit-to-bit skew, high channel-to-channel noise isolation and is compatible with various standards, such as 10/100/1000 Base-T (Ethernet).

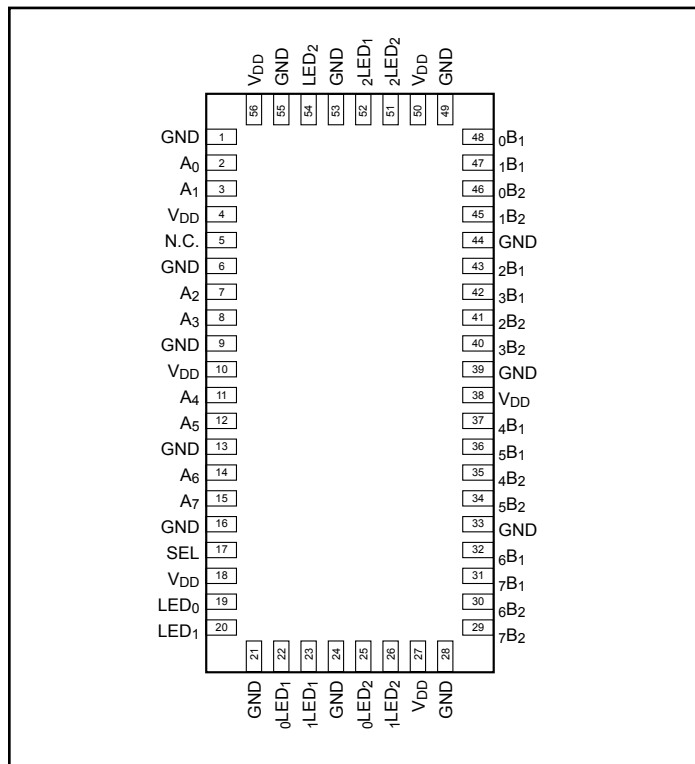
Generally, this part can be used to replace mechanical relays in low-voltage LAN applications that interface a physical layer over CAT 5 or CAT 6 unshielded twisted pair cable through an isolation transformer.

Note: 1. IEC 61000-4-2

Block Diagram



Pin Description



Pin Description

Pin Name	Description
A _X	DeMux Inputs
xBy	Mux Inputs
LED _Z	LED Demux Inputs
zLED _Y	LED Mux Inputs
GND	Ground
V _{CC}	Power
SEL	Select

Truth Table

SEL	Function
L	A _X to xB ₁ ; LED _Z to zLED ₁
H	A _X to xB ₂ ; LED _Z to zLED ₂

Maximum Ratings

(Above which useful life may be impaired. For user guidelines, not tested.)

Storage Temperature.....	–65°C to +150°C
Supply Voltage to Ground Potential	–0.5V to +4.0V
DC Input Voltage.....	–0.5V to +5.5V
DC Output Current	120mA
Power Dissipation	0.5W

Note:

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

DC Electrical Characteristics for 1000 Base-T Ethernet Switching over Operating Range
 $(T_A = -40^{\circ}\text{C to } +85^{\circ}\text{C}, V_{CC} = 3.3\text{V} \pm 10\%)$

Parameter	Description	Test Conditions ⁽¹⁾	Min.	Typ. ⁽²⁾	Max.	Units
V_{IH}	Input HIGH Voltage	Guaranteed HIGH level (Control Pins)	2.0	–	–	V
V_{IL}	Input LOW Voltage	Guaranteed LOW level (Control Pins)	–0.5	–	0.8	
V_{IK}	Clamp Diode Voltage	$V_{CC} = \text{Max.}, I_{IN} = -18\text{mA}$	–	–0.7	–1.2	
I_{IH}	Input HIGH Current	$V_{CC} = \text{Max.}, V_{IN} = V_{CC}$	–	–	± 5	μA
I_{IL}	Input LOW Current	$V_{CC} = \text{Max.}, V_{IN} = \text{GND}$	–	–	± 5	
R_{ON}	Switch On-Resistance ⁽³⁾	$V_{CC} = \text{Min.}, 1.5\text{V} \leq V_{IN} \leq V_{CC}$ $I_{IN} = -40\text{mA}$	–	4.0	6.5	Ω
$R_{FLAT(ON)}$	On-Resistance Flatness ⁽³⁾	$V_{CC} = \text{Min.}, V_{IN} @ 1.5\text{V and } V_{CC}$ $I_{IN} = -40\text{mA}$	–	0.5	–	
ΔR_{ON}	On-Resistance match from center ports to any other port ⁽³⁾	$V_{CC} = \text{Min.}, 1.5\text{V} \leq V_{IN} \leq V_{CC}$ $I_{IN} = -40\text{mA}$	–	0.4	1.0	

DC Electrical Characteristics for 10/100 Base-T Ethernet Switching over Operating Range
 $(T_A = -40^{\circ}\text{C to } +85^{\circ}\text{C}, V_{CC} = 3.3\text{V} \pm 10\%)$

Parameter	Description	Test Conditions ⁽¹⁾	Min.	Typ. ⁽²⁾	Max.	Units
V_{IH}	Input HIGH Voltage	Guaranteed HIGH level (Control Pins)	2.0	–	–	V
V_{IL}	Input LOW Voltage	Guaranteed LOW level (Control Pins)	–0.5	–	0.8	
V_{IK}	Clamp Diode Voltage	$V_{CC} = \text{Max.}, I_{IN} = -18\text{mA}$	–	–0.7	–1.2	
I_{IH}	Input HIGH Current	$V_{CC} = \text{Max.}, V_{IN} = V_{CC}$	–	–	± 5	μA
I_{IL}	Input LOW Current	$V_{CC} = \text{Max.}, V_{IN} = \text{GND}$	–	–	± 5	
R_{ON}	Switch On-Resistance ⁽³⁾	$V_{CC} = \text{Min.}, 1.25\text{V} \leq V_{IN} \leq V_{CC}$ $I_{IN} = -10\text{mA to } -30\text{mA}$	–	4.0	6.5	Ω
$R_{FLAT(ON)}$	On-Resistance Flatness ⁽³⁾	$V_{CC} = \text{Min.}, V_{IN} @ 1.25\text{V and } V_{CC}$ $I_{IN} = -10\text{mA to } -30\text{mA}$	–	0.5	–	
ΔR_{ON}	On-Resistance match from center ports to any other port ⁽³⁾	$V_{CC} = \text{Min.}, 1.25\text{V} \leq V_{IN} \leq V_{CC}$ $I_{IN} = -10\text{mA to } -30\text{mA}$	–	0.4	1.0	

Capacitance ($T_A = 25^{\circ}\text{C}, f = 1\text{MHz}$)

Parameters ⁽⁴⁾	Description	Test Conditions ⁽¹⁾	Typ.	Max.	Units
C_{IN}	Input Capacitance	$V_{IN} = 0\text{V}$	2.0	3.0	pF
$C_{OFF(B1, B2)}$	Port B Capacitance, Switch OFF		4.0	6.0	
$C_{ON(A/B)}$	A/B Capacitance, Switch ON		10.0	11.0	

Notes:

- For max. or min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
- Typical values are at $V_{CC} = 3.3\text{V}$, $T_A = 25^{\circ}\text{C}$ ambient and maximum loading.
- Measured by the voltage drop between A and B pins at indicated current through the switch. ON resistance is determined by the lower of the voltages on the two (A & B) pins.
- This parameter is determined by device characterization but is not production tested.

Power Supply Characteristics

Parameters	Description	Test Conditions ⁽¹⁾	Min.	Typ. ⁽²⁾	Max.	Units
I _{CC}	Quiescent Power Supply Current	V _{CC} = Max., V _{IN} = GND or V _{CC}	—	0.5	3.5	mA

Dynamic Electrical Characteristics Over the Operating Range (T_A = -40°C to +85°C, V_{CC} = 3.3V ±10%, GND = 0V)

Parameter	Description	Test Conditions ⁽¹⁾	Min.	Typ. ⁽²⁾	Max.	Units
X _{TALK}	Crosstalk ⁽³⁾	R _L = 100Ω, f = 250MHz	—	-27	—	dB
O _{IRR}	OFF Isolation ⁽³⁾		—	-32	—	
BW	Bandwidth -3dB ⁽³⁾	R _L = 100Ω	—	350	—	MHz

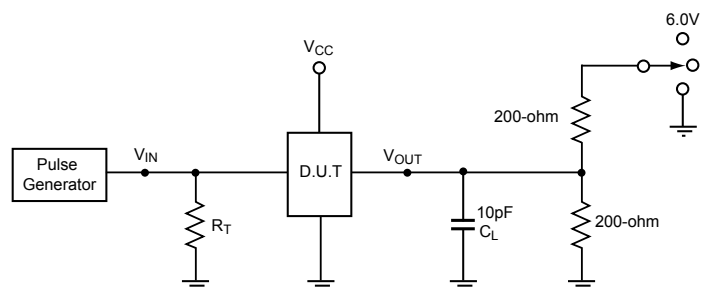
Switching Characteristics

Parameter	Description	Min.	Typ. ⁽²⁾	Max.	Units
t _{PD}	Propagation Delay ^(3,4)	—	0.25		ns
t _{PZH} , t _{PZL}	Line Enable Time - SEL to A _N , B _N	0.5	—	15.0	
t _{PHZ} , t _{PLZ}	Line Disable Time - SEL to A _N , B _N	0.5	—	9.0	
t _{SK(o)}	Output Skew between center port (A ₄ to A ₅) to any other port ⁽³⁾	—	0.1	0.2	
t _{SK(p)}	Skew between opposite transitions of the same output (t _{PHL} - t _{PLH}) ⁽³⁾	—	0.1	0.2	

Notes:

- For max. or min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
- Typical values are at V_{CC} = 3.3V, T_A = 25°C ambient and maximum loading..
- Guaranteed by design.
- The bus switch contributes no propagational delay other than the RC delay of the ON resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.25ns for 10pF load. Since this time constant is much smaller than the rise/fall times of typical driving signals, it adds very little propagational delay to the system. Propagational delay of the bus switch when used in a system is determined by the driving circuit on the driving side of the switch and its interactions with the load on the driven side.

Test Circuit for Electrical Characteristics



Switch Positions

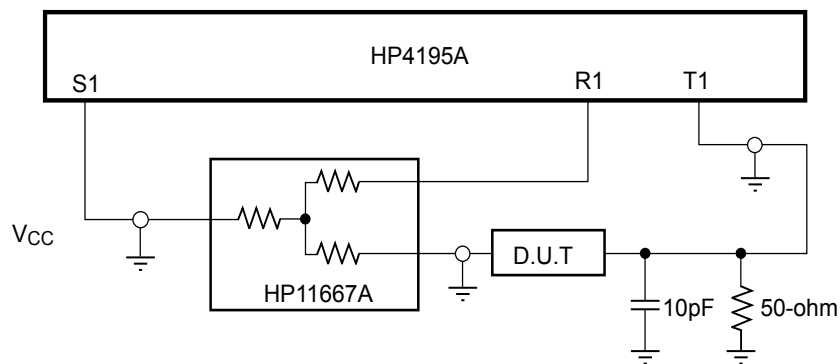
Test	Switch
t_{PLZ} , t_{PZL} (output on B-side)	6.0V
t_{PHZ} , t_{PZH} (output on B-side)	GND
Prop Delay	Open

Notes:

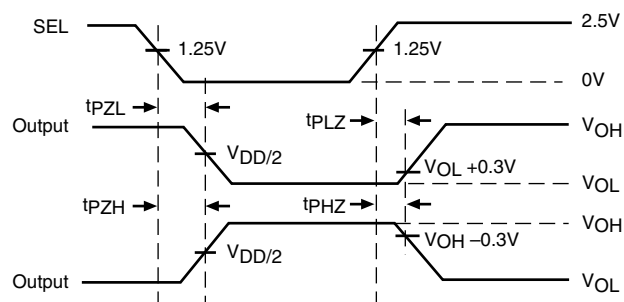
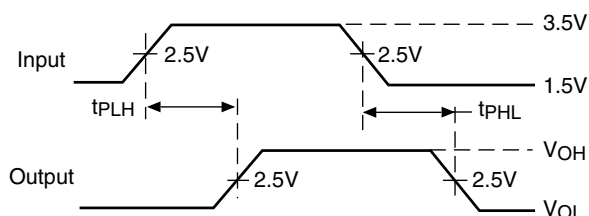
C_L = Load capacitance: includes jig and probe capacitance.

R_T = Termination resistance: should be equal to Z_{OUT} of the Pulse Generator

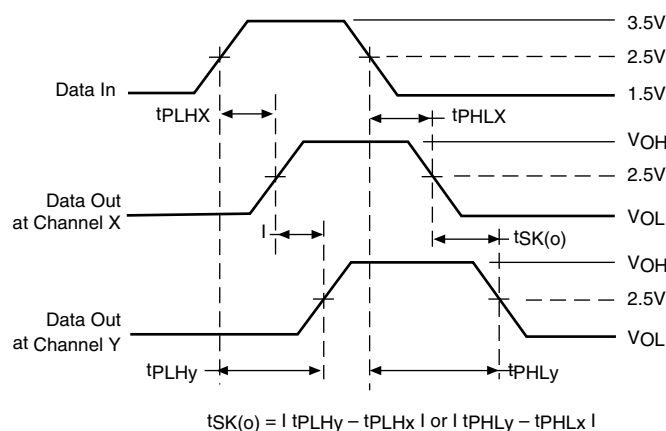
Test Circuit for Dynamic Electrical Characteristics



Switching Waveforms

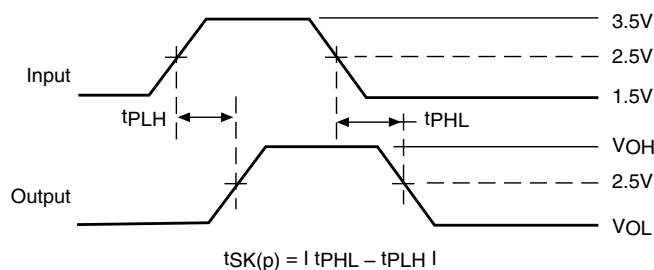


Voltage Waveforms Propagation Delay Times



Output Skew - $t_{SK(o)}$

Voltage Waveforms Enable and Disable Times



Pulse Skew - $t_{SK(p)}$

Notes:

Waveform 1 is for an output with internal conditions such that the output is low except when disabled by the output control.

Waveform 2 is for an output with internal conditions such that the output is high except when disabled by the output control.

All input impulses are supplied by generators having the following characteristics: $PRR \leq \text{MHz}$, $Z_O = 50\Omega$, $t_R \leq 2.5\text{ns}$, $t_F \leq 2.5\text{ns}$.

Applications Information

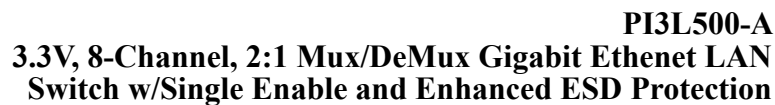
Logic Inputs

The logic control inputs can be driven up to +3.6V regardless of the supply voltage. For example, given a +3.3V supply, the output enables or select pins may be driven low to 0V and high to 3.6V. Driving IN Rail-to-Rail® minimizes power consumption.

Power-Supply Sequencing

Proper power-supply sequencing is advised for all CMOS devices. It is recommended to always apply V_{CC} before applying signals to the input/output or control pins.

Rail-to-Rail is a registered trademark of Nippon Motorola, Ltd



The drawing illustrates the mechanical specifications of a 56-pin DIP package. It includes three views: a top view, a side view, and a detail view of the pin array.

Top View Dimensions:

- Overall width: 11.00 ± 0.10
- Overall height: 5.00 ± 0.10
- Pin #1 Corner is indicated by a dot and an arrow.

Side View Dimensions:

- Pin height: $0.0-0.05$
- Pin pitch: 0.20 REF.
- Maximum pin length: 0.80 MAX.

Detail View Dimensions (Pin Array):

- Overall width of pin array: 9.50 REF.
- Overall height of pin array: 3.50 REF.
- Inner width of pin array: 8.40 ± 0.10
- Inner height of pin array: 2.40 ± 0.10
- Pin #1 Corner is indicated by a dot and an arrow.
- Pin pitch: 0.50 TYP. (56x)
- Pin width: 0.25 ± 0.05
- Pin height: 0.40 ± 0.10

Ordering Number	Package Code	Package Description
PI3L500-AZFE	ZF	Pb-free & Green, 56-contact TQFN

Thermal characteristics can be found on the company web site at www.pericom.com/packaging/
Add 'X' for Tape and Reel