



SM531-1 and PT532-1 ADSL Line Transformers



Features

- For use with Analog Devices AD20msp918 ADSL Chipset
- Excellent Total Harmonic Distortion (THD) performance in a small size
- Both SMD and Thru-Hole Package Version
- Designed to meet UL1950 and EN60950 Supplementary Insulation requirements for voltages up to 250Vrms maximum

ELECTRICAL SPECIFICATIONS AT 25°C: OPERATING TEMP: 0°C TO 70°C

TURNS RATIO	1 : 1.27±2%
OCL @10kHz, 0.1V	
(1-4) WITH (2-3) SHORTED	1.80mH MIN
(10-7) WITH (8-9) SHORTED	2.90mH MIN
LEAKAGE INDUCTANCE @100kHz 0.1V	
(1-4) WITH (2-3), (8-9), (10-7) SHORTED	7.5μH MAX
LONGITUDINAL BALANCE 30kHz – 1.1MHz	50dB MIN
INSERTION LOSS @100kHz	<0.5dB
ISOLATION VOLTAGE (MAX)	1500 Vrms
LOAD RESISTANCE	100Ω
DC RESISTANCE (1-4), (10-7)	2Ω ΜΑΧ
THD LINEARITY @30kHz	-80dB TYP
PACKAGE	SMT

SMT THT **OUTLINE DIMENSIONS** $10 \times \frac{.028 \pm .002}{0,71 \pm 0,05}$ 1 605 15,37 DATATRONIC 695 7.65 MAX 100 Date Code 2,54 4 □ .530 DATATRONIC 13,46 3□ 8 .098 10,00 ₉ 2,50 MAX 2 .500 12.70MAX 1 = $10 \times \frac{.051 \pm .002}{1,30 \pm 0,05}$.098 2,50 .516 13.11MAX→ $.173 \pm .015$ 4.39 ± 0.38 .475 10 X $\square \frac{.018}{0.46}$.485 12,32 12,07 _<u>.394</u> 10.00 MAX MAX .005/0,13 10 SURFACES 10 0 10 NOTES: Dimensions are shown in inches/millimeters Unless otherwise specified, tolerance = Inches: ±0.010 3 0 08 = Millimeters: ±0,25 09 For non-RoHS parts replace SM prefix with 42- (e.g. SM531-1 becomes 42-531-1) 2. 4 0 07 Terminal finish is compliant to RoHS requirements. Solder in accordance with J-STD-020C.

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