

3.3V PECL VCXO WITH DIFFERENTIAL OUTPUT & ENABLE/DISABLE

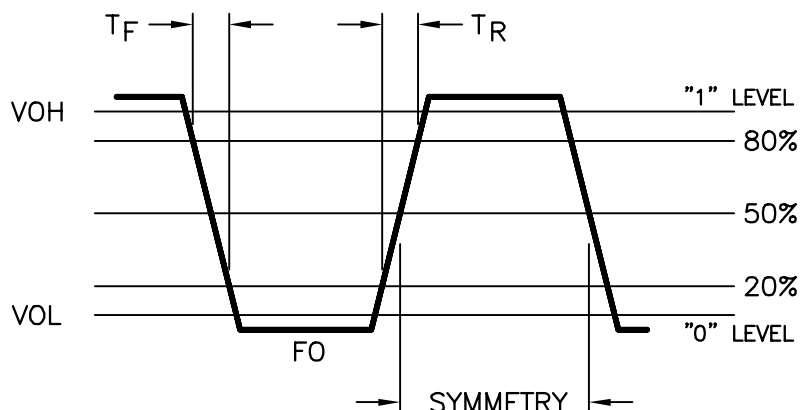
SPECIFICATIONS	PVL54I90
Frequency Range (Fo)	86.097902MHz , 155.52MHz , 166.6285MHz
Frequency Stability (1.65Vdc Pin 1)	
Vs Temperature	±10ppm Typical
Vs Calibration @ 25°C	±5ppm Typical
Vs Aging (10yrs)	±5ppm
Total	±20ppm Maximum (Reference to Fo @ 1.65Vdc pin 1)
Temperature Range	0°C to +70°C
Waveform	Low Voltage PECL Squarewave
Load	50 Ohms Terminated into Vcc-2V or Thevenin Equivalent
Voltage Voh	2.26 Minimum
Output Vol	1.70 Maximum
Duty Cycle	45/55 Maximum (Both outputs)
Rise/Fall Time	1.0nS Maximum
RMS Jitter	1.0ps RMS Maximum over 12KHz to 20MHz bandwidth 5.0ps RMS Maximum over 10Hz to 20MHz bandwidth
SSB Phase Noise (typical)	-80dBc/Hz @ 100Hz , -130dBc/Hz @ 10KHz
Frequency Control	
Control Voltage	0.3 to +3.0Vdc
Pullability @25°C (Referenced to 1.65V freq.)	±45ppm Minimum ±90ppm Maximum
Absolute Pull Range	±25ppm Minimum
Slope	Positive
Linearity	±10% Maximum
Freq Modulation Bandwidth	≥ 10KHz (3dB cutoff)
Enable/Disable	Enable; Low Level (Vil) , Disable; High Level (Vih) NOTE: When oscillator is disabled the output Q is in a low state (Vol), Output Q̄ is in a high state (Voh)
Supply Voltage	+3.3Vdc ±5%
Supply Current	80mA Maximum
Package	All metal, hermetically sealed, welded package

ORDERING INFORMATION

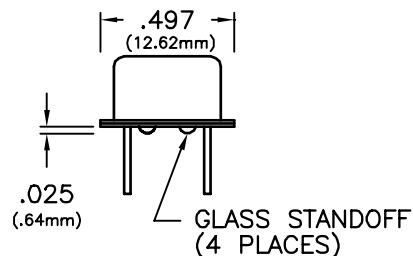
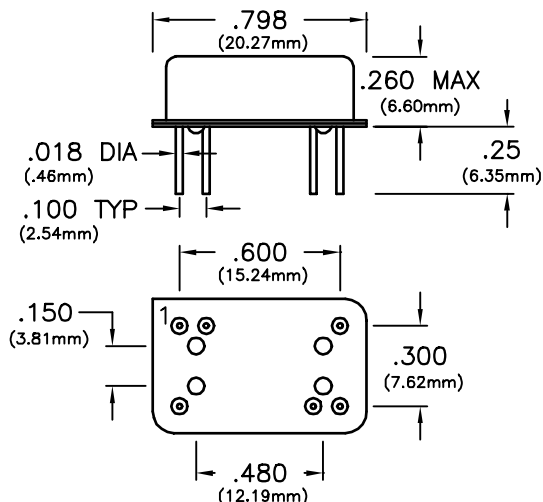
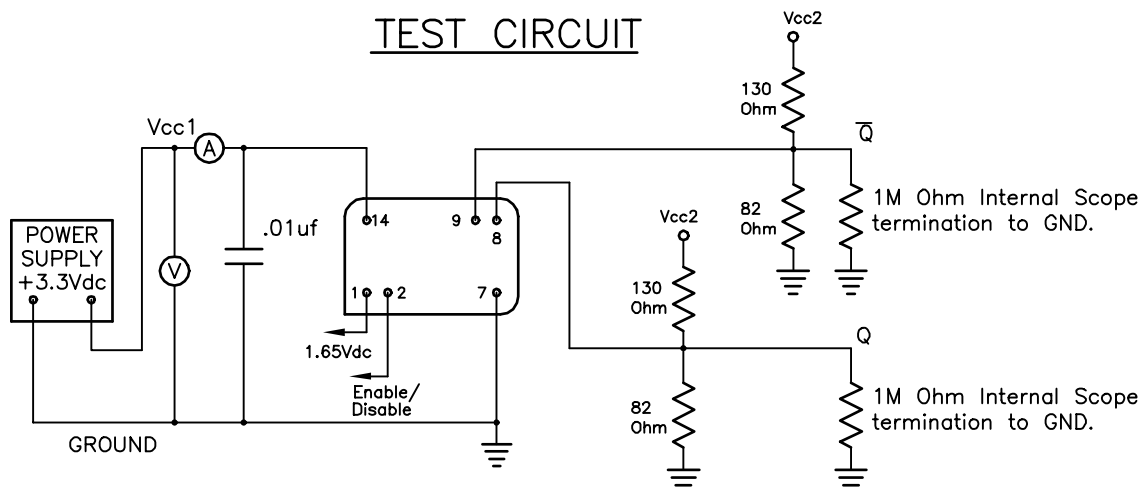
P V L 5 4 I 9 0 - 1 5 5 . 5 2 M H z
 VCXO SERIES CENTER FREQUENCY

14 PIN VCXO OSCILLATOR

OUTPUT WAVEFORM



TEST CIRCUIT



PIN	CONNECTION
1	CONTROL VOLTAGE
2	ENABLE/DISABLE
7	GROUND (CASE)
8	OUTPUT Q
9	COMP OUTPUT $\bar{Q}$
14	(+) SUPPLY