





Typical unit

FEATURES

- 264 Watts total output power
- 96% Ultra-high effi ciency @ full load
- 48V Input (40.5 to 57V range)
- 12V/22A Output for Intermediate Bus Architectures with POL converters
- Input Over/Under Voltage Shutdown
- Synchronous-rectifier topology
- 150kHz fi xed switching frequency
- Output current sharing
- Fully isolated, 2250V (BASIC)
- Low 80mVp-p ripple/noise
- Standard quarter-brick package
- Stable no-load condition
- Thermal shutdown
- Fully I/O protected
- IEC/EN/UL/cUL60950-1 certified

The QBC-12/22-L48 DC/DC converter is one of DATEL's new generation, fully isolated, Intermediate Bus Converters, designed and optimized for total on-board solutions in combination with our non-isolated point of load converters of the HEN, LEN, LQN, LSM and LSN series.

PRODUCT OVERVIEW

The QBC's convert the standard 48V (40.5 to 57V limited range) to a nonregulated 12V (9.6 to 13.5V range) bus voltage with a total output power of 264

Watts. Taking full advantage of a synchronous-rectifier topology, the QBC-series achieve ultra-high efficiency of 96%, minimizing power losses and enabling full-power operation to ambient temperatures up to +70°C with minimal air flow. These high-density, open-frame DC/DC converters are standard quarter-brick packages with industry-standard footprint and are only 0.42 inches (10.67mm) high, or 0.54 inches (13.72mm) with optional heat sink.

Assembled using fully automated, SMT-on-pcb techniques, QBC's provide fixed frequency conversion, output On/Off control with choice of positive (standard) or negative (optional) logic, stable no-

load operation, current sharing capability, and low output ripple/noise (80mVp-p).

The fully functional QBC bus converters feature full I/O fault protection including input overvoltage and undervoltage shutdown, output overvoltage, output current limiting, with choice of "hiccup" (standard) or "latching" (optional), short-circuit protection, and thermal shutdown.

All models are IEC/EN/UL60950-1 certified and EMC compliant. Safety, CB, HALT and EMC reports are available upon request.

Refer to the DATEL application note, Bus Converters Aim to Boost Efficiency In IBA-Based Power Designs.

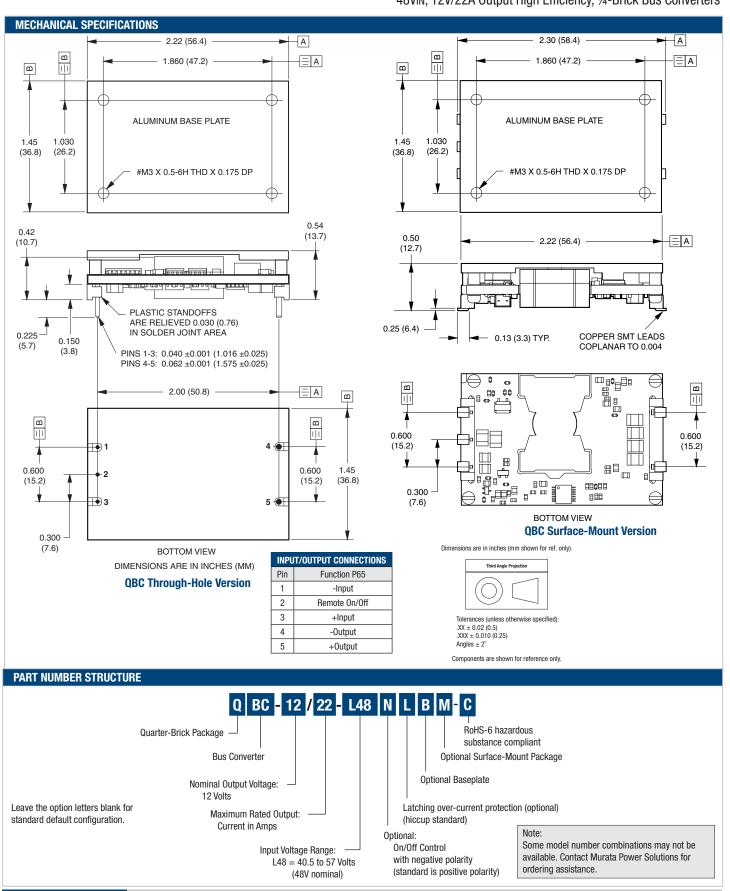
ORDERING GUID	E SUMN	IARY ^①											
				Output					Input				
Model	Vout	lout ⑤	D/M (m)	/ p-p) ②	Re	egulation (3	\	/in	lin ⁴	Effici	ency	Package/Pinout
WIOUGI	V 001	IOUT ©	ח/וז (וווז	(p-p)	Line	Load	Temp.	Nom.	Range	IIII \odot			rackaye/rillout
	V	Α	Тур.	Max.	Max.	Max.	Max.	V	V	mA/A	Min.	Тур.	
QBC-12/22-L48-C	12	22	80	120	±10%	±5%	±2%	48	40.5-57	125/5.7	94.5%	96%	C49, C50/P65

- \odot Typical at TA = +25°C under nominal line voltage and nominal-load conditions, unless noted.
- @ Ripple/Noise (R/N) is tested/specified over a 20MHz bandwidth. All models are specified with an external 0.1µF multi-layer ceramic capacitor installed across their output pins.
- ® Devices have no minimum-load requirements and will regulate under no-load conditions. Regulation specifications describe the output-voltage deviation as the line voltage or load is varied from its midpoint value to either extreme.
- $\\ \ \, \textbf{ \emptyset Nominal line voltage, no-load/full-load conditions.}$
- ⑤ IOUT max. at low line is 24 Amps; 20 Amps at high line. (See Performance Curves.)











Performance/Functional Specifications

Typical @ TA = +25°C under nominal line voltage and full-load conditions unless noted. ①

	Input
Input Voltage Range	40.5-57 Volts (48V nominal)
Overvoltage Shutdown	57.5-59.5 Volts (58V typical)
Start-Up Threshold ②	37-40 Volts (39.5V typical)
Undervoltage Shutdown ②	36-39.5 Volts (38.5V typical)
Input Current	See Performance Spec
Input Reflected Ripple Current ③	10mVp-p
Internal Filter Type	Pi
Reverse Polarity Protection	None (see Absolute Max. Ratings)
On/Off Control 4	
Positive Logic	On= open (internal pull-up)
	Off= 0 to 0.8V (0.8mA max.)
Negative Logic ("N" Suffix)	On = pulled low to 0-0.8V (0.8mA max.)
	Off = open (internal pull-up)

Output						
Vout Range:						
(over line, load and temperature)	9.6 to 13.5V					
Minimum Loading Per Spec	No load					
Ripple/Noise (20MHz BW)	See Performance Spec					
Line/Load Regulation	See Performance Spec					
Efficiency	See Performance Spec					
Isolation Voltage:						
Input/Output	2250Vdc min. (BASIC)					
Isolation Resistance	10ΜΩ					
Isolation Capacitance	470pF					
Current Limit Inception ⑤	25-28 Amps @ 98% Vouт					
Short Circuit Current	TBD					
Overvoltage Protection	13.85V					
Capacitive Loading (Resistive Load)	10000uF					
Temperature Coefficient	±0.02% /°C					

Dynamic onaractoristics					
Dynamic Load Response (50-75% load step to within 1.5% of Voυτ) ⑤	100µsec				
Start up time: ⑥					
Vin to Vout	30 msec				
On/Off to Vout	30 msec				
Switching Frequency, Fixed	150kHz (± TBD)				
Enviro	onmental				

Dynamic Characteristics

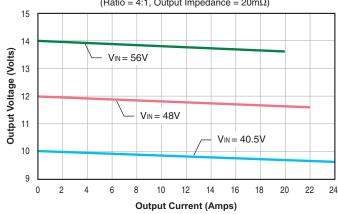
	Environmental		
Calculated MTBF ⑦	TBC million hours		
Operating PCB Temperature ®			
without Derating	-40 to +100°C		
Thermal Shutdown	+115 to +125°C		
Storage Temperature	−55 to +125°C		
	Physical		
Dimensions	See Mechanical Dimensions		
Pin Material underplate	Copper, solder coated over nickel		
Weight	1.62 ounces (46 grams)		

- ① All models are tested and specified with no external output and no external input capacitors, and 300 lfm air flow, unless otherwise noted. All models will effectively regulate under no-load conditions (with perhaps a slight increase in output ripple/noise).
- ② See Technical Notes/Performance Curves for additional explanations and details.
- Input Ripple Current is tested/specified over a 5-20MHz bandwidth with an external 33µF input capacitor and a simulated source impedance of 220µF and 12µH. See I/O Filtering, Input Ripple Current and Output Noise for details.
- ④ The On/Off Control is designed to be driven with open collector or by appropriate voltage levels
- ⑤ The Current-Limit-Inception point is the output current level at which the converter's power-limiting circuitry drops the output voltage 3% from its initial value. See Output Current Limiting and Short-Circuit Protection for more details.
- ® For Start-Up-Time specifications, output settling time is defined as the output voltage having reached ±1% of its final value and the load current having reached at least 80% of its final value.
- MTBF is calculated using TELCORDIA SR-332 Method 1 Case 3, ground fixed, +25°C ambient air and full-load conditions. Contact DATEL for demonstrated life test data.
- ® All models are fully operational and meet all published specifications, including "cold start," at -40°C.

Absolute Maximum Ratings				
Input Voltage:				
Continuous or transient	60 Volts			
Input Reverse-Polarity Protection	None (Input current must be <1.5A all the time)			
Output Current	Current limited. Devices can withstand an indefinite output short			
circuit without damage.				
Storage Temperature	−55 to +125°C			
Lead Temperature (soldering, 10 sec.)	+300°C			
These are stress ratings. Exposure of devices affect long-term reliability. Proper operation un Performance/Functional Specifications Table is	der conditions other than those listed in the			

Typical Performance Curves

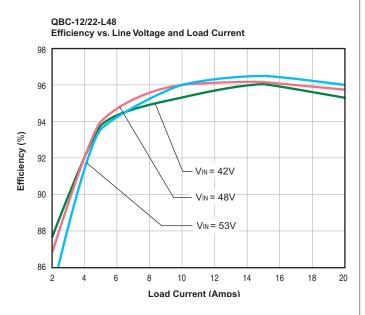


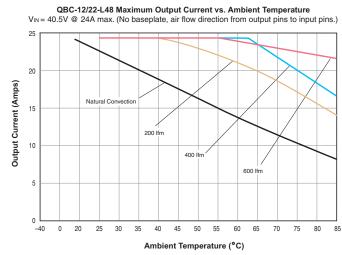


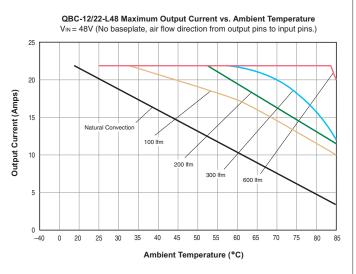


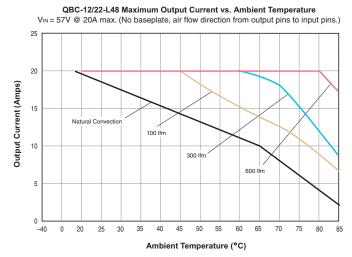
Primary to Secondary Insulation Level Basic

Typical Performance Curves

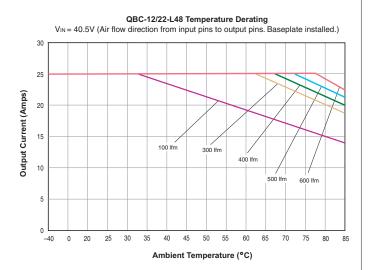


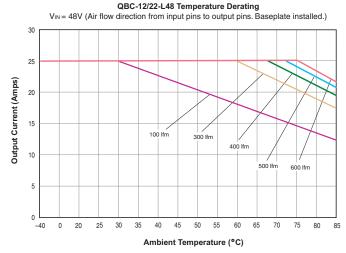


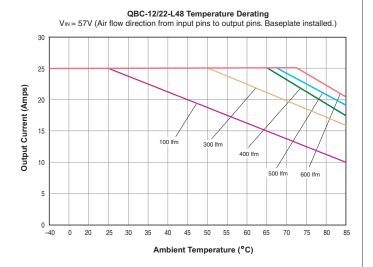




Typical Performance Curves







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