

RJK0358DPA

Silicon N Channel Power MOS FET Power Switching

REJ03G1651-0400

Rev.4.00

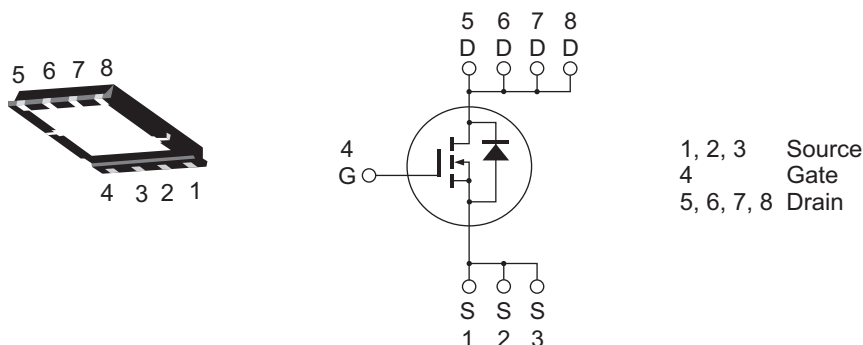
Apr 10, 2008

Features

- High speed switching
 - Capable of 5 V gate drive
 - Low drive current
 - High density mounting
 - Low on-resistance
- $R_{DS(on)} = 2.6 \text{ m}\Omega$ typ. (at $V_{GS} = 10 \text{ V}$)

Outline

RENESAS Package code: PWSN0008DA-A
(Package name: WPAK)



Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Ratings	Unit
Drain to source voltage	V_{DSS}	30	V
Gate to source voltage	V_{GSS}	± 20	V
Drain current	I_D	38	A
Drain peak current	$I_{D(pulse)}$ ^{Note 1}	152	A
Body-drain diode reverse drain current	I_{DR}	38	A
Avalanche current	I_{AP} ^{Note 2}	19	A
Avalanche energy	E_{AR} ^{Note 2}	36.1	mJ
Channel dissipation	P_{ch} ^{Note 3}	45	W
Channel to ambient thermal impedance	θ_{ch-c} ^{Note 3}	2.78	°C/W
Channel temperature	T_{ch}	150	°C
Storage temperature	T_{stg}	-55 to +150	°C

Notes: 1. $PW \leq 10 \mu s$, duty cycle $\leq 1\%$
2. Value at $T_{ch} = 25^\circ C$, $R_g \geq 50 \Omega$
3. $T_c = 25^\circ C$

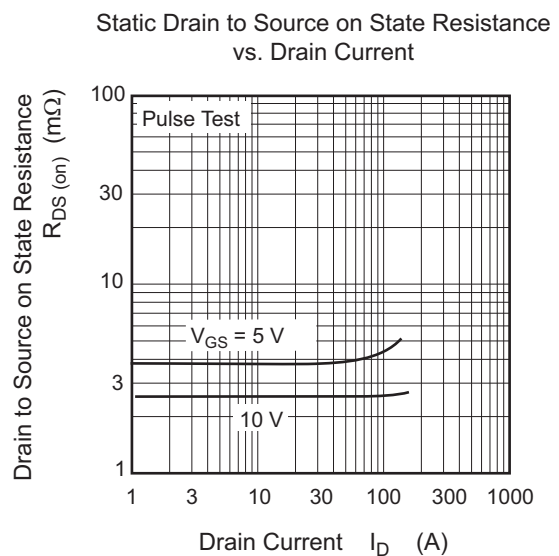
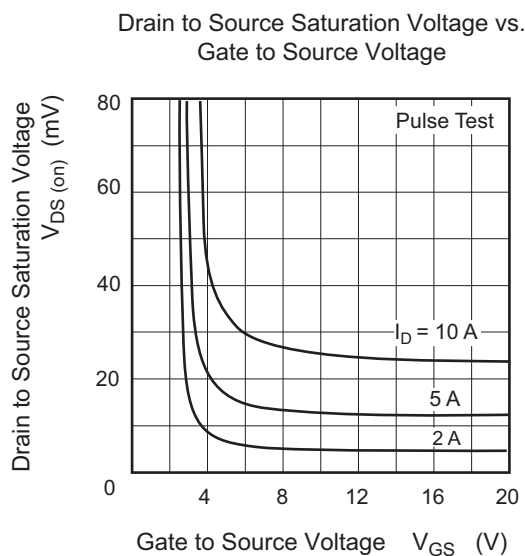
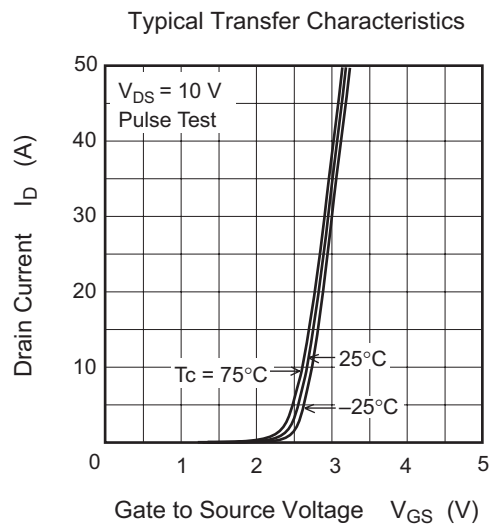
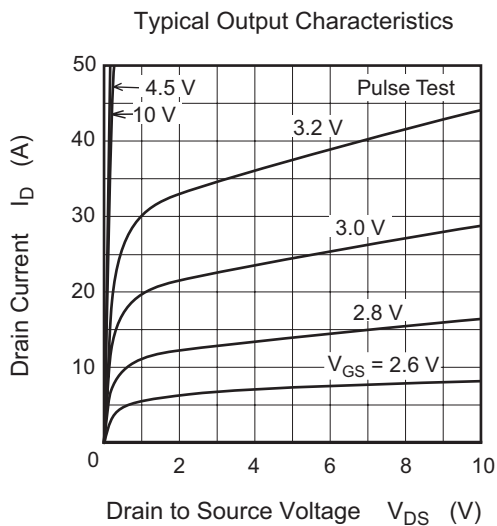
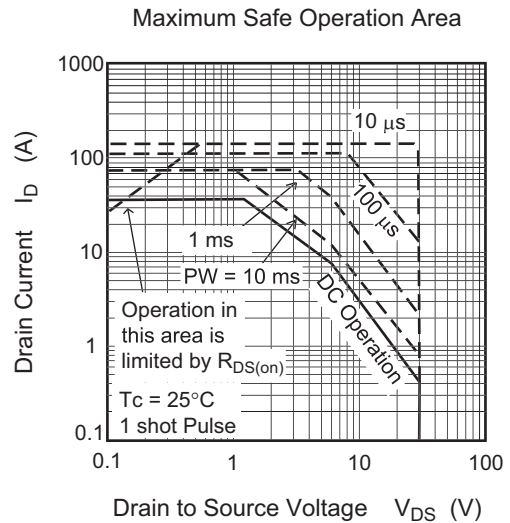
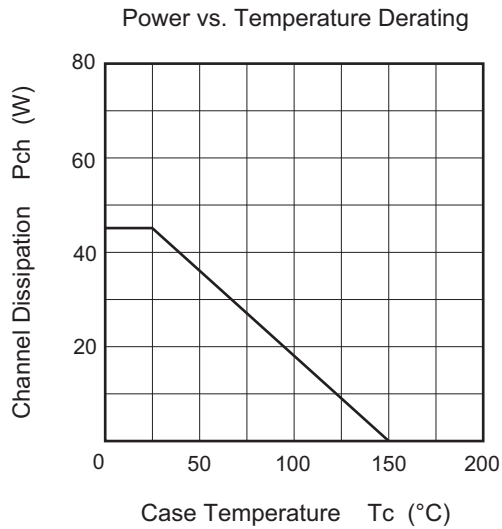
Electrical Characteristics

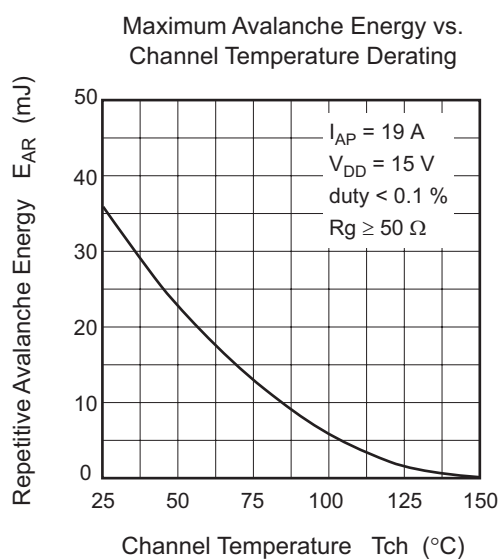
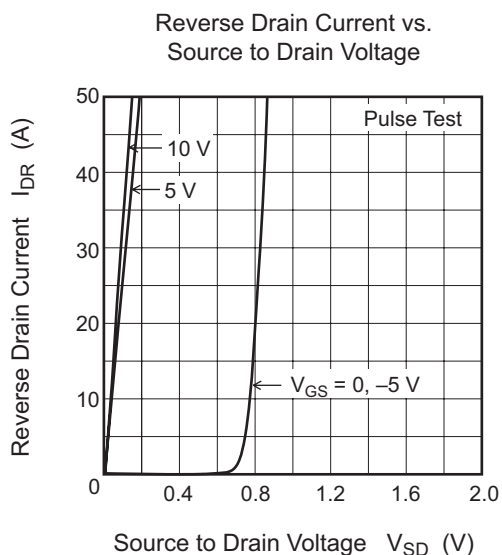
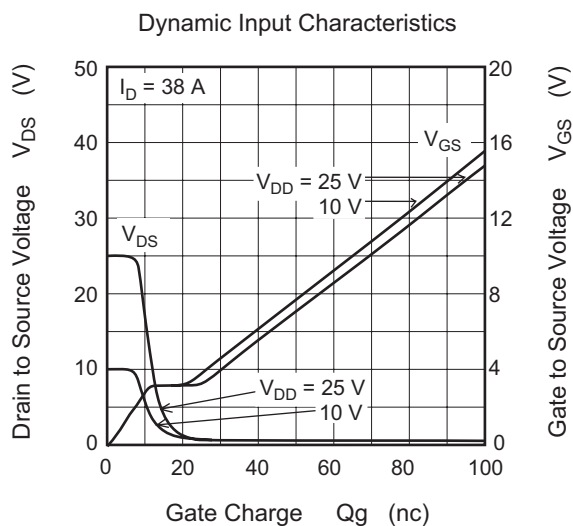
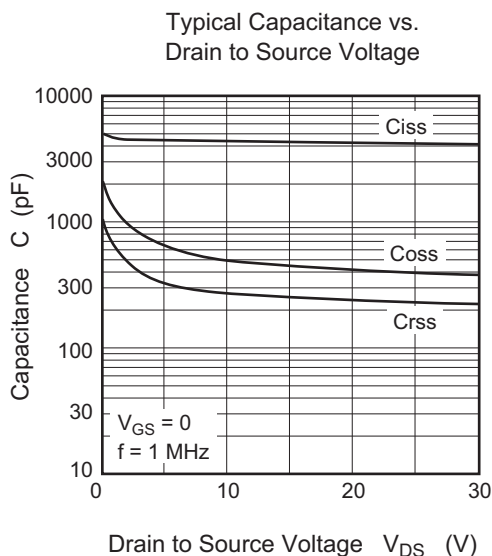
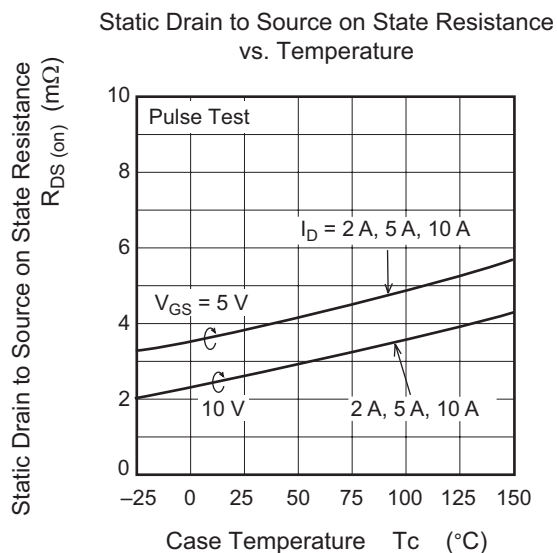
(Ta = 25°C)

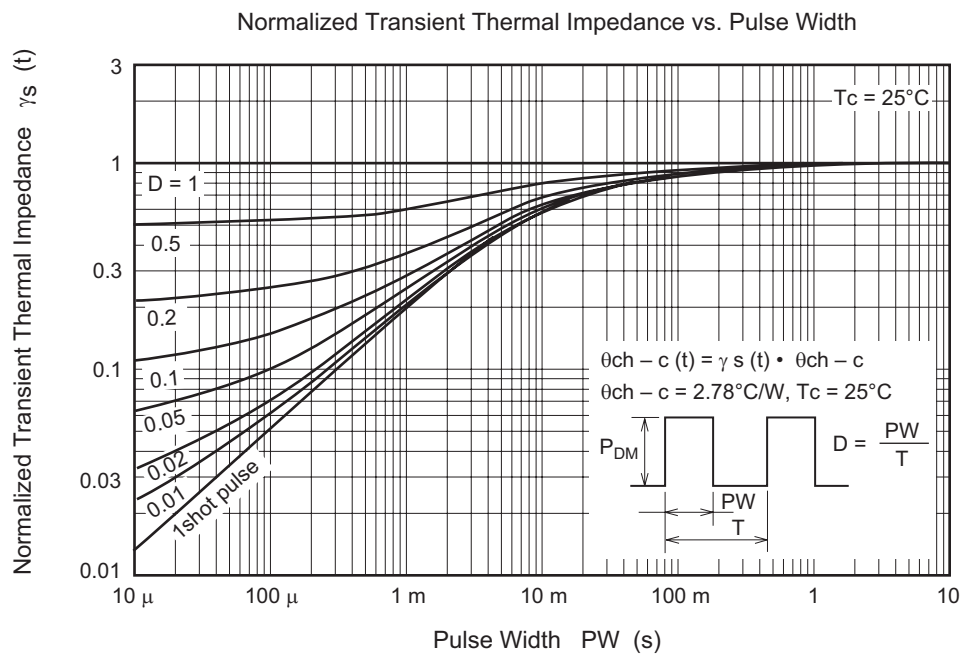
Item	Symbol	Min	Typ	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	30	—	—	V	$I_D = 10 \text{ mA}$, $V_{GS} = 0$
Gate to source leak current	I_{GSS}	—	—	± 0.1	μA	$V_{GS} = \pm 20 \text{ V}$, $V_{DS} = 0$
Zero gate voltage drain current	I_{DSS}	—	—	1	μA	$V_{DS} = 30 \text{ V}$, $V_{GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	1.0	—	2.5	V	$V_{DS} = 10 \text{ V}$, $I_D = 1 \text{ mA}$
Static drain to source on state resistance	$R_{DS(on)}$	—	2.6	3.4	$\text{m}\Omega$	$I_D = 19 \text{ A}$, $V_{GS} = 10 \text{ V}$ ^{Note4}
	$R_{DS(on)}$	—	3.8	5.4	$\text{m}\Omega$	$I_D = 19 \text{ A}$, $V_{GS} = 5 \text{ V}$ ^{Note4}
Forward transfer admittance	$ y_{fs} $	—	50	—	S	$I_D = 19 \text{ A}$, $V_{DS} = 10 \text{ V}$ ^{Note4}
Input capacitance	C_{iss}	—	4300	—	pF	$V_{DS} = 10 \text{ V}$
Output capacitance	C_{oss}	—	500	—	pF	$V_{GS} = 0$
Reverse transfer capacitance	C_{rss}	—	280	—	pF	$f = 1 \text{ MHz}$
Total gate charge	Q_g	—	33	—	nC	$V_{DD} = 10 \text{ V}$
Gate to source charge	Q_{gs}	—	13	—	nC	$V_{GS} = 5 \text{ V}$
Gate to drain charge	Q_{gd}	—	8	—	nC	$I_D = 38 \text{ A}$
Turn-on delay time	$t_{d(on)}$	—	11	—	ns	$V_{GS} = 10 \text{ V}$, $I_D = 19 \text{ A}$
Rise time	t_r	—	5.8	—	ns	$V_{DD} \cong 10 \text{ V}$
Turn-off delay time	$t_{d(off)}$	—	68	—	ns	$R_L = 0.53 \Omega$
Fall time	t_f	—	12	—	ns	$R_g = 4.7 \Omega$
Body-drain diode forward voltage	V_{DF}	—	0.84	1.10	V	$I_F = 38 \text{ A}$, $V_{GS} = 0$ ^{Note4}
Body-drain diode reverse recovery time	t_{rr}	—	30	—	ns	$I_F = 38 \text{ A}$, $V_{GS} = 0$ $di_F/dt = 100 \text{ A}/\mu\text{s}$

Notes: 4. Pulse test

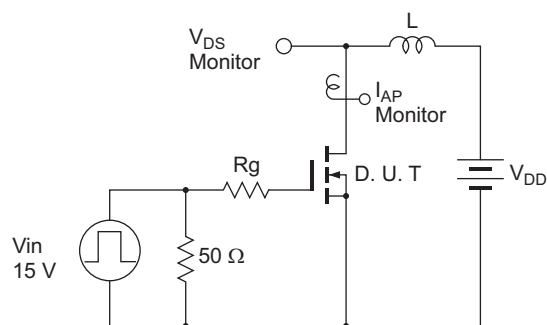
Main Characteristics



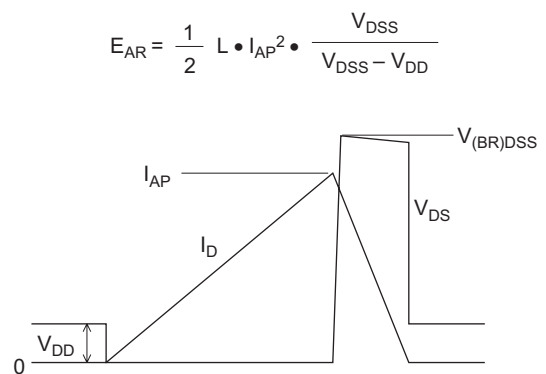




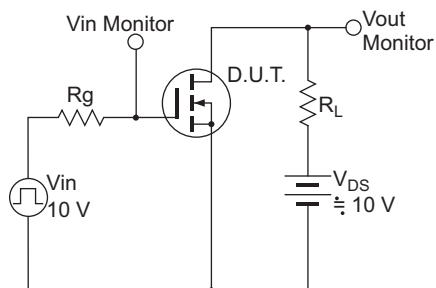
Avalanche Test Circuit



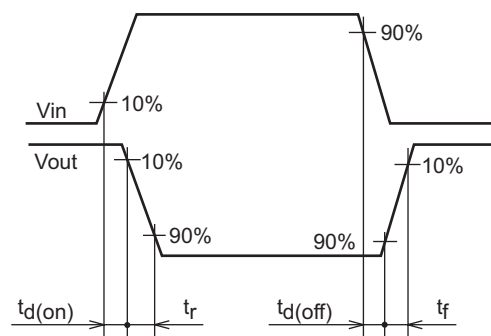
Avalanche Waveform



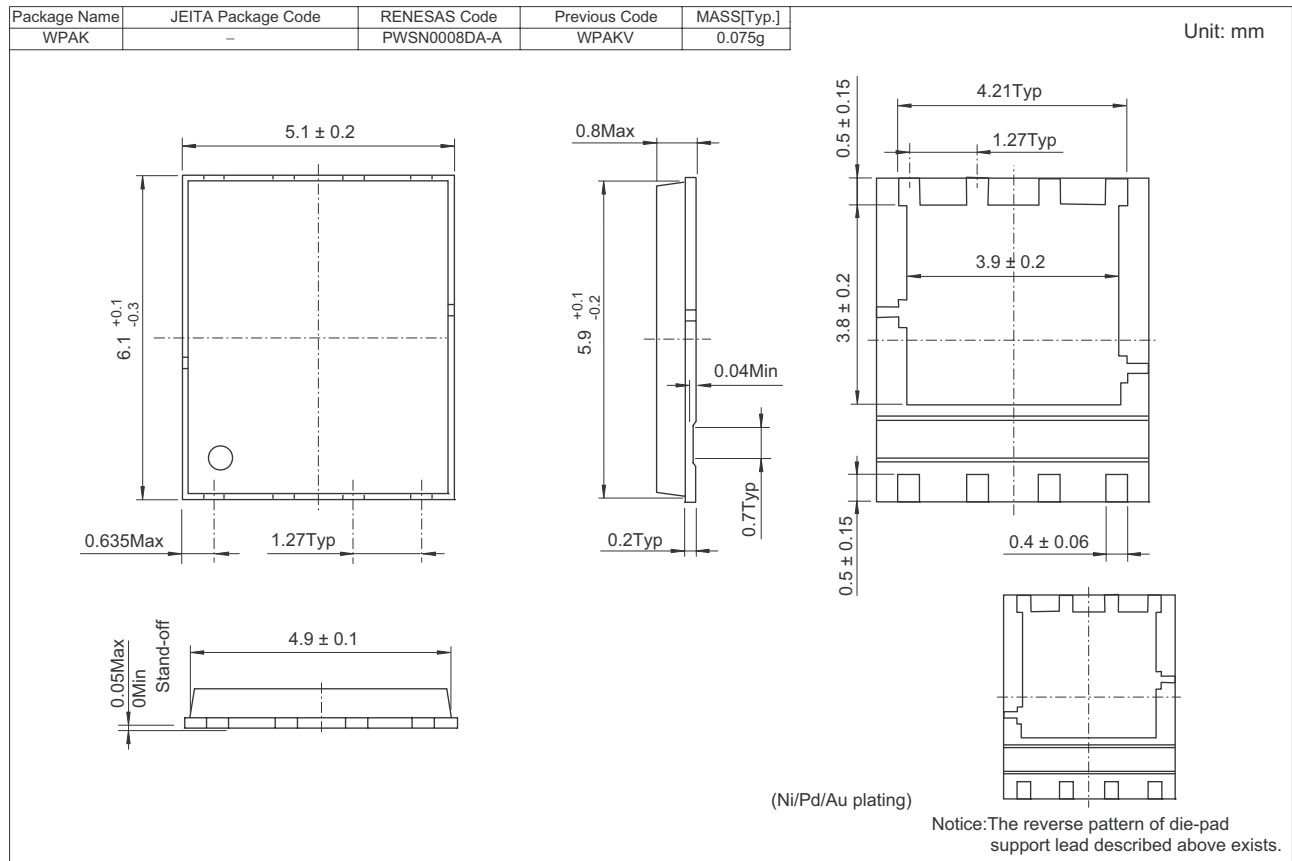
Switching Time Test Circuit



Switching Time Waveform



Package Dimensions



Ordering Information

Part No.	Quantity	Shipping Container
RJK0358DPA-00-J0	2500 pcs	Taping

Notes:

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