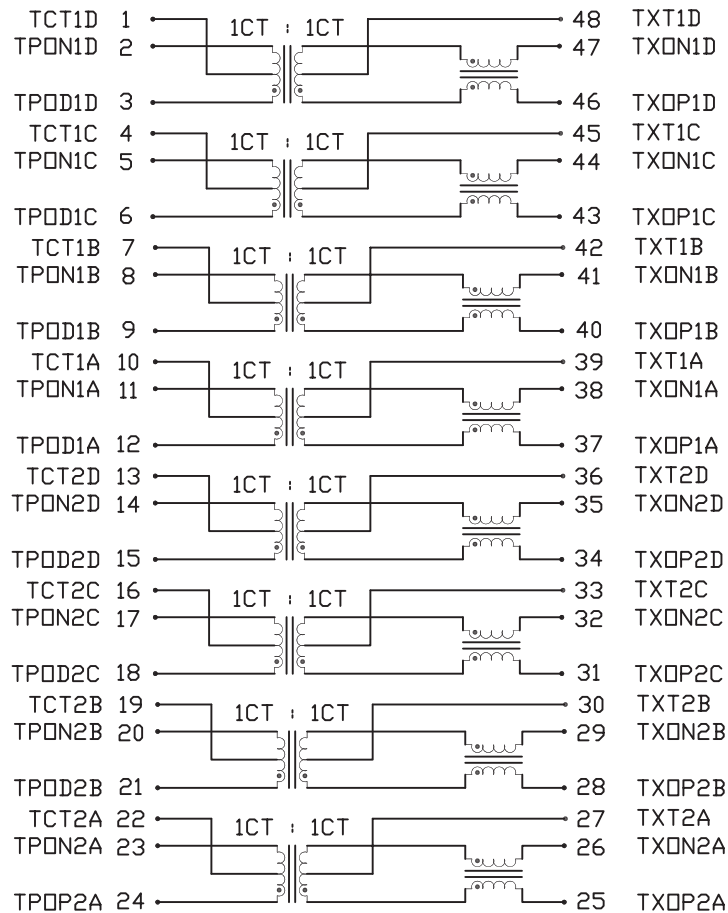


GIGABIT ETHERNET MAGNETICS

S558-5999-Q2-F Dual Transformer Module



SCHEMATIC



ELECTRICAL CHARACTERISTICS @ 25°C

Turns Ratio 1CT : 1CT $\pm 2\%$

DCL 350 μ H MIN. @100kHz, 100mV, WITH 8mA D.C. BIAS.
PIN <46-47>, <43-44>, <40-41>, <37-38>
PIN <34-35>, <31-32>, <28-29>, <25-26>

INSERTION LOSS

FREQ.	1MHz	65MHz	100MHz	125MHz
MAX.	1.1dB	0.5dB	0.8dB	1.2dB

RETURN LOSS
MEASURED UNDER A
100 Ω ENVIRONMENT

18dB MIN. @ 0.5 TO 40 MHz
12-20LOG<f/80MHz>dB MIN. @ 40 TO 100MHz.

COMMON TO COMMON
MODE REJECTION

30dB MIN. @ 0.1 TO 100MHz.

CROSSTALK

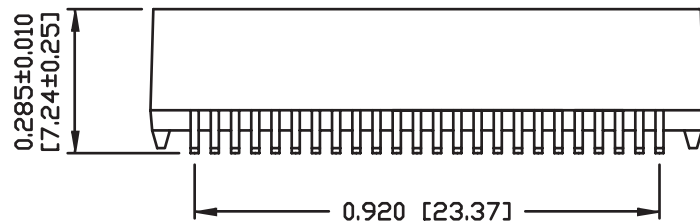
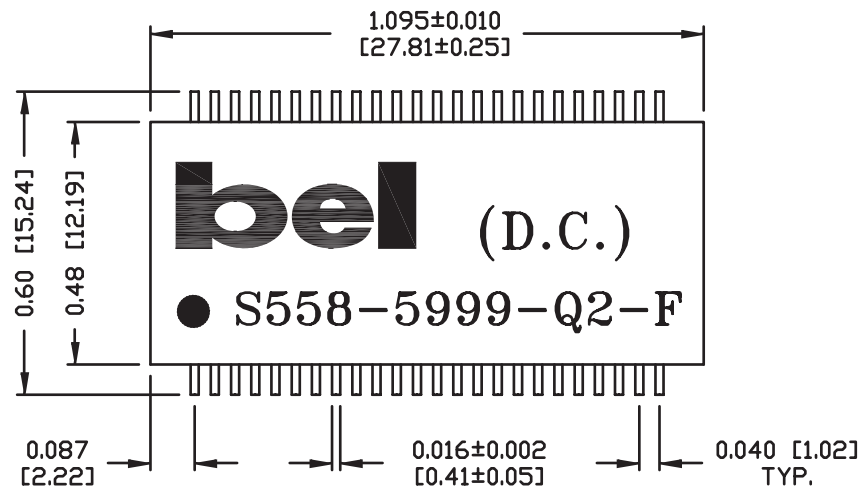
33+20LOG<f/100MHz>dB MIN. @ 1 TO 100MHz.

HIPOT

PER HAND-WORK-03

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NOTES:

1. STANDARD MARKING REFER TO DOC. HAND-WORK-04.
2. PACKAGE CODE: "QVS001".

SUGGESTED PCB PAD LAYOUT

