

P-Channel 20-V (D-S) MOSFET

PRODUCT SUMMARY

V_{DS} (V)	$R_{DS(on)}$ (Ω)	I_D (A)	Q_g (Typ.)
- 20	0.150 at $V_{GS} = - 4.5$ V	1.06	6.0
	0.166 at $V_{GS} = - 2.5$ V	1.0	
	0.214 at $V_{GS} = - 1.8$ V	0.49	

FEATURES

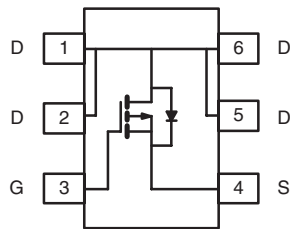
- Halogen-free Option Available
- TrenchFET® Power MOSFET
- 100 % R_g Tested

APPLICATIONS

- Load Switch for Portable Devices

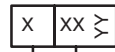

RoHS
COMPLIANT

SC-89 (6-LEADS)

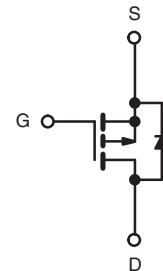


Top View

Marking Code


Lot Traceability
and Date Code

Part # Code



P-Channel MOSFET

Ordering Information: Si1067X-T1-E3 (Lead (Pb)-free)
Si1067X-T1-GE3 (Lead (Pb)-free and Halogen-free)

ABSOLUTE MAXIMUM RATINGS $T_A = 25$ °C, unless otherwise noted

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	- 20	V
Gate-Source Voltage	V_{GS}	± 8	
Continuous Drain Current ($T_J = 150$ °C) ^a	$T_A = 25$ °C	- 1.06 ^{b, c}	A
	$T_A = 70$ °C	- 0.85 ^{b, c}	
Pulsed Drain Current	I_{DM}	- 8	
Continuous Source-Drain Diode Current	I_S	- 0.2 ^{b, c}	
Maximum Power Dissipation ^a	$T_A = 25$ °C	0.236 ^{b, c}	W
	$T_A = 70$ °C	0.151 ^{b, c}	
Operating Junction and Storage Temperature Range	T_J, T_{stg}	- 55 to 150	°C

THERMAL RESISTANCE RATINGS

Parameter	Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^{a, b}	R_{thJA}	440	530	°C/W
		540	650	

Notes:

- Maximum under Steady State conditions is 650 °C/W.
- Surface Mounted on 1" x 1" FR4 board.
- $t = 5$ s.

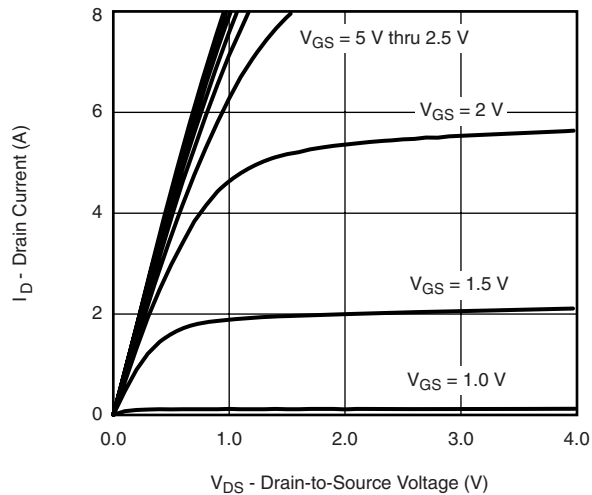
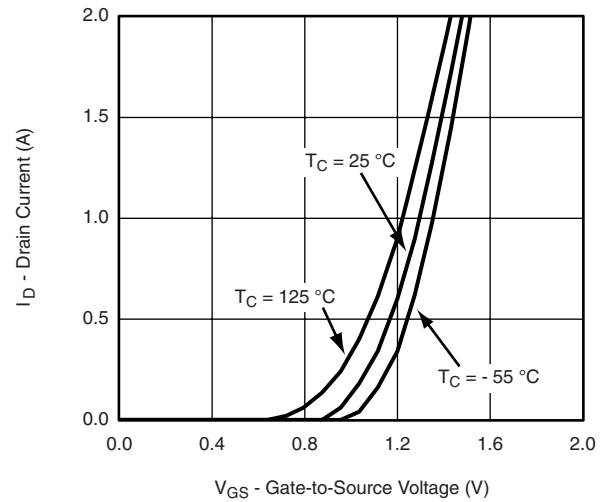
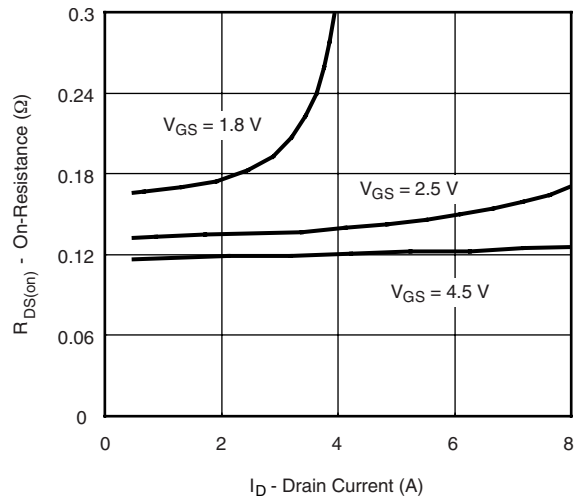
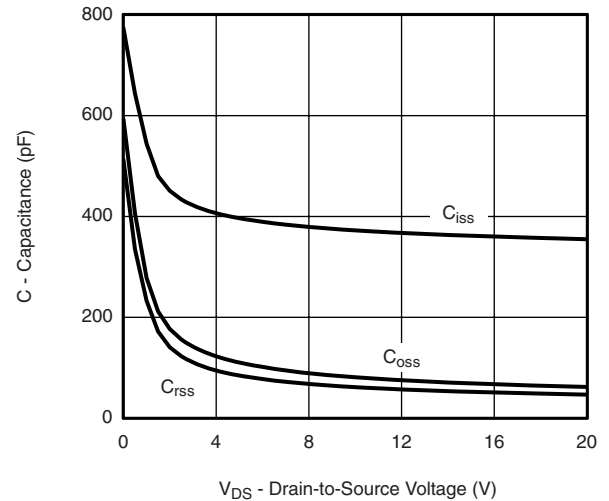
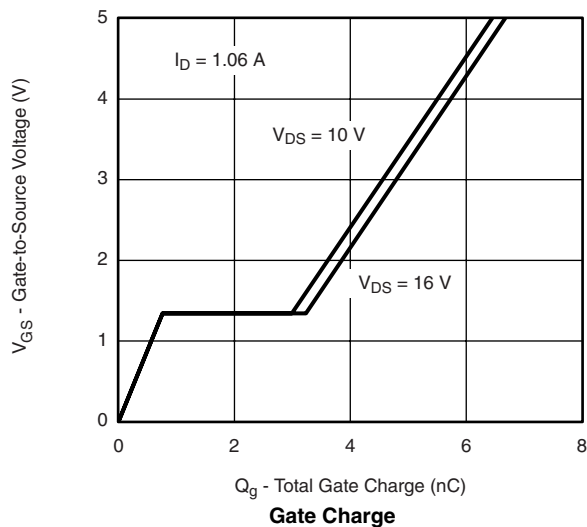
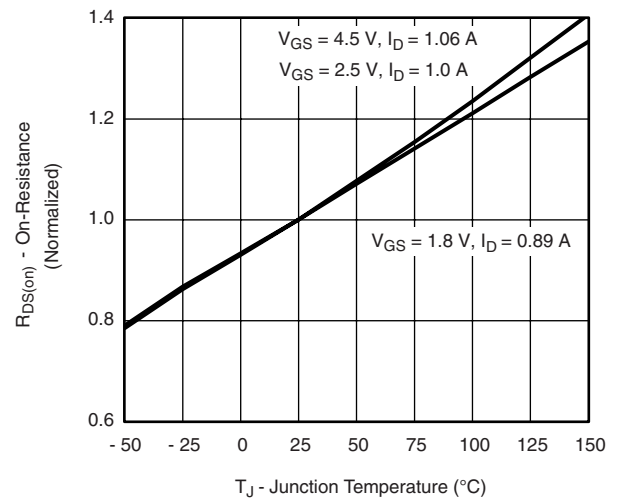
SPECIFICATIONS T _J = 25 °C, unless otherwise noted						
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0 V, I _D = - 250 μA	- 20			V
V _{DS} Temperature Coefficient	ΔV _{DS} /T _J	I _D = - 250 μA		- 32.07		mV/°C
V _{GS(th)} Temperature Coefficient	ΔV _{GS(th)} /T _J			3.02		
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = - 250 μA	- 0.45		- 0.95	V
Gate-Source Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 8 V			± 100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = - 20 V, V _{GS} = 0 V			- 1	μA
		V _{DS} = - 20 V, V _{GS} = 0 V, T _J = 85 °C			- 10	
On-State Drain Current ^a	I _{D(on)}	V _{DS} = ≥ 5 V, V _{GS} = - 4.5 V	- 8			A
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = - 4.5 V, I _D = - 1.06 A		0.125	0.150	Ω
		V _{GS} = - 2.5 V, I _D = - 1.0 A		0.138	0.166	
		V _{GS} = - 1.8 V, I _D = - 0.49 A		0.165	0.214	
Forward Transconductance	g _{fs}	V _{DS} = - 10 V, I _D = - 1.06 A		4.0		S
Dynamic ^b						
Input Capacitance	C _{iss}	V _{DS} = - 10 V, V _{GS} = 0 V, f = 1 MHz		375		pF
Output Capacitance	C _{oss}			82		
Reverse Transfer Capacitance	C _{rss}			62		
Total Gate Charge	Q _g	V _{DS} = - 10 V, V _{GS} = - 5 V, I _D = - 1.06 A		6.5	9.3	nC
		V _{DS} = - 10 V, V _{GS} = - 4.5 V, I _D = - 1.06 A		6.0	9.1	
Q _{gs}			0.76			
Q _{gd}			2.23			
Gate Resistance	R _g	f = 1 MHz		8.8	13.2	Ω
Turn-On Delay Time	t _{d(on)}	V _{DD} = - 10 V, R _L = 19.74 Ω I _D ≡ - 0.76 A, V _{GEN} = - 4.5 V, R _g = 1 Ω		14	21	ns
Rise Time	t _r			22	33	
Turn-Off Delay Time	t _{d(off)}			48	72	
Fall Time	t _f			17	25.5	
Drain-Source Body Diode Characteristics						
Pulse Diode Forward Current ^a	I _{SM}				8	A
Body Diode Voltage	V _{SD}	I _S = - 0.63 A		0.8	1.2	V
Body Diode Reverse Recovery Time	t _{rr}	I _F = - 0.7 A, dI/dt = 100 A/μs		12.8	19.2	nC
Body Diode Reverse Recovery Charge	Q _{rr}			4.5	6.8	ns
Reverse Recovery Fall Time	t _a			7.3		
Reverse Recovery Rise Time	t _b			5.5		

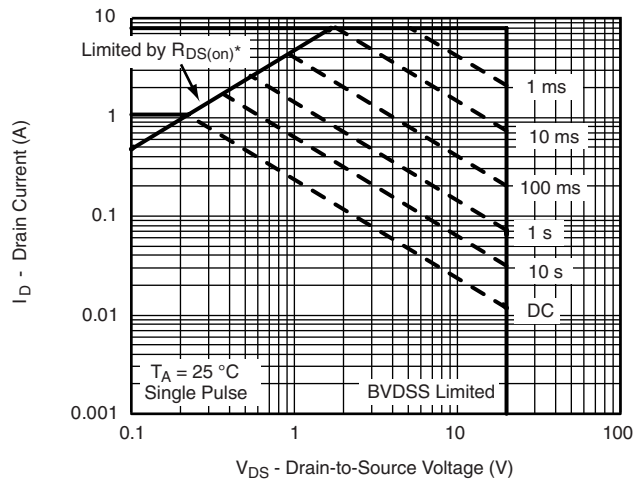
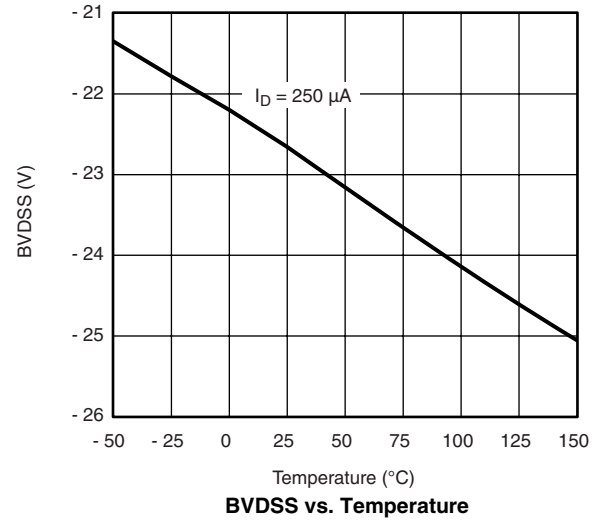
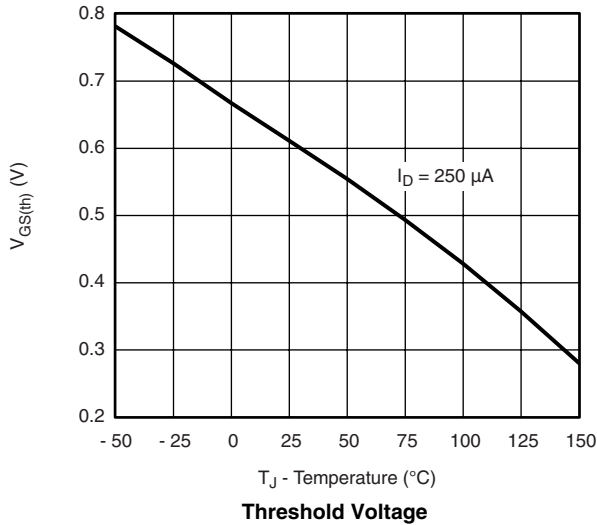
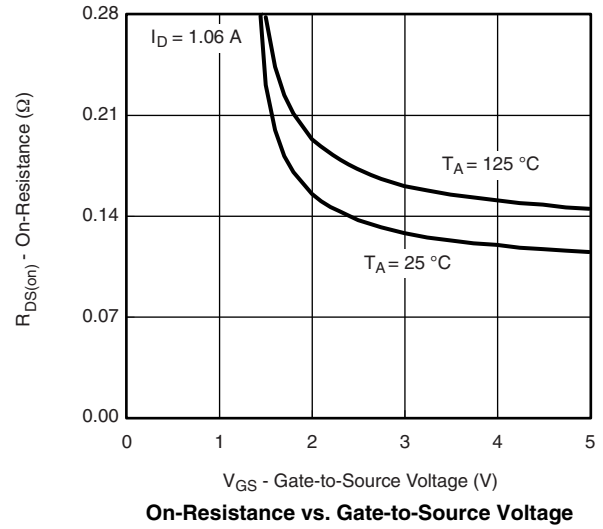
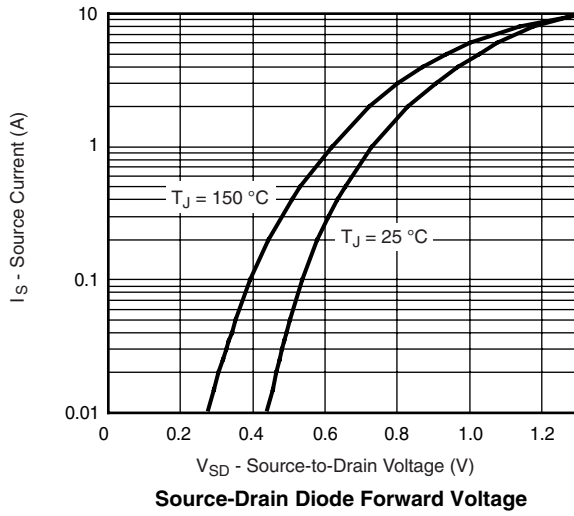
Notes:

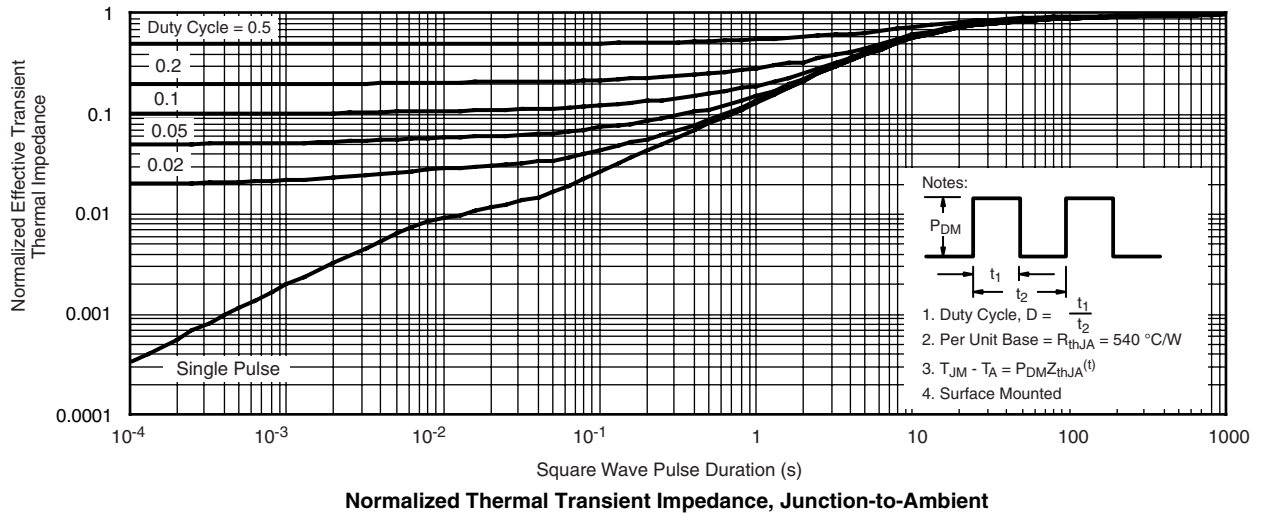
a. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.

b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

**TYPICAL CHARACTERISTICS** $T_A = 25^\circ\text{C}$, unless otherwise noted**Output Characteristics****Transfer Characteristics curves vs. Temperature****On-Resistance vs. Drain Current****Capacitance****Gate Charge****On-Resistance vs. Junction Temperature**

TYPICAL CHARACTERISTICS $T_A = 25\text{ }^{\circ}\text{C}$, unless otherwise noted* $V_{GS} >$ minimum V_{GS} at which $R_{DS(on)}$ is specified**Safe Operating Area, Junction-to-Ambient**

**TYPICAL CHARACTERISTICS** $T_A = 25\text{ }^{\circ}\text{C}$, unless otherwise noted

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