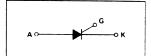
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2N6167 thru 2N6170

SCRs 20 AMPERES RMS 100 thru 600 VOLTS



Silicon Controlled Rectifier Reverse Blocking Triode Thyristor

... designed for industrial and consumer applications such as power supplies; battery chargers; temperature, motor, light and welder controls.

- Economical for a Wide Range of Uses
- High Surge Current ITSM = 240 Amps
- Rugged Construction in Isolated Stud Package

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
*Peak Repetitive Forward and Reverse Blocking Voltage (1) (T _J = -40°C to +100°C) 2N6167 2N6168 2N6169 2N6170	VDRM or VRRM	100 200 400 600	Volts
*Non-Repetitive Peak Reverse Blocking Voltage (t ≤ 5 ms) 2N6167 2N6168 2N6169 2N6170	VRSM	150 250 450 650	Volts
*Average Forward Current (T _C = -40 to +65°C) (+85°C)	lT(AV)	13 6.5	Amps
*Peak Surge Current (One cycle, 60 Hz) ($T_C = +65^{\circ}C^{1}$ (1.5 ms pulse @ $T_J = 100^{\circ}C$) Preceded and followed by no current or Voltage	ITSM	240 560	Amps
Circuit Fusing $(T_J = -40 \text{ to } +100^{\circ}\text{C}) \text{ (t } = 1 \text{ to } 8.3 \text{ ms)}$	12t	235	A ² s
*Peak Gate Power	PGM	5	Watts
*Average Gate Power	P _G (AV)	0.5	Watt

^{*}Indicates JEDEC Registered Data

(cont)

⁽¹⁾ Ratings apply for zero or negative gate voltage. Devices shall not have a positive bias applied to the gate concurrently with a negative potential on the anode. Devices should not be tested with a constant current source for forward or reverse blocking capability such that the voltage applied exceeds the rated blocking voltage.

MAXIMUM RATINGS — continued

Rating	Symbol	Symbol Value	
*Peak Forward Gate Current	^I GFM	2	Amps
*Operating Junction Temperature Range	TJ	-40 to +100	°C
*Storage Temperature Range	T _{stg}	-40 to +150	°C
*Stud Torque	_	30	in. lb.

*THERMAL CHARACTERISTICS

Characteristic `	Symbol	Max	Unit
Thermal Resistance, Junction to Case	R _Ø JC	1.5	°C/W

ELECTRICAL CHARACTERISTICS ($T_C = 25^{\circ}C$ unless otherwise noted.)

Characteristic	Symbol	Min-	Тур	Max	Unit
*Peak Forward or Reverse Blocking Current (Rated VDRM or VRRM, gate open, T _C = 100°C)	DRM, RRM				mA
2N6167 2N6168 2N6169		=	1 1 1	2 2.5 3	
2N6170 (Rated V_{DRM} or V_{RRM} , gate open, $T_{C} = 25^{\circ}C$) All Devices		_	1 -	4 10	μА
*Peak Forward "On" Voltage (I _{TM} = 41 A Peak)	VTM	_	1.5	1.7	Volts
Gate Trigger Current, Continuous dc $^{\circ}T_{C}=-40^{\circ}C$ $(V_{D}=12~V,~R_{L}=24~\Omega)$ $T_{C}=25^{\circ}C$	^I GT	_	 2.1	75 40	mA
Gate Trigger Voltage, Continuous dc $^*T_C = -40^{\circ}C$ $(V_D = 12 \text{ V}, R_L = 24 \Omega)$ $T_C = 25^{\circ}C$	V _G T	_	0. 8 0. 63	2.5 1.6	Volts
Holding Current $^{\circ}T_{C} = -40^{\circ}C$ $(V_{D} = 12 \text{ V, gate open, } I_{T} = 200 \text{ mA})$ $T_{C} = 25^{\circ}C$	'н	_	 3.5	90 50	mA
*Turn-On Time $(t_d + t_r)$ $(l_{TM} = 41$ Adc, $V_D = Rated V_{DRM}$, $l_{GT} = 200$ mAdc, Rise Time ≤ 0.05 μ s, Pulse Width $= 10$ μ s)	ton	_		1	μs
Turn-Off Time $(I_{TM} = 10 \text{ A}, I_{R} = 10 \text{ A})$ $(I_{TM} = 10 \text{ A}, I_{R} = 10 \text{ A}, T_{J} = 100^{\circ}\text{C})$	toff	_	25 40	-	μs
Forward Voltage Application Rate (T _J = 100°C, V _D = Rated V _{DRM})	dv∙dt	_	50	_	V/μs

^{*}Indicates JEDEC Registered Data.

