

SCHOTTKY BARRIER RECIFITERS

PRODUCT SUMMARY

Surface Mount Low V_F Reverse Voltage 20 to 40 volts Forward Current 1.0 Ampere

FEATURES

For surface mounted application
Metal silicon junction, majority carrier conduction
Low forward voltage drop
Easy pick and place
High surge current capability
Plastic material used carries Underwriters Laboratory
Classification 94V-O
Epitaxial construction
High temperature soldering:
250°C / 10 seconds at terminals

MECHANCIAL DATA

Cases: Molded plastic Terminals: Solder plated

Polarity: Indicated by cathode band Weight: 0.002 ounce, 0.064 gram



Pb) Pb-free; RoHS-compliant

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Rating at 25 °C ambient temperature unless otherwise specified. Single phase, half wave, 60 Hz, resistive or inductive load. For capacitive load, derate current by 20%

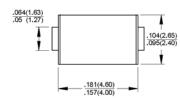
Parameter	Symbols	SL12	SL13	SL14	Units
Maximum repetitive peak reverse voltage	V _{RRM}	20	30	40	Volts
Maximum RMS voltage	V _{RMS}	14	21	28	Volts
Maximum DC blocking voltage	V _{DC}	20	30	40	Volts
Maximum average forward rectified current See Fig. 1	I _(AV)	1.0			Amp
Peak forward surge current, 8.3 ms single half sine-wave superimposed on rated load (JEDEC Method)	I _{FSM}	30.0			Amps
Maximum instantaneous forward voltage @ 1.0A (Note 1)	V _F	0.385	0.385	0.400	Volts
Maximum DC reverse current @ T _A =25°C at rated DC blocking voltage @ T _A =100°C	l _R	2.0 100			mA
Maximum thermal resistance (Note 2)	$R_{_{\theta JA}}$	28 88			°C/W
Operating junction temperature range	T _J	-55 to +125			°C
Storage temperature range	T _{stg}	-55 to +150			°C

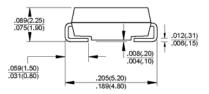
Notes: 1. Pulse Test with PW=300usec, 1% Duty Cycle.

2. Measured on P.C. Board with 0.2 x 0.2" (5.0 x 5.0 mm) Copper Pad Areas.



DO-214AC (SMA)

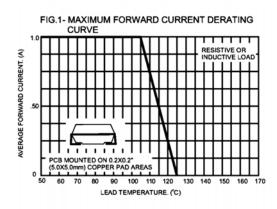


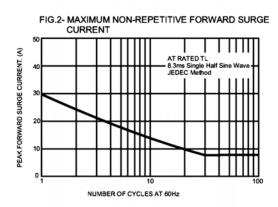


Dimensions in inches and (millimeters)

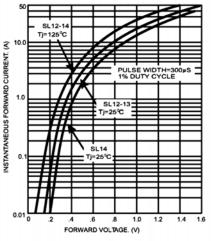


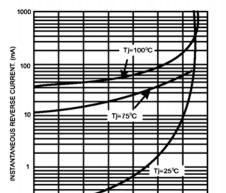
RATINGS AND CHARACTERISTIC CURVES











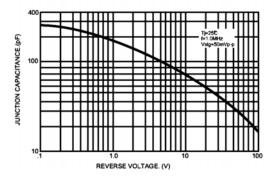
PERCENT OF RATED PEAK REVERSE VOLTAGE. (%)

100

20 40 60 80

FIG.4- TYPICAL REVERSE CHARACTERISTICS

FIG.5- TYPICAL JUNCTION CAPACITANCE



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