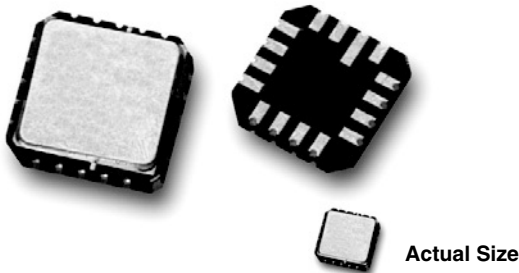


Hermetic, 50 Mil Pitch, Leadless Chip Resistor Networks



Capable of meeting the characteristics of MIL-PRF-83401 these networks are available in a wide range of resistance values: several standard configurations are presented with the SLCC series.

FEATURES

- High stability ultrafilm (0.05 % at 1000 h at + 70 °C under Pn)
- Low noise < 35 dB
- Hermetic package



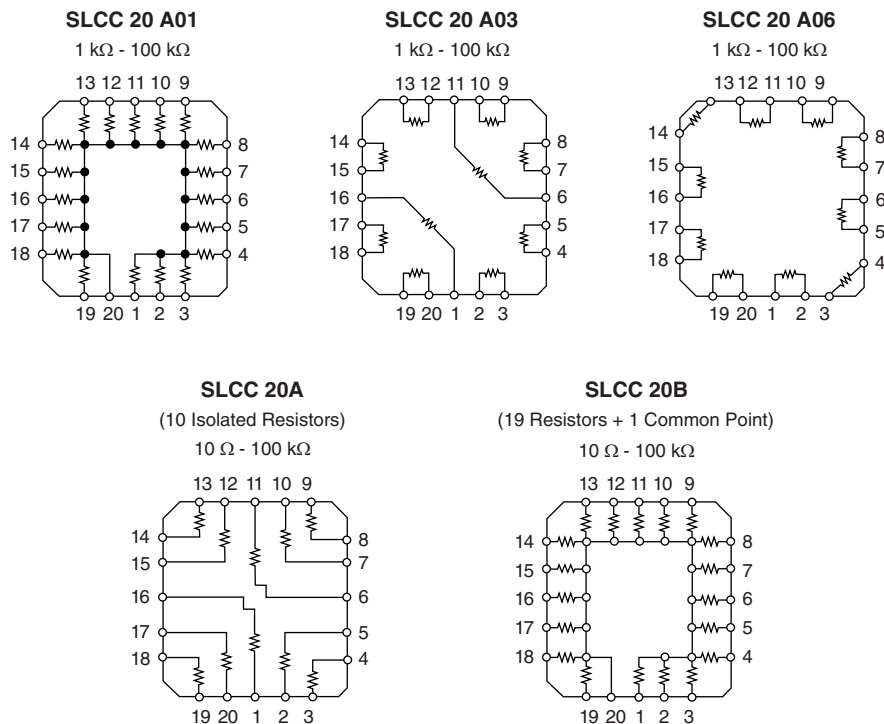
RoHS
COMPLIANT

TYPICAL PERFORMANCE

	ABS	TRACKING
TCR	25 ppm/°C	2 ppm/°C
	ABS	RATIO
TOL.	0.1 %	0.1 %

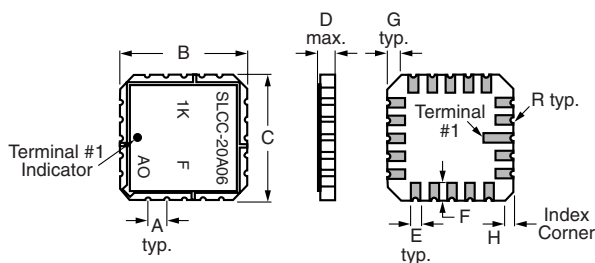
Resistance Range: Noted on schematics

SCHEMATIC



STANDARD ELECTRICAL SPECIFICATIONS

TEST	SPECIFICATIONS	CONDITIONS
PIN NUMBER	20	
TCR:	Tracking	± 2 ppm/°C (typical)/5 ppm/°C maximum
	Absolute	± 25 ppm/°C maximum
Tolerance:	Ratio	$\pm 0.1\%$ to 1.0 % tighter specs on request
	Absolute	$\pm 0.1\%$ to 5.0 % tighter specs on request
Power rating:	Resistor	50 mW
	Package	500 mW
Stability (ΔR ratio)		at + 70 °C
Voltage coefficient		at + 70 °C
Working voltage		1000 h at + 70 °C under Pn
Operating temperature range		< 0.1 ppm/V
Storage temperature range		100 V _{DC} on R
Noise		- 55 °C to + 125 °C
Thermal EMF		- 55 °C to + 150 °C
Shelf life stability		- 35 dB typical
		0.1 μ V/°C
		< 50 ppm
		1 year at + 25 °C

DIMENSIONS in millimeters [inches]

	20 Pin
A	1.27 [0.050]
B	8.89 [0.350]
C	8.89 [0.350]
D	1.96 [0.077]
E	0.635 [0.025]
F	1.27 [0.050]
G	1.02 [0.040]
H	0.508 [0.020]

PACKAGING

Tape and reel 250 pieces per tape

GLOBAL PART NUMBER INFORMATION

New Global Part Numbering: SLCC20A01-10KF0005

S	L	C	C	2	0	A	0	1	-	1	0	K	F	T	0	0	0	5
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GLOBAL MODEL	SCHEMATIC	VALUE	TOL.	PACKAGING	OPTION
SLCC20	A B A01 A03 A06	Decimal R, K or M	B (0.1 % 0.1 %) D (0.5 % 0.25 %) F (1.0 % 0.5 %) G (2.0 % 1.0 %) J (5.0 % 1.0 %)	Tape and reel	leave blank if no option

Historical Part Number example: SLCC 20 A01 10K F R0005 e4

SLCC 20	A01	10K	F	R0005	e4
GLOBAL MODEL	SCHEMATIC	VALUE	TOL.	OPTION	RoHs
				leave blank if no option	



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