



SPN8080 N-Channel Enhancement Mode MOSFET

DESCRIPTION

The SPN8080 is the N-Channel logic enhancement mode power field effect transistors are produced using high cell density , DMOS trench technology.

This high density process is especially tailored to minimize on-state resistance.

These devices are particularly suited for low voltage application , notebook computer power management and other battery powered circuits where high-side switching .

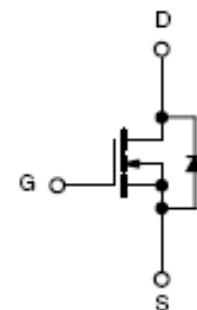
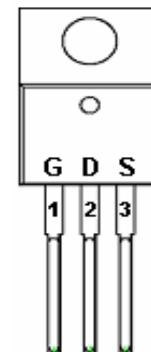
FEATURES

- ◆ 80V/80A,R_{DS(ON)}= 4.7mΩ@V_{GS}= 10V
- ◆ 80V/37A,R_{DS(ON)}= 8.7mΩ@V_{GS}= 6V
- ◆ Super high density cell design for extremely low RDS (ON)
- ◆ Exceptional on-resistance and maximum DC current capability
- ◆ TO-220-3L package design

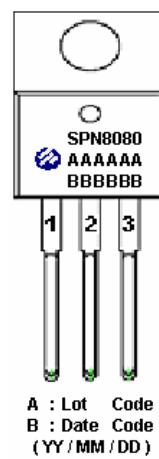
APPLICATIONS

- DC/DC Converter
- Load Switch
- SMPS Secondary Side Synchronous Rectifier

PIN CONFIGURATION(TO-220-3L)



PART MARKING





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PIN DESCRIPTION

Pin	Symbol	Description
1	G	Gate
2	D	Drain
3	S	Source

ORDERING INFORMATION

Part Number	Package	Part Marking
SPN8080T220TGB	TO-220-3L	SPN8080

※ SPN8080T220TGB: Tube ; Pb – Free; Halogen – Free

ABSOLUTE MAXIMUM RATINGS

(TA=25°C Unless otherwise noted)

Parameter	Symbol	Typical	Unit
Drain-Source Voltage	V _{DSS}	80	V
Gate –Source Voltage	V _{GSS}	±20	V
Continuous Drain Current(T _J =150°C)	T _A =25°C	ID	A
	T _A =70°C		
Pulsed Drain Current	I _{DM}	300	A
Avalanche Current	I _{AS}	15	A
Power Dissipation	T _A =25°C	P _D	W
	T _A =70°C		
Avalanche Energy with Single Pulse (T _j =25°C, ID=30A, V _{DD} =37.5V)	E _{AS}	400	mJ
Operating Junction Temperature	T _J	-55/150	°C
Storage Temperature Range	T _{STG}	-55/150	°C
Thermal Resistance-Junction to Ambient	R _{θJA}	2	°C/W



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ELECTRICAL CHARACTERISTICS

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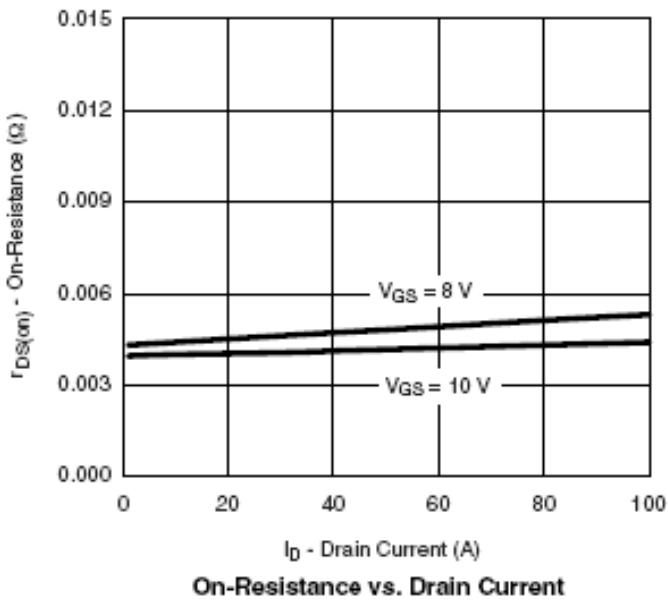
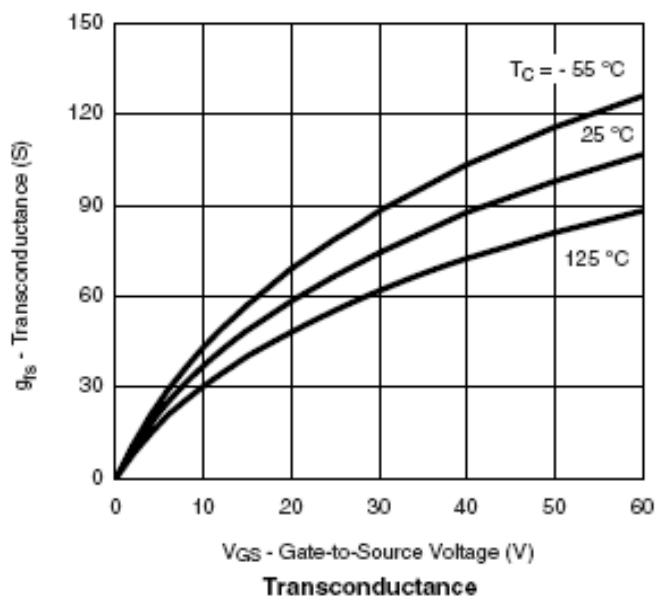
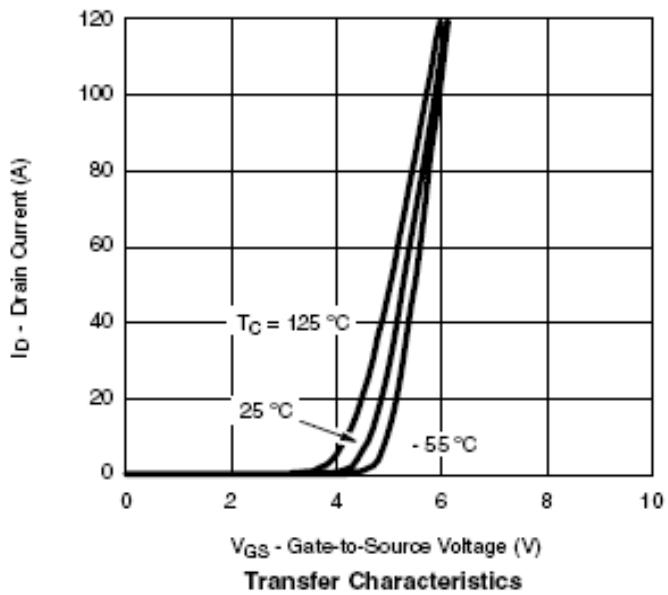
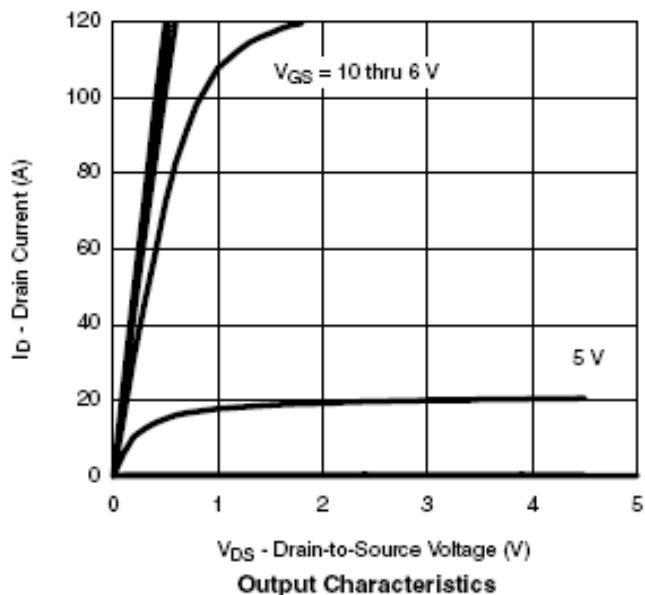
Parameter	Symbol	Conditions	Min.	Typ	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V(BR)DSS	VGS=0V, ID=250uA	80			V
Gate Threshold Voltage	VGS(th)	VDS=VGS, ID=250uA	2.0		4.0	
Gate Leakage Current	IGSS	VDS=0V, VGS=±20V			±100	nA
Zero Gate Voltage Drain Current	IDSS	VDS=80V, VGS=0V			1	uA
		VDS=80V, VGS=0V TJ = 150 °C			250	
On-State Drain Current	ID(on)	VDS≥5V, VGS =10V	70			A
Drain-Source On-Resistance	RDS(on)	VGS= 10V, ID=80A		4.0	4.7	mΩ
		VGS= 6V, ID=37A		5.8	8.7	
Forward Transconductance	gfs	VDS=10V, ID=80A		150		S
Diode Forward Voltage	VSD	IS=40A, VGS =0V			1.5	V
Dynamic						
Total Gate Charge	Qg	VDS=40V, VGS=10V ID= 80A		250		nC
Gate-Source Charge	Qgs			83		
Gate-Drain Charge	Qgd			62		
Input Capacitance	Ciss	VDS=25V, VGS=0V f=1MHz		14500		pF
Output Capacitance	Coss			850		
Reverse Transfer Capacitance	Crss			280		
Turn-On Time	td(on)	VDD=40V, RL=20Ω ID=37A, VGEN=10V RG=3.3Ω		80		nS
	tr			37		
Turn-Off Time	td(off)			140		
	tf			27		



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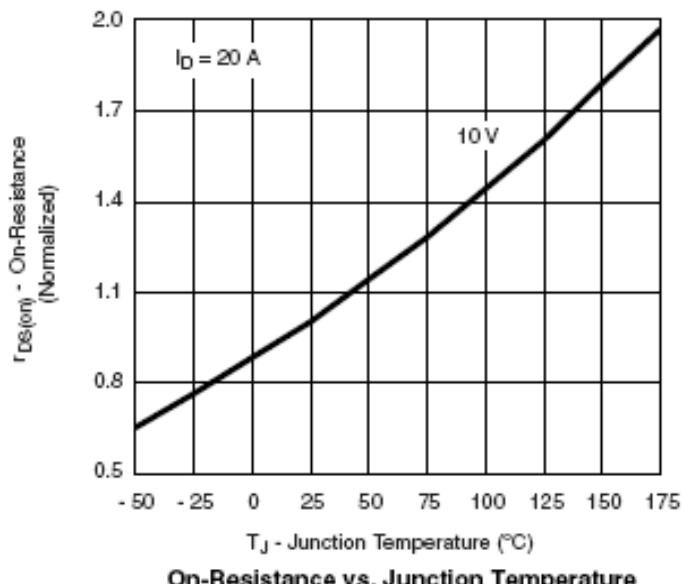
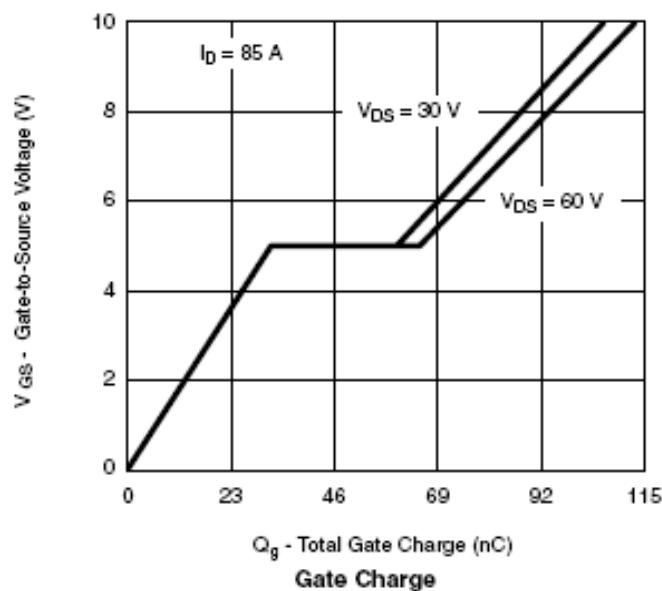
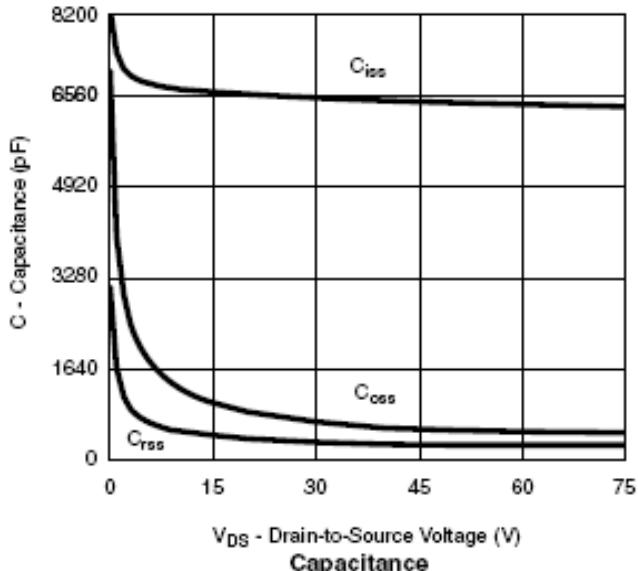
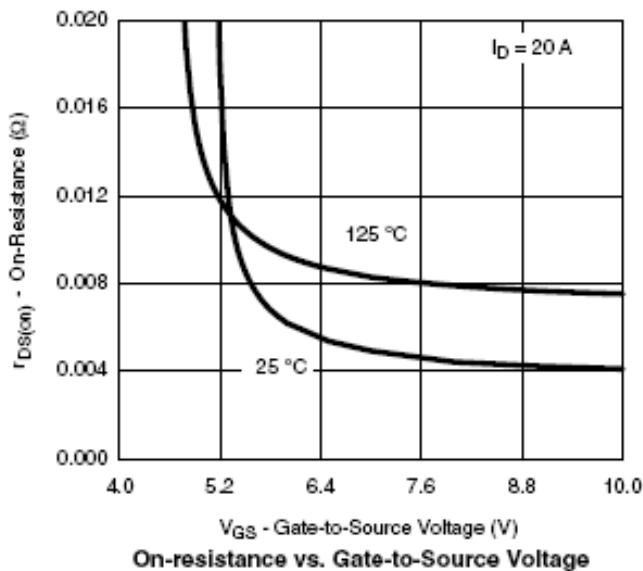
TYPICAL CHARACTERISTICS





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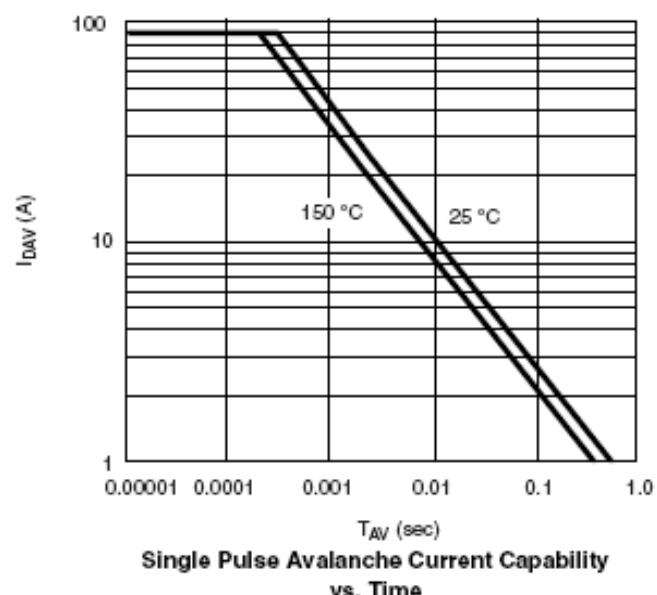
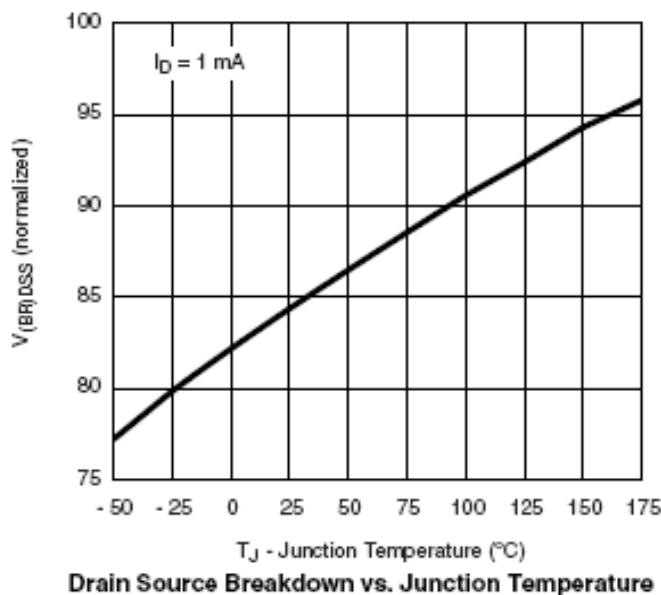
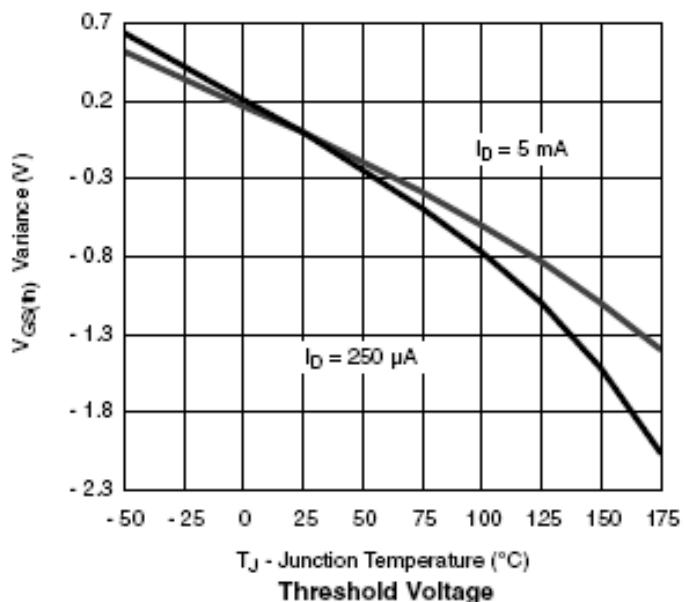
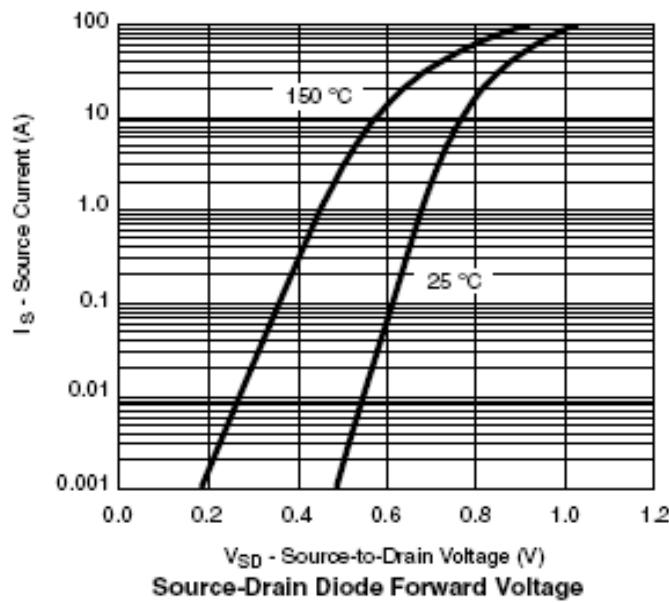




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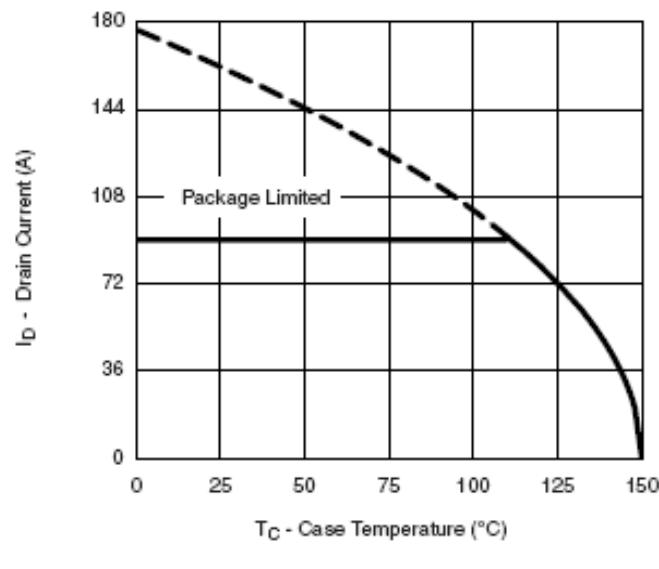




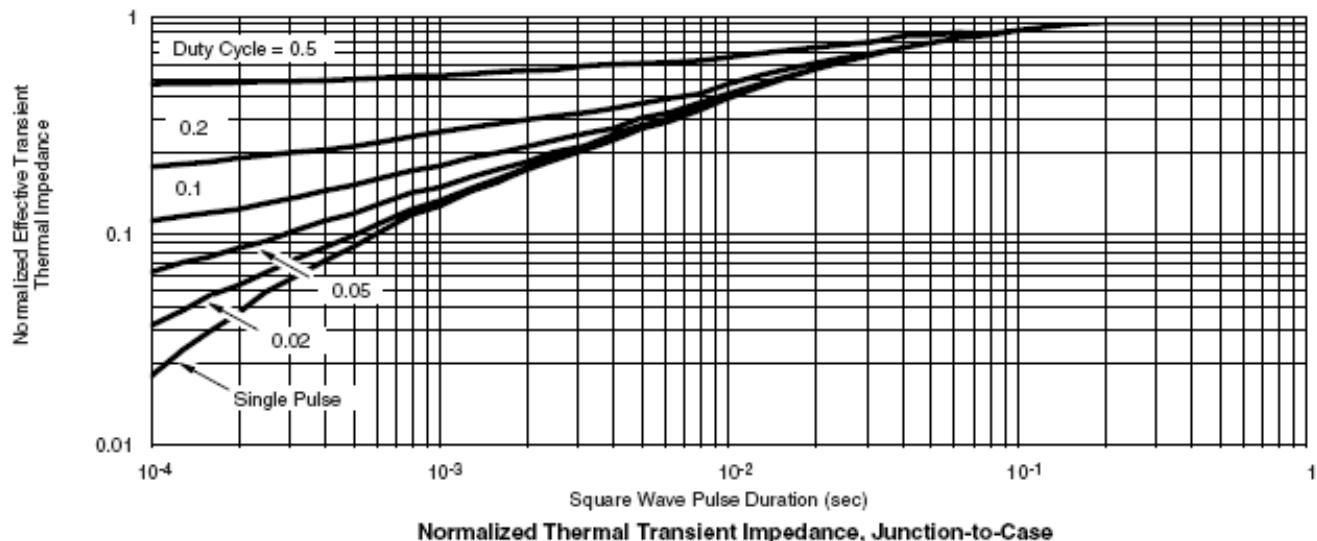
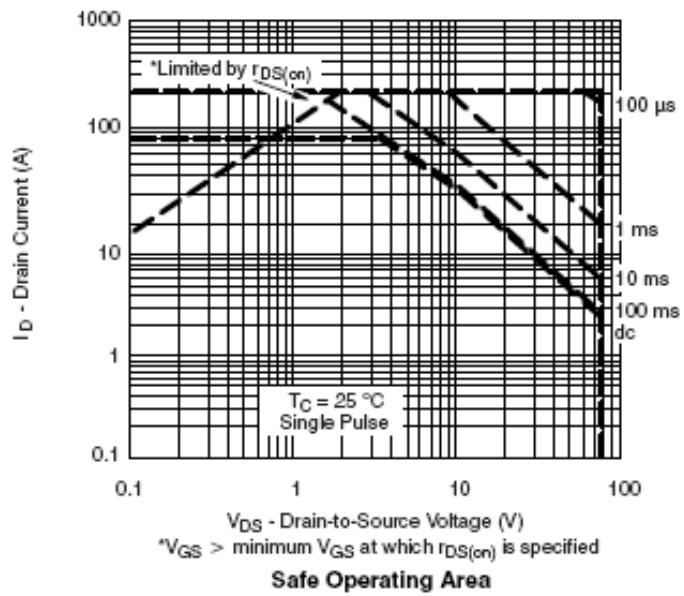
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TYPICAL CHARACTERISTICS



Maximum Drain Current vs. Case Temperature

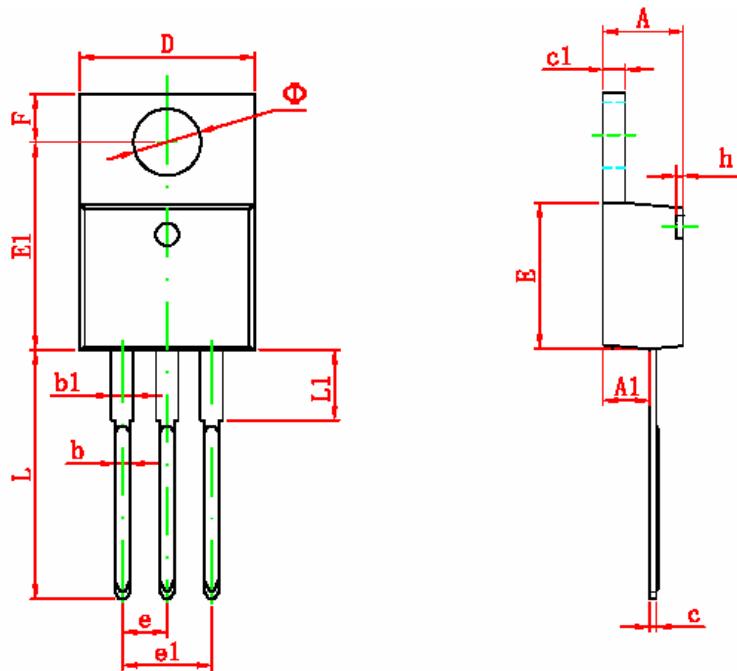




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TO-220-3L PACKAGE OUTLINE



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	4.470	4.670	0.176	0.184
A1	2.520	2.820	0.099	0.111
b	0.710	0.910	0.028	0.036
b1	1.170	1.370	0.046	0.054
c	0.310	0.530	0.012	0.021
c1	1.170	1.370	0.046	0.054
D	10.010	10.310	0.394	0.406
E	8.500	8.900	0.335	0.350
E1	12.060	12.460	0.475	0.491
e	2.540 TYP		0.100 TYP	
e1	4.980	5.180	0.196	0.204
F	2.590	2.890	0.102	0.114
h	0.000	0.300	0.000	0.012
L	13.400	13.800	0.528	0.543
L1	3.560	3.960	0.140	0.156
• •	3.735	3.935	0.147	0.155



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