

RoHS Compliant Product

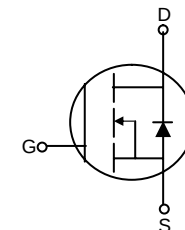
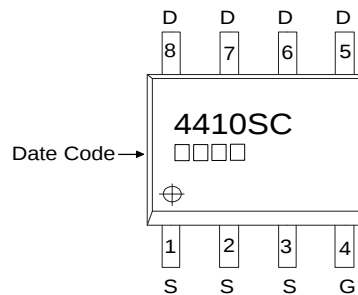
Description

The SSG4410 provide the designer with the best Combination of fast switching, ruggedized device design, Ultra low on-resistance and cost-effectiveness.

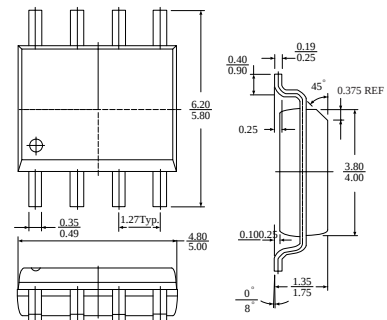
The SOP-8 is universally preferred for all commercial industrial surface mount application and suited for low voltage applications such as DC/DC converters.

Features

- * Dynamic dv/dt Rating
- * Simple drive requirement
- * Repetitive avalanche rated
- * Fast switching



SOP-8



Dimensions in millimeters

Absolute Maximum Ratings

Parameter	Symbol	Ratings	Unit
Drain-Source Voltage	V_{DS}	30	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current, $V_{GS}@10V$	$I_D@T_A=25^\circ C$	10	A
Continuous Drain Current, $V_{GS}@10V$	$I_D@T_A=70^\circ C$	8	A
Pulsed Drain Current ¹	I_{DM}	50	A
Total Power Dissipation	$P_D@T_A=25^\circ C$	2.5	W
Linear Derating Factor		0.02	W/ $^\circ C$
Operating Junction and Storage Temperature Range	T_J, T_{stg}	-55~+150	$^\circ C$

Thermal Data

Parameter	Symbol	Ratings	Unit
Thermal Resistance Junction-ambient Max.	R_{thj-a}	50	$^\circ C/W$

Electrical Characteristics($T_j=25^{\circ}\text{C}$ Unless otherwise specified)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Condition
Drain-Source Breakdown Voltage	BV_{DSS}	30	—	—	V	$V_{GS}=0V, I_D=250\mu A$
Breakdown Voltage Temp. Coefficient	$\Delta BV_{DS}/\Delta T_j$	—	0.037	—	V/ $^{\circ}\text{C}$	Reference to $25^{\circ}\text{C}, I_D=1mA$
Gate Threshold Voltage	$V_{GS(th)}$	1.0	—	3.0	V	$V_{DS}=V_{GS}, I_D=250\mu A$
Gate-Source Leakage Current	I_{GSS}	—	—	± 100	nA	$V_{GS}=\pm 20V$
Drain-Source Leakage Current ($T_j=25^{\circ}\text{C}$)	I_{DSS}	—	—	1	μA	$V_{DS}=30V, V_{GS}=0$
Drain-Source Leakage Current ($T_j=70^{\circ}\text{C}$)		—	—	25	μA	$V_{DS}=24V, V_{GS}=0$
Static Drain-Source On-Resistance ²	$R_{DS(ON)}$	—	11.5	13.5	m Ω	$V_{GS}=10V, I_D=10A$
		—	16.5	20		$V_{GS}=4.5V, I_D=5A$
Total Gate Charge ²	Q_g	—	20	—	nC	$I_D=10A$ $V_{DS}=15V$ $V_{GS}=5V$
Gate-Source Charge	Q_{gs}	—	3	—		
Gate-Drain ("Miller") Charge	Q_{gd}	—	11	—		
Turn-on Delay Time ²	$T_{d(ON)}$	—	7.5	—	nS	$V_{DD}=25V$ $I_D=1A$ $V_{GS}=5V$ $R_G=3.3\Omega$ $R_D=25\Omega$
Rise Time	T_r	—	10.2	—		
Turn-off Delay Time	$T_{d(OFF)}$	—	29	—		
Fall Time	T_f	—	33	—		
Input Capacitance	C_{iss}	—	955	—	pF	$V_{GS}=0V$ $V_{DS}=15V$ $f=1.0MHz$
Output Capacitance	C_{oss}	—	555	—		
Reverse Transfer Capacitance	C_{rss}	—	204	—		
Forward Transconductance	G_{fs}	—	20	—	S	$V_{DS}=15V, I_D=10A$

Source-Drain Diode

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Condition
Forward On Voltage ²	V_{DS}	—	—	1.3	V	$I_S=1.7A, V_{GS}=0V.$
Continuous Source Current (Body Diode)	I_S	—	—	2.3	A	$V_D=V_G=0V, V_S=1.3V$
Pulsed Source Current (Body Diode)	I_{SM}	—	—	50	A	

Notes: 1.Pulse width limited by safe operating area.

2.Pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$.

Characteristics Curve

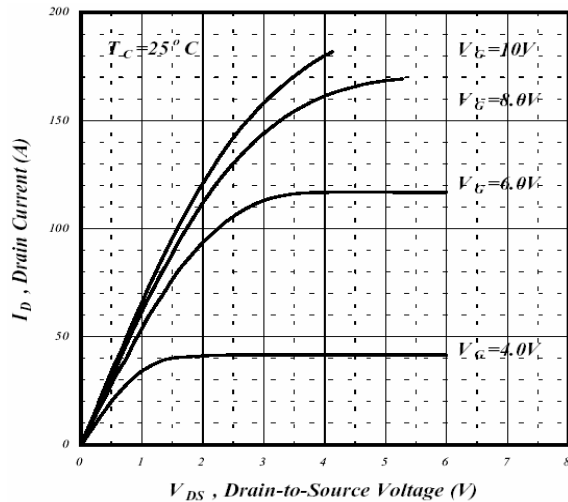


Fig 1. Typical Output Characteristics

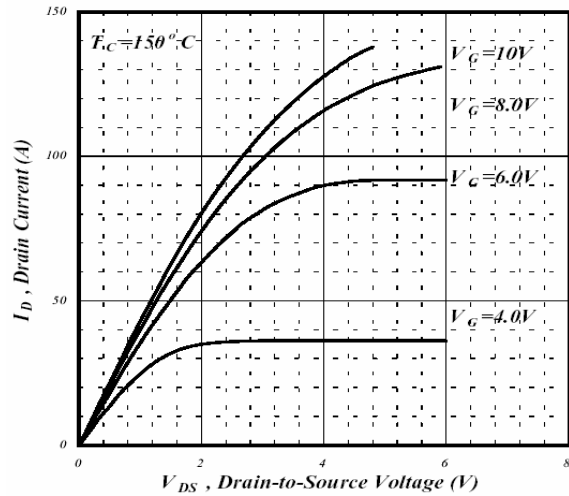


Fig 2. Typical Output Characteristics

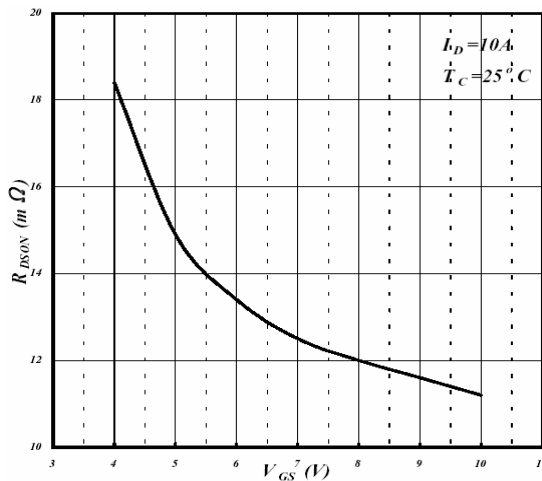


Fig 3. On-Resistance v.s. Gate Voltage

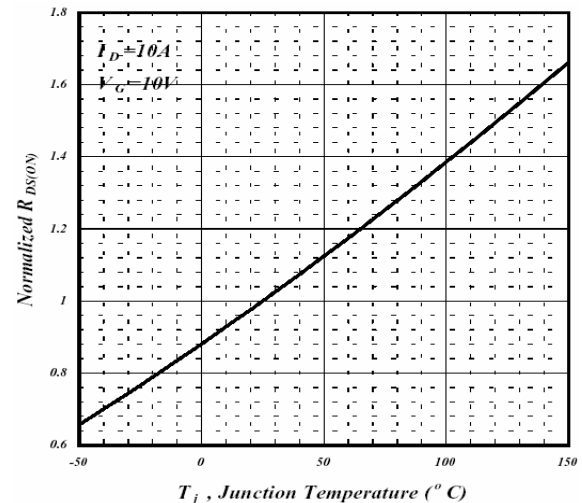


Fig 4. Normalized On-Resistance v.s. Junction Temperature

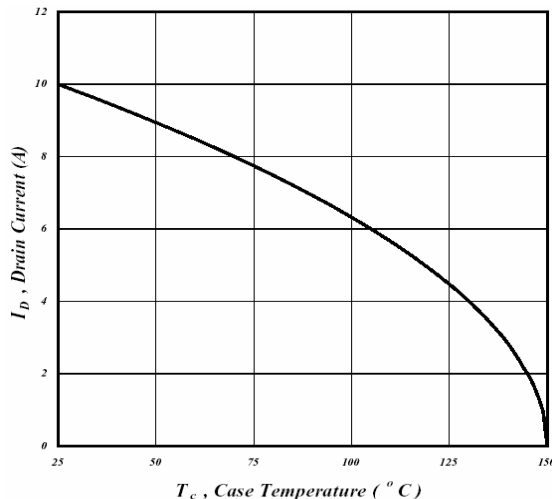


Fig 5. Maximum Drain Current v.s. Case Temperature

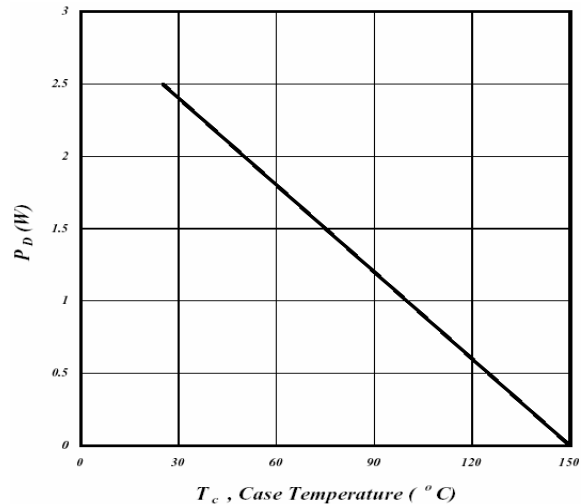


Fig 6. Type Power Dissipation

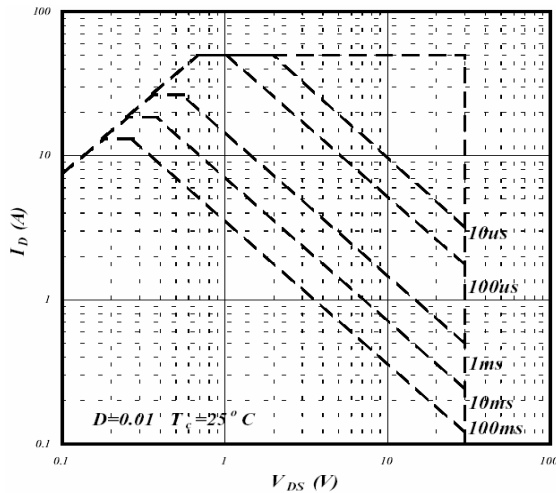


Fig 7. Maximum Safe Operating Area

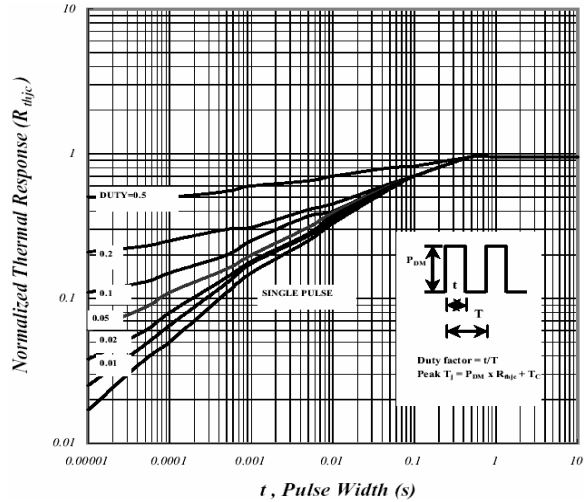


Fig 8. Effective Transient Thermal Impedance

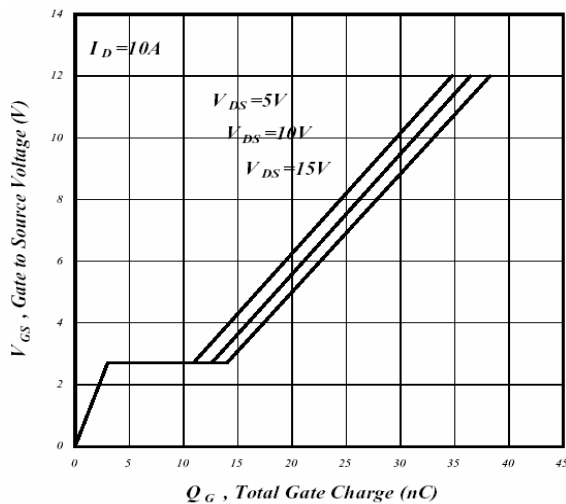


Fig 9. Gate Charge Characteristics

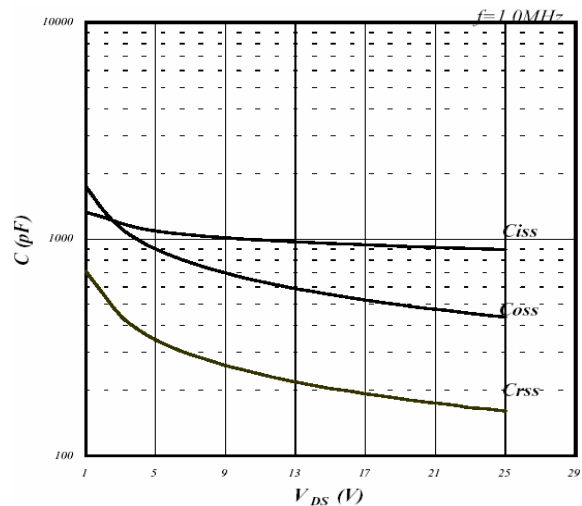


Fig 10. Typical Capacitance Characteristics

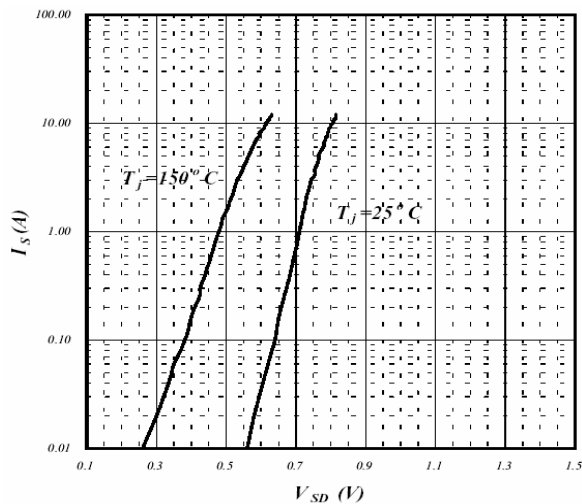


Fig 11. Forward Characteristics of Reverse Diode

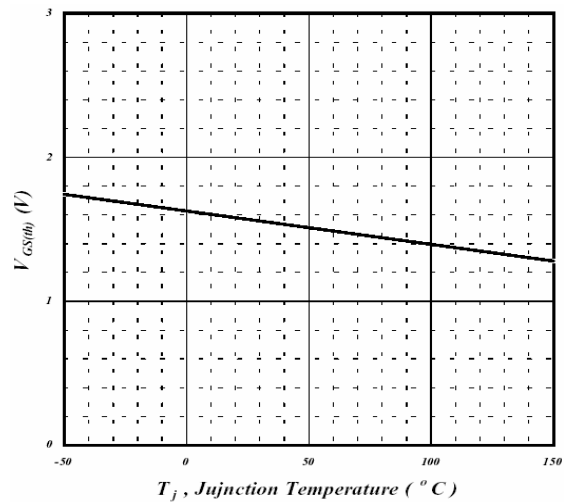


Fig 12. Gate Threshold Voltage v.s. Junction Temperature

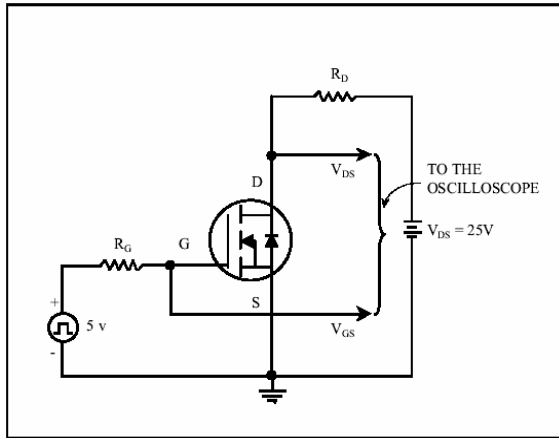


Fig 13. Switching Time Circuit

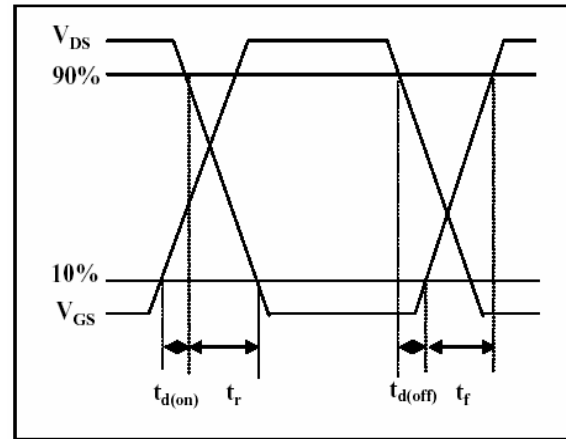


Fig 14. Switching Time Waveform

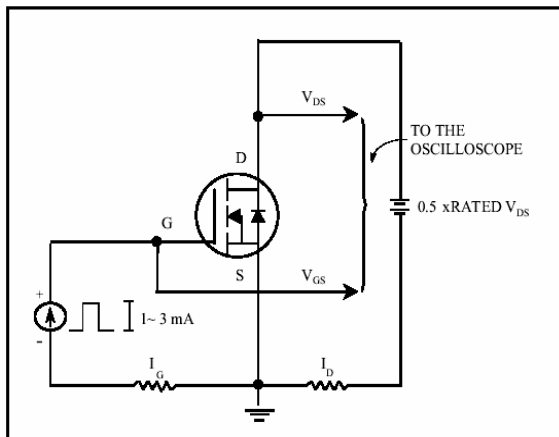


Fig 15. Gate Charge Circuit

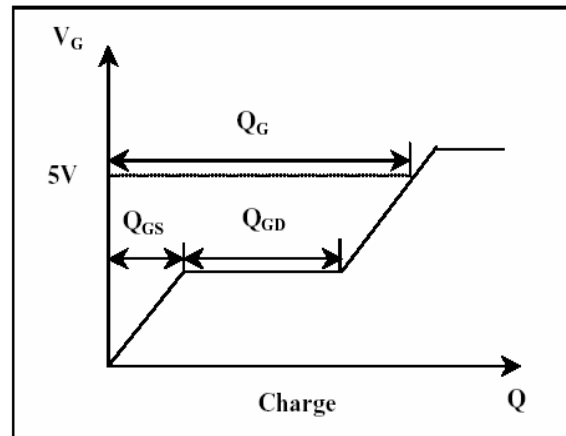


Fig 16. Gate Charge Waveform