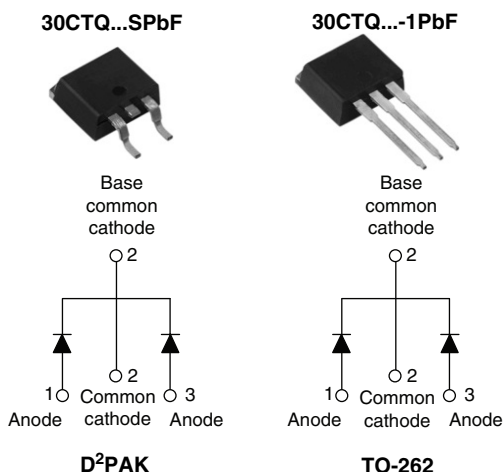


### Schottky Rectifier, 2 x 15 A



#### FEATURES

- 175 °C T<sub>J</sub> operation
- Center tap configuration
- Low forward voltage drop
- High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Guard ring for enhanced ruggedness and long term reliability
- Lead (Pb)-free ("PbF" suffix)
- Designed and qualified for Q101 level



**RoHS\***  
COMPLIANT

#### DESCRIPTION

This center tap Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

#### PRODUCT SUMMARY

I <sub>F(AV)</sub>	2 x 15 A
V <sub>R</sub>	80/100 V

#### MAJOR RATINGS AND CHARACTERISTICS

SYMBOL	CHARACTERISTICS	VALUES	UNITS
I <sub>F(AV)</sub>	Rectangular waveform	30	A
V <sub>RRM</sub>		80/100	V
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	850	A
V <sub>F</sub>	15 Apk, T <sub>J</sub> = 125 °C (per leg)	0.67	V
T <sub>J</sub>	Range	- 55 to 175	°C

#### VOLTAGE RATINGS

PARAMETER	SYMBOL	30CTQ080SPbF 30CTQ080-1PbF	30CTQ100SPbF 30CTQ100-1PbF	UNITS
Maximum DC reverse voltage	V <sub>R</sub>	80	100	V
Maximum working peak reverse voltage	V <sub>RWM</sub>			

#### ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum average forward current per device See fig. 5	I <sub>F(AV)</sub>	50 % duty cycle at T <sub>C</sub> = 129 °C, rectangular waveform	30	A
			15	
Maximum peak one cycle non-repetitive surge current per leg See fig. 7	I <sub>FSM</sub>	5 μs sine or 3 μs rect. pulse	850	
		10 ms sine or 6 ms rect. pulse	275	
Non-repetitive avalanche energy per leg	E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 0.50 A, L = 60 mH	7.50	mJ
Repetitive avalanche current per leg	I <sub>AR</sub>	Current decaying linearly to zero in 1 μs Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>R</sub> typical	0.50	A

\* Pb containing terminations are not RoHS compliant, exemptions may apply

ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS	
Maximum forward voltage drop per leg See fig. 1	$V_{FM}^{(1)}$	15 A	$T_J = 25\text{ }^{\circ}\text{C}$	0.86	V	
		30 A		1.05		
		15 A	$T_J = 125\text{ }^{\circ}\text{C}$	0.67		0.82
		30 A				
Maximum reverse leakage current per leg See fig. 2	$I_{RM}^{(1)}$	$T_J = 25\text{ }^{\circ}\text{C}$	$V_R = \text{Rated } V_R$	0.55	mA	
		$T_J = 125\text{ }^{\circ}\text{C}$		7.0		
Maximum junction capacitance per leg	$C_T$	$V_R = 5\text{ }V_{DC}$ (test signal range 100 kHz to 1 MHz) $25\text{ }^{\circ}\text{C}$		500	pF	
Typical series inductance per leg	$L_S$	Measured lead to lead 5 mm from package body		8.0	nH	
Maximum voltage rate of change	dV/dt	Rated $V_R$		10 000	V/ $\mu$ s	

**Note**(1) Pulse width < 300  $\mu$ s, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		- 55 to 175	°C
Maximum thermal resistance, junction to case per leg	R <sub>thJC</sub>	DC operation	3.25	°C/W
Maximum thermal resistance, junction to case per package			1.63	
Typical thermal resistance, case to heatsink	R <sub>thCS</sub>	Mounting surface, smooth and greased	0.50	
Approximate weight			2	g
			0.07	oz.
Mounting torque	minimum		6 (5)	kgf · cm (lbf · in)
	maximum		12 (10)	
Marking device		Case style D <sup>2</sup> PAK	30CTQ100S	
		Case style TO-262	30CTQ100-1	

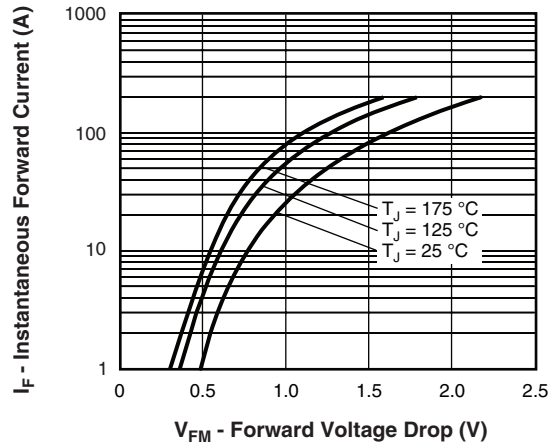


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

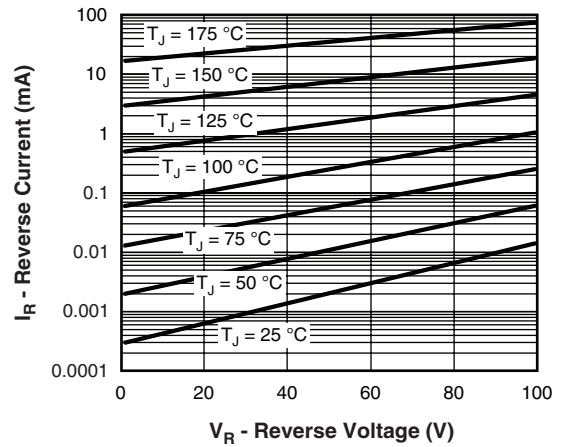


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

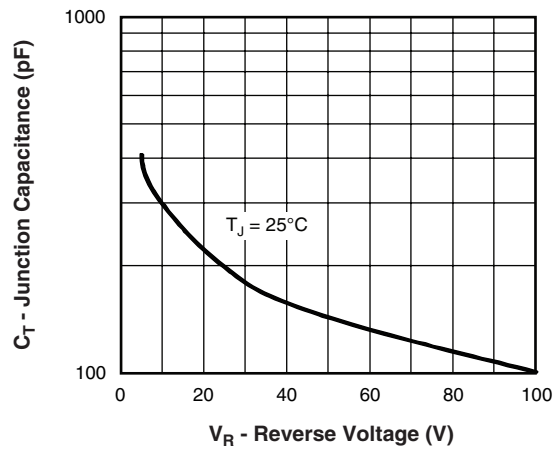


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

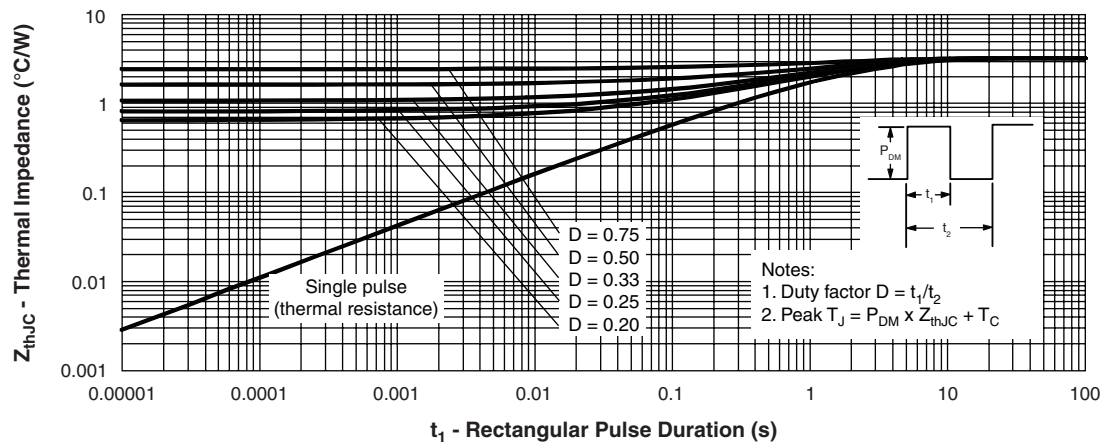


Fig. 4 - Maximum Thermal Impedance  $Z_{thJC}$  Characteristics (Per Leg)

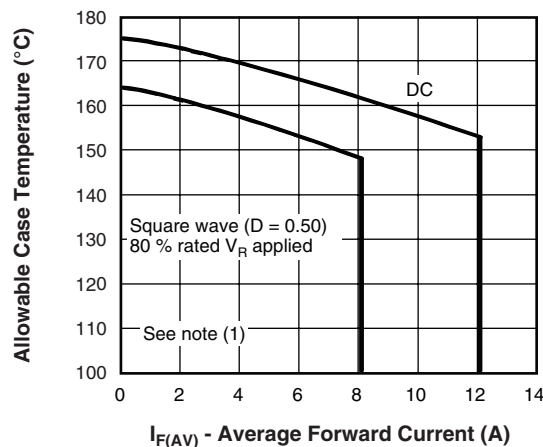


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

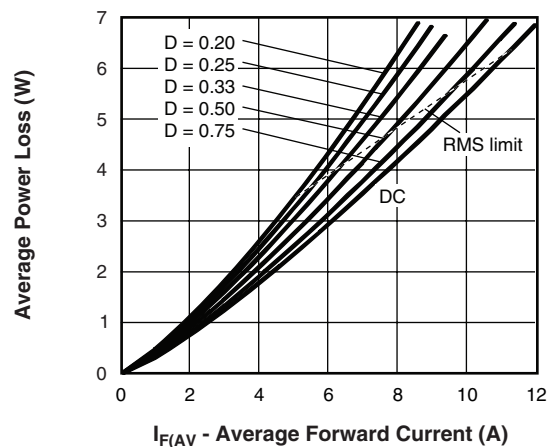


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

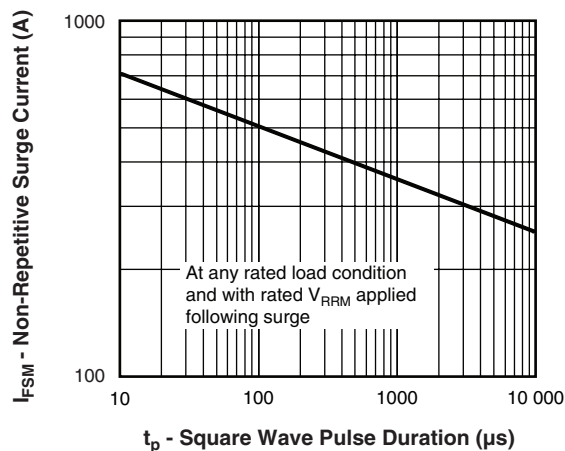


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)



Fig. 8 - Unclamped Inductive Test Circuit

### Note

- (1) Formula used:  $T_C = T_J - (P_d + P_{dREV}) \times R_{thJC}$ ;  
 $P_d$  = Forward power loss =  $I_{F(AV)} \times V_{FM}$  at  $(I_{F(AV)}/D)$  (see fig. 6);  
 $P_{dREV}$  = Inverse power loss =  $V_{R1} \times I_R (1 - D)$ ;  $I_R$  at  $V_{R1} = 10$  V

**ORDERING INFORMATION TABLE**

Device code	<b>30</b>	<b>C</b>	<b>T</b>	<b>Q</b>	<b>100</b>	<b>S</b>	<b>TRL</b>	<b>PbF</b>
	①	②	③	④	⑤	⑥	⑦	⑧

- |          |   |  |
|----------|---|--|
| <b>1</b> | - | Current rating (30 A)  |
| <b>2</b> | - | Circuit configuration:<br>C = Common cathode   |
| <b>3</b> | - | T = TO-220   |
| <b>4</b> | - | Schottky "Q" series  |
| <b>5</b> | - | Voltage ratings  |
| <b>6</b> | - | • S = D <sup>2</sup> PAK<br>• -1 = TO-262  |
| <b>7</b> | - | • None = Tube (50 pieces)<br>• TRL = Tape and reel (left oriented - for D <sup>2</sup> PAK only)<br>• TRR = Tape and reel (right oriented - for D <sup>2</sup> PAK only) |
| <b>8</b> | - | • None = Standard production<br>• PbF = Lead (Pb)-free   |
- 080 = 80 V  
100 = 100 V

LINKS TO RELATED DOCUMENTS	
Dimensions	<a href="http://www.vishay.com/doc?95014">http://www.vishay.com/doc?95014</a>
Part marking information	<a href="http://www.vishay.com/doc?95008">http://www.vishay.com/doc?95008</a>
Packaging information	<a href="http://www.vishay.com/doc?95032">http://www.vishay.com/doc?95032</a>



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