

Silicon NPN Power Transistors

2SC3557

DESCRIPTION

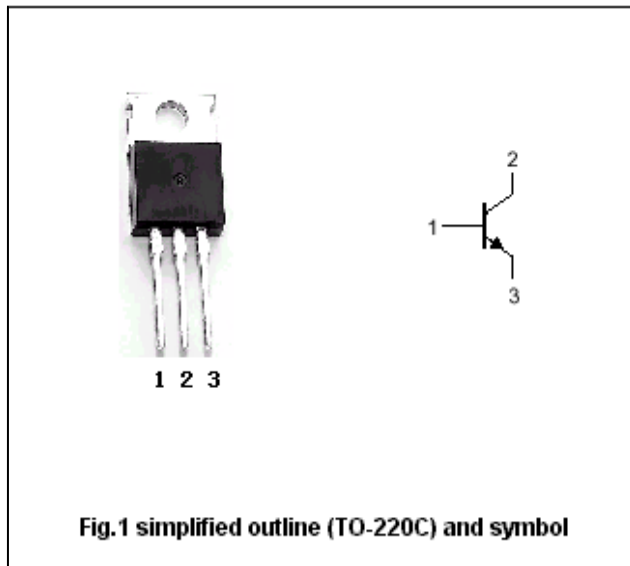
- With TO-220C package
- Low collector saturation voltage

APPLICATIONS

- For medium power linear and switching applications

PINNING

PIN	DESCRIPTION
1	Base
2	Collector;connected to mounting base
3	Emitter



Absolute maximum ratings (Ta=25℃)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V_{CBO}	Collector-base voltage	Open emitter	80	V
V_{CEO}	Collector-emitter voltage	Open base	80	V
V_{EBO}	Emitter-base voltage	Open collector	5	V
I_C	Collector current		4	A
P_C	Collector power dissipation	$T_C=25^{\circ}\text{C}$	40	W
T_j	Junction temperature		150	$^{\circ}\text{C}$
T_{stg}	Storage temperature		-55~150	$^{\circ}\text{C}$

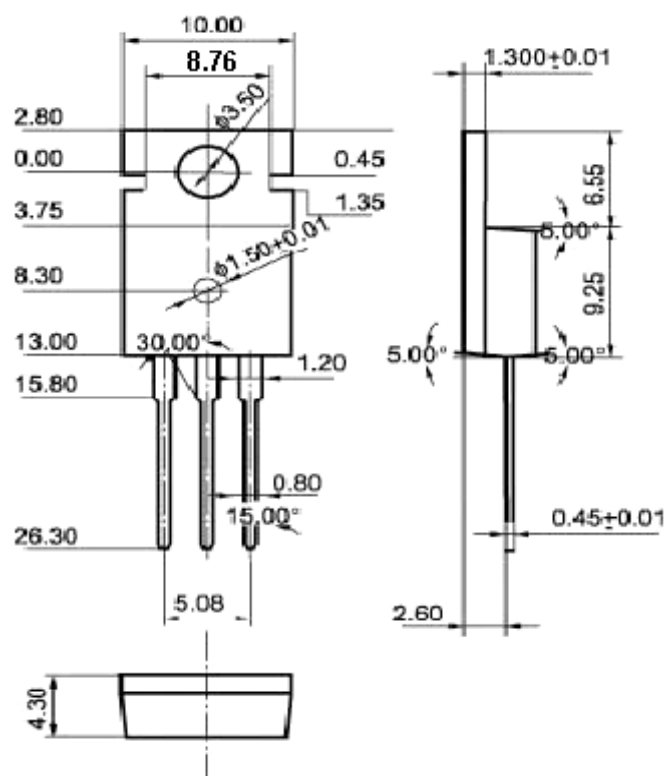
Silicon NPN Power Transistors**2SC3557****CHARACTERISTICS****T_j=25°C unless otherwise specified**

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{CEO(SUS)}	Collector-emitter sustaining voltage	I _C =30mA; I _B =0	80			V
V _{(BR)EBO}	Emitter-base breakdown voltage	I _E =1mA; I _C =0	5			V
V _{CEsat}	Collector-emitter saturation voltage	I _C =3A; I _B =0.3 A			1.5	V
I _{CEO}	Collector cut-off current	V _{CE} =60V; I _B =0			0.5	mA
I _{CBO}	Collector cut-off current	V _{CB} =80V; I _E =0			0.1	mA
I _{EBO}	Emitter cut-off current	V _{EB} =5V; I _C =0			0.1	mA
h _{FE}	DC current gain	I _C =0.2A ; V _{CE} =1V	150			

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PACKAGE OUTLINE

Fig.2 Outline dimensions (unindicated tolerance: ± 0.10 mm)