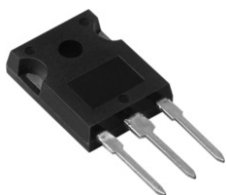
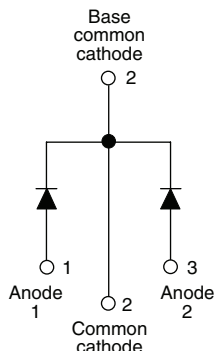


Schottky Rectifier, 2 x 20 A


TO-247AC


FEATURES

- 150 °C T_J operation
- Center tap TO-247 package
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Very low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Designed and qualified for industrial level

DESCRIPTION

The 40L..CW center tap Schottky rectifier has been optimized for very low forward voltage drop with moderate leakage. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in parallel switching power supplies.

PRODUCT SUMMARY

$I_{F(AV)}$	2 x 20 A
V_R	40/45 V

MAJOR RATINGS AND CHARACTERISTICS

SYMBOL	CHARACTERISTICS	VALUES	UNITS
$I_{F(AV)}$	Rectangular waveform	40	A
V_{RRM}		40/45	V
I_{FSM}	$t_p = 5 \mu s$ sine	1240	A
V_F	20 Apk, $T_J = 125^\circ C$ (per leg, typical)	0.42	V
T_J		- 55 to 150	°C

VOLTAGE RATINGS

PARAMETER	SYMBOL	40L40CW	40L45CW	UNITS
Maximum DC reverse voltage	V_R	40	45	V
Maximum working peak reverse voltage	V_{RWM}			

ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum average forward current See fig. 5	$I_{F(AV)}$	50 % duty cycle at $T_C = 122^\circ C$, rectangular waveform	20	A
			40	
Maximum peak one cycle non-repetitive surge current per leg See fig. 7	I_{FSM}	5 μs sine or 3 μs rect. pulse	1240	
		10 ms sine or 6 ms rect. pulse	350	
Non-repetitive avalanche energy per leg	E_{AS}	$T_J = 25^\circ C$, $I_{AS} = 3 A$, $L = 4.4 mH$	20	mJ
Repetitive avalanche current per leg	I_{AR}	Current decaying linearly to zero in 1 μs Frequency limited by T_J maximum $V_A = 1.5 \times V_R$ typical	3	A

ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS		TYP.	MAX.	UNITS
Maximum forward voltage drop per leg See fig. 1	$V_{FM}^{(1)}$	20 A	$T_J = 25\text{ }^{\circ}\text{C}$	0.48	0.53	V
		40 A		0.61	0.69	
		20 A	$T_J = 125\text{ }^{\circ}\text{C}$	0.42	0.49	
		40 A		0.60	0.70	
Reverse leakage current per leg See fig. 2	$I_{RM}^{(1)}$	$T_J = 25\text{ }^{\circ}\text{C}$	$V_R = \text{Rated } V_R$	-	1.5	mA
		$T_J = 100\text{ }^{\circ}\text{C}$		20	80	
Threshold voltage	$V_{F(TO)}$	$T_J = T_J \text{ maximum}$		0.27		V
Forward slope resistance	r_t			8.72		mΩ
Maximum junction capacitance per leg	C_T	$V_R = 5\text{ }V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		-	1500	pF
Typical series inductance per leg	L_S	Measured lead to lead 5 mm from package body		7.5	-	nH
Maximum voltage rate of change	dV/dt	Rated V_R		10 000		V/μs

Note

(1) Pulse width < 300 μs , duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T_J, T_{Stg}		- 55 to 150	$^{\circ}\text{C}$
Maximum thermal resistance, junction to case per leg	R_{thJC}	DC operation See fig. 4	1.6	$^{\circ}\text{C/W}$
Maximum thermal resistance, junction to case per package		DC operation	0.8	
Typical thermal resistance, case to heatsink	R_{thCS}	Mounting surface, smooth and greased	0.24	
Approximate weight			6	g
			0.21	oz.
Mounting torque	minimum	Non-lubricated threads	6 (5)	kgf · cm
	maximum		12 (10)	(lbf · in)
Marking device		Case style TO-247AC (JEDEC)	40L40CW	
			40L45CW	

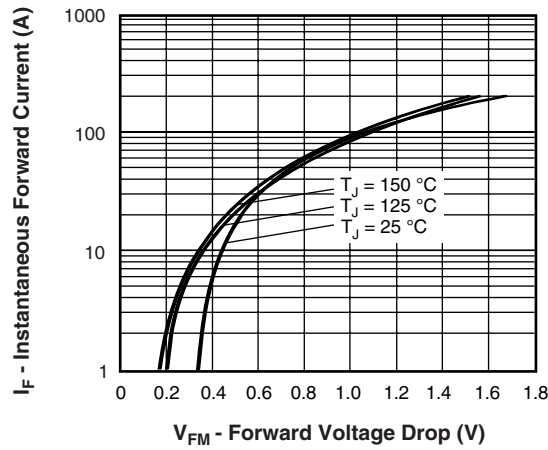


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

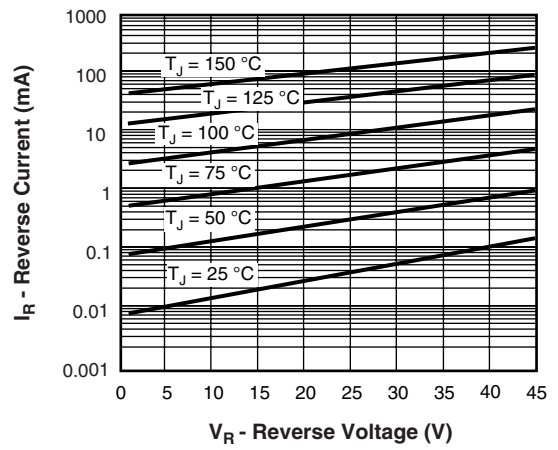


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

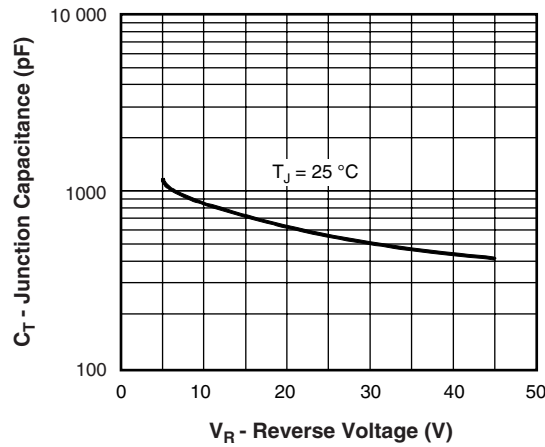


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

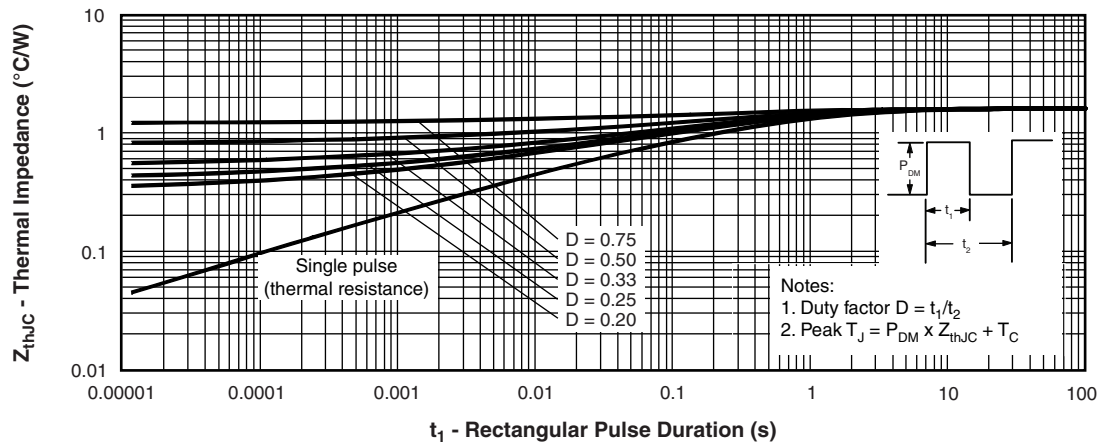


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

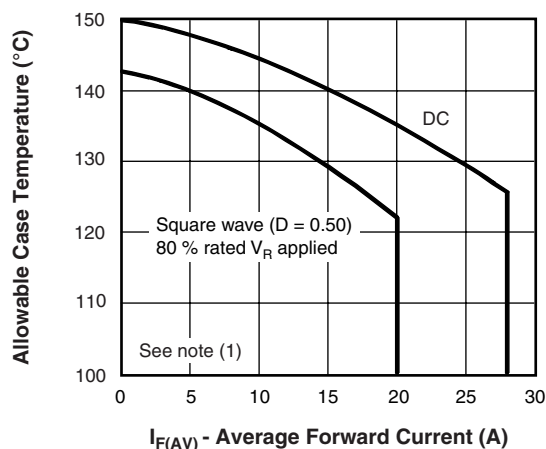


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

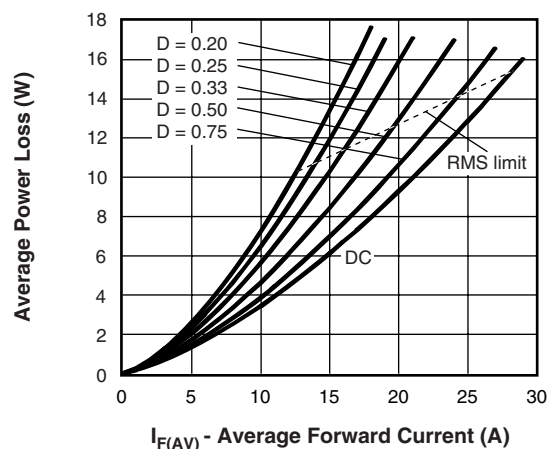


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

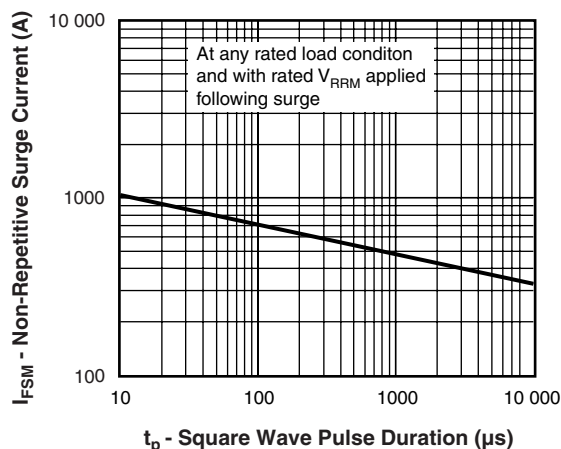


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

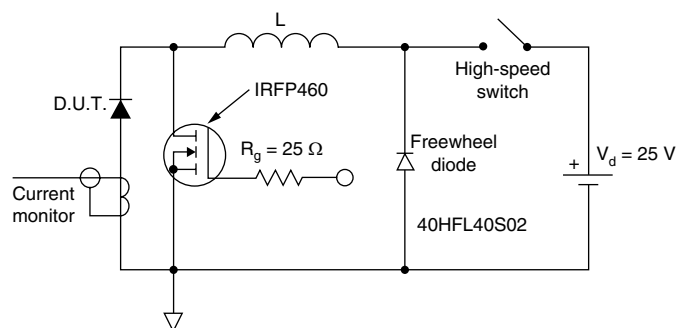


Fig. 8 - Unclamped Inductive Test Circuit

Note

- (1) Formula used: $T_C = T_J - (P_d + P_{dREV}) \times R_{thJC}$;
 P_d = Forward power loss = $I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 6);
 P_{dREV} = Inverse power loss = $V_{R1} \times I_R (1 - D)$; I_R at $V_{R1} = 80\%$ rated V_R

**ORDERING INFORMATION TABLE**

Device code	40	L	45	C	W	-
	1	2	3	4	5	6
	1	-	Current rating (40 = 40 A)			
	2	-	Schottky "L" series			
	3	-	Voltage code			40 = 40 V 45 = 45 V
	4	-	Circuit configuration: C = Common cathode			
	5	-	Package: W = TO-247			
	6	-	• None = Standard production • PbF = Lead (Pb)-free			

Tube standard pack quantity: 25 pieces

LINKS TO RELATED DOCUMENTS	
Dimensions	http://www.vishay.com/doc?95223
Part marking information	http://www.vishay.com/doc?95226



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