

40 PIN SMD ETHERNET 10 BASE-T QUAD PORT TRANSFORMER

P/N:40ST8062A DATA SHEET

A.GENERAL SPECIFICATIONS

- 1.IEEE 802.3 ETHERNET COMPATIBLE, SUITABLE FOR “FILTERLESS”TRANSCEIVER CHIP (LEVEL ONE CHIP)
- 2.SURFACE MOUNTABLE
- 3.DESIGN FOR MULTIPORT REPEATER AND ETHERNET SWITCHING HUB APPLICATION
- 4.CUSTOMER DESIGNS AVAILABLE FOR SPECIFIC APPLICATION
5. ISOLATION 1500V_{rms}

B.ELECTRICAL SPECIFICATIONS @ 25°C:

- 1.PEAK PULSE RATING: 50 Volts
- 2.DISSIPATION RATING: 75 mW.
- 3.INSULATION RESISTANCE: 10,000 M OHM@ 50 Vdc.
- 4.TURN RATIO (±5%): TX=1CT: 1.41CT; RX=1:1.
- 5.PRI.OCL: TX & RX: 150uH MIN.
- 6.Cw/w: 18 pF MAX (TX & RX).
- 7.LEAKAGE INDUCTANCE: 0.4 uH MAX (TX & RX).
- 8.PRI.RESISTANCE: 0.3Ω Max(TX & RX).

C.DIMENSIONS & SCHEMATIC & MARKING (UNIT mm Tolerance +/-0.25):

