

PLEASE CHECK WWW.MOLEX.COM FOR LATEST PART INFORMATION

Part Number: [0458450001](#)
Status: **Active**
Overview: [edge_card_connectors](#)
Description: 2.50mm (.098") Pitch EXTreme PowerEdge™, Signal Connector, Double Sided, 2 Segments, 8 Signal Contacts per Segment, Press-Fit

Documents:
[3D Model](#) [Product Specification PS-45719-001 \(PDF\)](#)
[Drawing \(PDF\)](#) [RoHS Certificate of Compliance \(PDF\)](#)

Agency Certification

CSA LR19980
TUV 30482572

General

Product Family Edge Card Connectors
Series [45845](#)
Component Type Edgecard to PCB
Overview [edge_card_connectors](#)
Product Name EXTreme PowerEdge™

Physical

Circuits (Loaded) 16
Circuits (maximum) 16
Circuits Detail All Signal
Color - Resin Black
Durability (mating cycles max) 250
Entry Angle Vertical (Top Entry)
Flammability 94V-0
Keying to Mating Part N/A
Material - Metal Phosphor Bronze
Material - Plating Mating Gold
Material - Plating Termination Tin
Material - Resin High Temperature Thermoplastic
PC Tail Length (in) 0.177 In
PC Tail Length (mm) 4.50 mm
PCB Thickness Recommended (in) 0.093 In
PCB Thickness Recommended (mm) 2.40 mm
Packaging Type Tray
Pitch - Mating Interface (in) 0.098 In
Pitch - Mating Interface (mm) 2.50 mm
Plating min: Mating (µin) 30
Plating min: Mating (µm) 0.75
Plating min: Termination (µin) 150
Plating min: Termination (µm) 3.75
Temperature Range - Operating -40°C to +105°C
Termination Interface: Style Through Hole - Compliant Pin

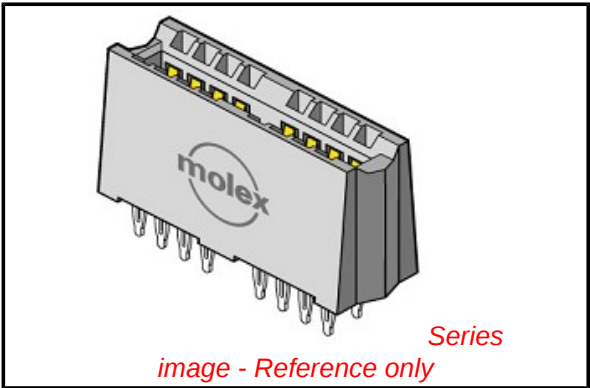
Electrical

Current - Maximum per Contact 3A
Voltage - Maximum 250V

Material Info

Reference - Drawing Numbers

Packaging Specification PK-45714-001
Product Specification PS-45719-001



EU RoHS
ELV and RoHS
Compliant
REACH SVHC
Contains SVHC: No
Halogen-Free
Status
Halogen-Free

China RoHS



Need more information on product environmental compliance?

Email productcompliance@molex.com
For a multiple part number RoHS Certificate of Compliance, [click here](#)

Please visit the [Contact Us](#) section for any non-product compliance questions.

Search Parts in this Series
[45845Series](#)

Mates With
1.58mm (.062") double sided card edge

This document was generated on 06/04/2010

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D

C

B

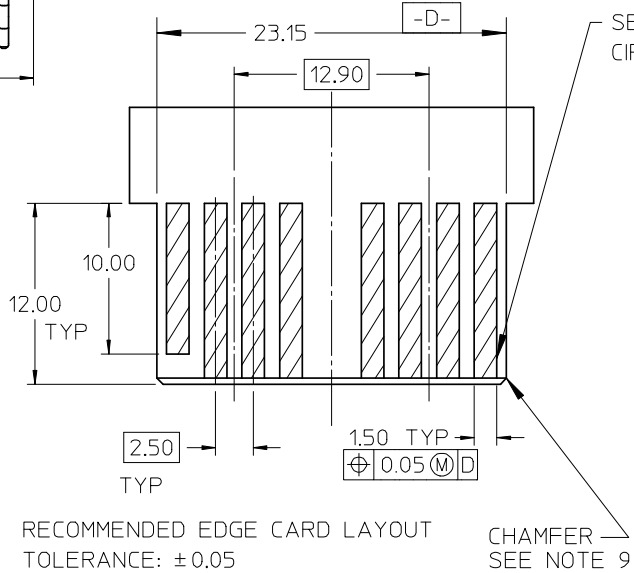
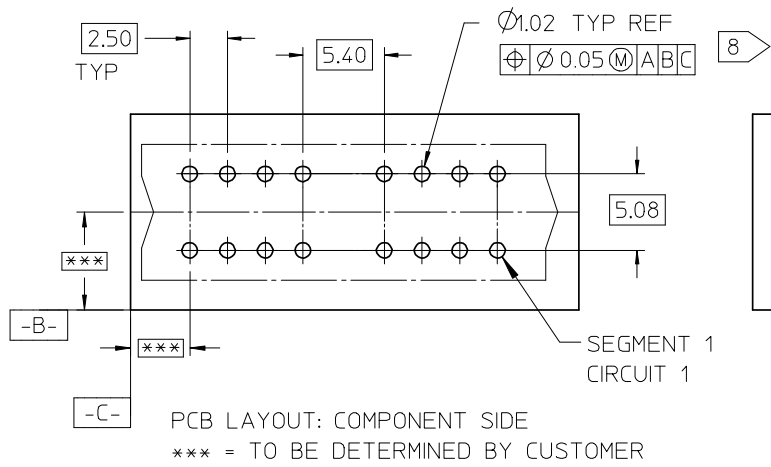
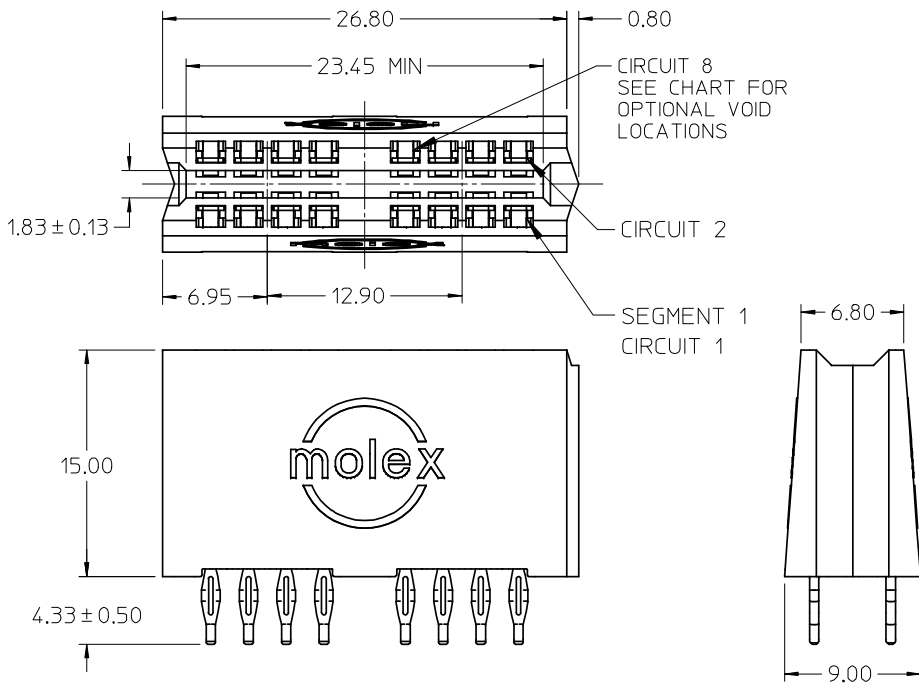
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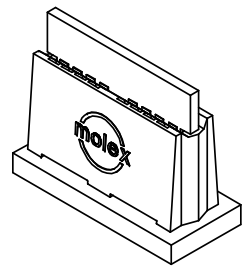
B

A



- NOTES:
1. MATERIAL: HOUSING 30% GF LCP, UL94V-0, BLACK
MATERIAL: TERMINAL C5210 ALLOY
 2. FINISH: SEE SHT 2 FOR FINISH SPECIFICATIONS
 3. PRODUCT SPECIFICATION: PS-45719-001.
 4. PACKAGING SPECIFICATION: PK-45714-001.
 5. THIS PART MATES WITH 1.60±0.16 THICK PCB OR BUSBAR
 6. DIMENSIONS GIVEN ACROSS CENTERLINES ARE SYMMETRICAL ABOUT THOSE CENTERLINES WITHIN HALF THE TOTAL TOLERANCE.
 7. PCB THICKNESS: 2.36 MIN
EDGE CARD THICKNESS: 1.57±0.15
 8. PCB HOLE SPECIFICATION: SEE PS-45719-001.
 9. 0.50 X 45° CHAMFER ON LEADING EDGE, TYP 4 SIDES.
 10. THIS PART IS RoHS COMPLIANT. SEE ES-45482-999 FOR LABEL REQUIREMENTS.

MATERIAL NUMBER	SEGMENTS	FINISH (SHT 2)	VOID LOCATIONS
45845-0001	2	1	NONE
45845-0002	3	1	NONE
45845-0003	4	1	NONE
45845-1001	2	1	SEG 1 CKT 7, 8
45845-1003	3	1	SEGS 1, 2 CKTS 5,6,15,16
45845-2001	2	2	SEG 1 CKT 7, 8
45845-3001	2	1	SEG 1 CKTS 3,4,7,8
45845-9001	2	3	NONE
45845-9003	4	3	NONE



ADD -1002	2008/04/16
EC NO: UCP2008-2498	DRWN: SCHAFER
CHKD: 2008/04/22	APPR: APATEL
2008/04/25	DESCRIPTION
H	REV

GENERAL TOLERANCES (UNLESS SPECIFIED)		
	mm	INCH
4 PLACES	± ---	± ---
3 PLACES	± ---	± ---
2 PLACES	± 0.25	± ---
1 PLACE	± ---	± ---
ANGULAR ± 1/2°		
DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS		

DIMENSION STYLE	
MM ONLY	
DRAWN BY	DATE
MARGULIS	2004/08/05
CHECKED BY	DATE
MARGULIS	2004/08/05
APPROVED BY	DATE
MARGULIS	2004/08/05
MATERIAL NUMBER	SEE CHART
SIZE	A

SCALE 2:1	DESIGN UNITS METRIC	 THIRD ANGLE PROJECTION
TITLE POWER EDGE ASSEMBLY SIGNAL VERTICAL CPI		
 MOLEX INCORPORATED		
DOCUMENT NO. SD-45845-001		SHEET NO. 1 OF 3
THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION		

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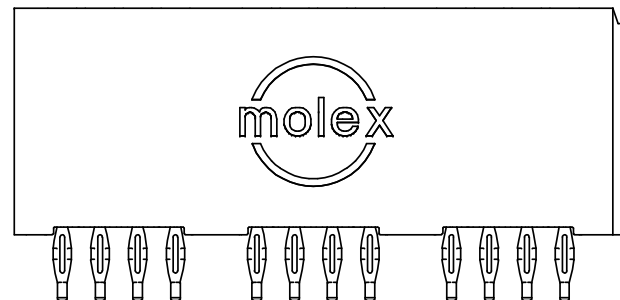
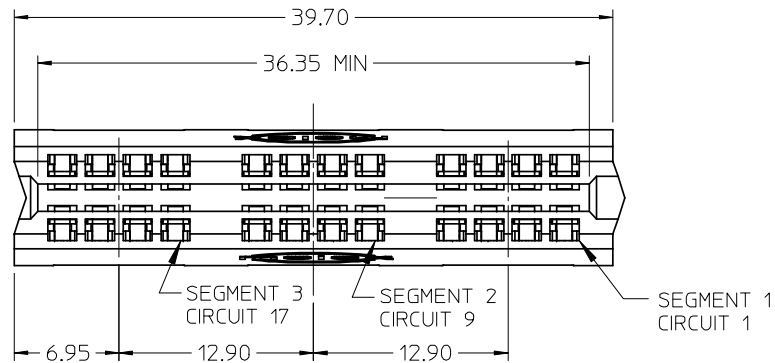
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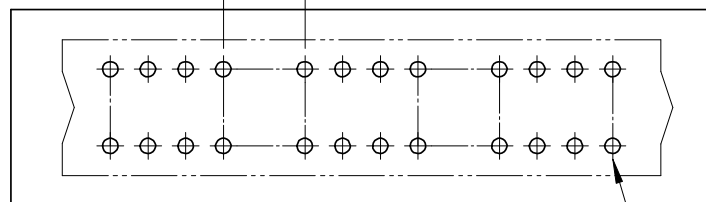
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5.40
TYP



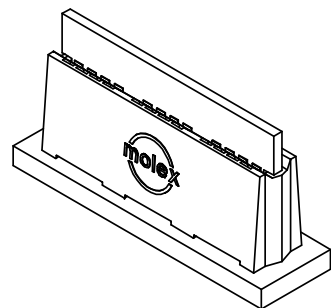
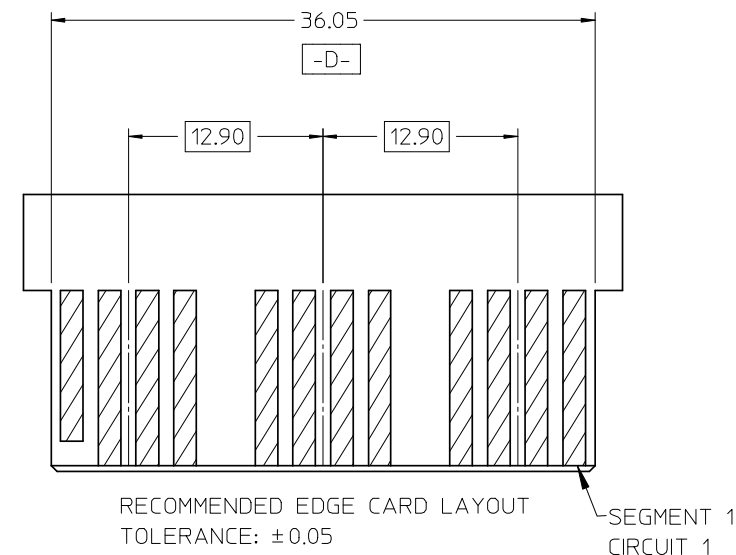
PCB LAYOUT: COMPONENT SIDE

SEGMENT 1
CIRCUIT 1

NOTES

1. FOR NOTES AND DIMENSIONS NOT SHOWN SEE SHEET 1

2. FINISH:

FINISH 1- SELECT GOLD 30 M.I.MIN IN CONTACT AREA
SELECT TIN 100 M.I.MIN IN PC TAIL AREA
BOTH OVER NICKEL 50 M.I.MIN OVERALLFINISH 2- SELECT GOLD PLATE OVER 100-200 M.I.
Pd/Ni IN MATING AREA OVER
NICKEL 15-50 M.I. IN MATING AREA.
TIN 15-60 M.I. IN TAIL AREA OVER
NICKEL 30-60 M.I. IN TAIL AREA.FINISH 3- SELECT GOLD 30 M.I.MIN IN CONTACT AREA
SELECT TIN/LEAD 100 M.I.MIN IN PC TAIL AREA
BOTH OVER NICKEL 50 M.I.MIN OVERALL

EC NO: UCP2008-2498	2008/04/16
DRWN: SCHAFER	2008/04/22
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DESCRIPTION	
REV	

GENERAL TOLERANCES
(UNLESS SPECIFIED)

	mm	INCH
4 PLACES	$\pm$ ---	$\pm$ ---
3 PLACES	$\pm$ ---	$\pm$ ---
2 PLACES	± 0.25	$\pm$ ---
1 PLACE	$\pm$ ---	$\pm$ ---

ANGULAR $\pm 1/2^\circ$ DRAFT WHERE APPLICABLE
MUST REMAIN
WITHIN DIMENSIONSDIMENSION STYLE
MM ONLY

DRAWN BY	DATE
MARGULIS	2004/08/05
CHECKED BY	DATE
MARGULIS	2004/08/05
APPROVED BY	DATE
MARGULIS	2004/08/05

MATERIAL NUMBER
SEE CHARTSIZE
ASCALE
2:1DESIGN UNITS
METRICTHIRD ANGLE
PROJECTIONTITLE
POWER EDGE ASSEMBLY
SIGNAL VERTICAL
CPI

MOLEX INCORPORATED

DOCUMENT NO.
SD-45845-001SHEET NO.
2 OF 3

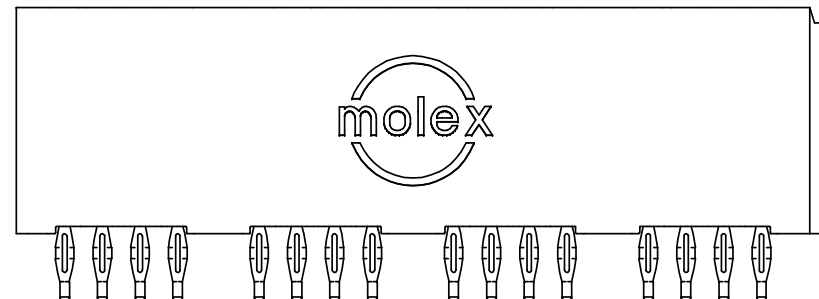
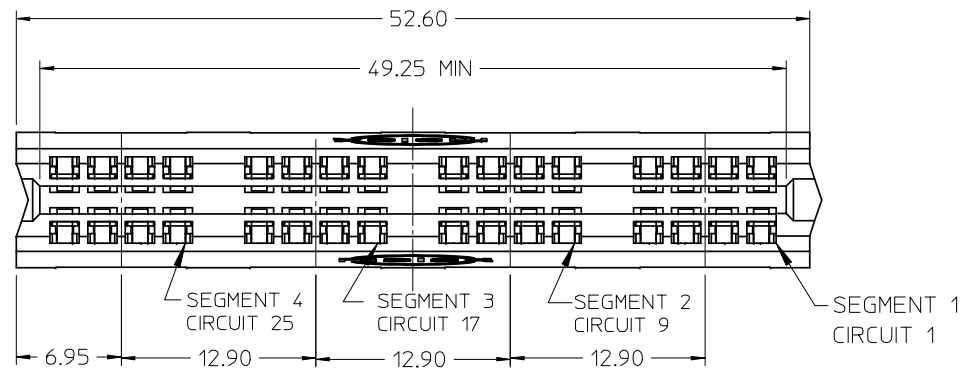
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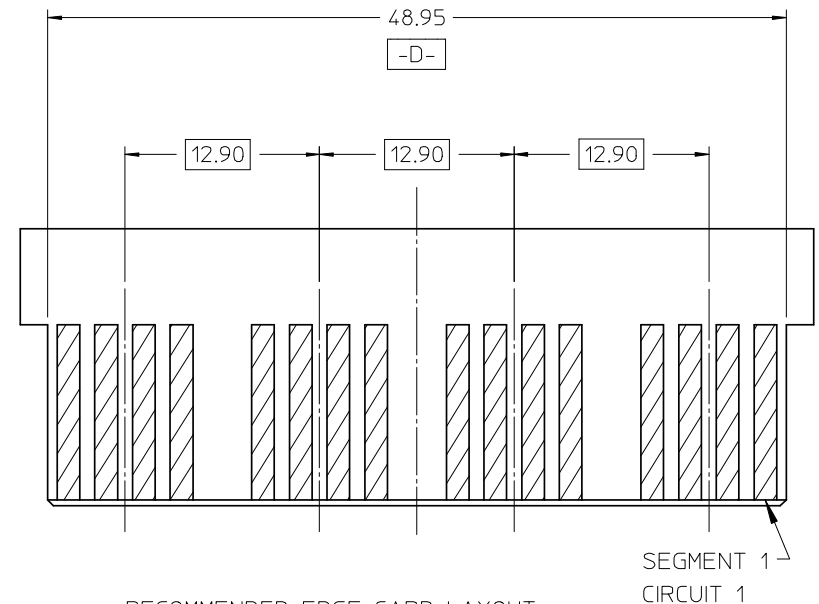
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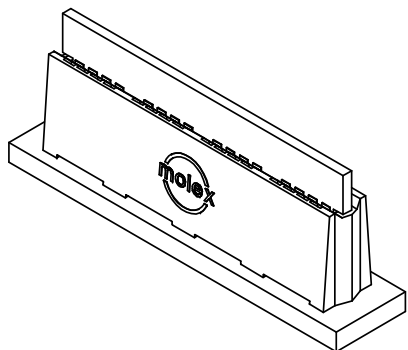
PCB LAYOUT: COMPONENT SIDE

SEGMENT 1
CIRCUIT 1

NOTE
FOR ALL OTHER DIMENSIONS AND NOTES SEE SHEET 1.



RECOMMENDED EDGE CARD LAYOUT
TOLERANCE: ± 0.05



EC NO: UCP2008-2498	2008/04/16	DESCRIPTION
DRWN: JSCHAFER	2008/04/22	
CHKD: APATEL	2008/04/25	
APPR: APATEL		
REV		

GENERAL TOLERANCES
(UNLESS SPECIFIED)

	mm	INCH
4 PLACES	$\pm$ ---	$\pm$ ---
3 PLACES	$\pm$ ---	$\pm$ ---
2 PLACES	± 0.25	$\pm$ ---
1 PLACE	$\pm$ ---	$\pm$ ---

ANGULAR $\pm 1/2^\circ$

DRAFT WHERE APPLICABLE
MUST REMAIN
WITHIN DIMENSIONS

DIMENSION STYLE
MM ONLY

DRAWN BY	DATE
MARGULIS	2004/08/05
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MARGULIS	2004/08/05

MATERIAL NUMBER
SEE CHART

SIZE
A

SCALE
2:1

DESIGN UNITS
METRIC

THIRD ANGLE
PROJECTION

TITLE
POWER EDGE ASSEMBLY
SIGNAL VERTICAL
CPI

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DOCUMENT NO.
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SHEET NO.
3 OF 3